In the fifth issue of this year, our focus is on “Smart and Autonomous Systems for Sustainability: Sustainable Computing and Computing for Sustainability.” Guest Editors Jana Doppa, Paul Bogdan, and Justinian Rosca argue that computing systems are sustainable if they maintain a certain performance reliably by consuming low power for a very long period of time. And, to achieve that, sustainable computing systems need to be adaptive.

Four special issue articles provide insights into various aspects of this computing paradigm. The guest editors provide a comprehensive survey entitled “Autonomous Design Space Exploration of Computing Systems for Sustainability: Opportunities and Challenges,” which serves as a very good base, providing a comprehensive overview of the field.

In the general interest section, we have two articles. The first is entitled “Fault-Tolerant Systolic Array Based Accelerators for Deep Neural Network Execution,” by Jeff (Jun) Zhang, Kanad Basu, and Siddharth Garg. The second article, “Design of Reversible Arithmetic Logic Unit with Built-In Testability” by Hari Mohan Gaur, Ashutosh Kumar Singh, and Umesh Ghanekar, reports a significant reduction in gate count of the reversible circuit analyzer, which is a prerequisite for built-in testability.

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