In 2013, under the direction of Prof. F. Rachidi, the IEEE Transactions on Electromagnetic Compatibility established a program to recognize the voluntary work of our reviewers, which is fundamental to maintaining the highest standards of the EMC Transactions.

In this program, a number of outstanding reviewers are recognized each year by receiving the title of Distinguished Reviewer of the IEEE Transactions on Electromagnetic Compatibility. The distinction is based on the number of submitted reviews and the reviewer score (Associate Editors were not considered).

In 2017, more than 875 experts in various fields of EMC have served as a reviewer for the Transactions. It is my great pleasure to introduce below the 2017 Distinguished Reviewers of the IEEE Transactions on Electromagnetic Compatibility. The selected reviewers have each reviewed more than 15 manuscripts in 2017 and obtained a score higher than 2.6 from the Associate Editors.

My sincere congratulations go to all of them for this recognition. I also take advantage of this opportunity to express my sincere gratitude to all our reviewers for their precious support, outstanding service and dedication.

2017 Distinguished Reviewers (in alphabetical order)

Carlo F. M. Carobbi (SM’15) was born in Pistoia (Italy) in 1968. He graduated in Electronic Engineering (with honors) and obtained the Ph.D. degree in Telematics at the University of Florence, respectively in 1994 and 2001. From 2001 he is an assistant professor at the Department of Information Engineering of the University of Florence (formerly Department of Electronics and Telecommunications), where he currently teaches Electronics Measurements. His current research interests are mainly devoted to EMC measurements, in particular to the evaluation of reproducibility of EMC test methods and EMC measurement uncertainty. Since 2011 he is chair of the Italian Electrotechnical Committee (CEI) Sub-Committee 210 / 77B (Electromagnetic Compatibility, High Frequency Phenomena) and acts as a member of the Sub-Committee 210 A (Radio-disturbances - Measurement Techniques and Limits). In 2009 he was appointed by CEI as the Italian representative in the International Task Force JTF IEC TC 77 - CISPR, responsible for drafting the IEC / TR 61000-1-6 Technical Report, “Electromagnetic Compatibility - Guide to the Assessment of Measurement Uncertainty” published in 2012. Since 2011 he is also a member of the International Electrotechnical Commission (IEC TC 77 / SC 77B) Sub-Committee 77B International Team Maintenance Team 12 (MT12), whose task is the maintenance of various international standards of immunity to pulses, such as IEC 61000-4-2 (Electrostatic Discharge), IEC 61000-4-4 (Fast Transient/Burst) and IEC 61000-4-5 (Surge). In 2015, Carlo received the IEC “1906 Award” for the contribution to standardization in EMC and, in particular, for evaluation of EMC measurement uncertainty. Since 2009, he has been a technical assessor of the Italian Accreditation Body (ACCREDIA) both for the testing laboratories department and for the calibration laboratories department. On behalf of ACCREDIA, he has been in charge of the accreditation of all major Italian Electromagnetic Compatibility Testing Laboratories. He is reviewer for several international journals (including IEEE Transactions on Electromagnetic Compatibility, IEEE Transactions on Instrumentation and Measurements, IOP Metrologia, IOP Measurement Science and Technology, Elsevier Measurement and others). He is associate editor of the IEEE Transactions on Electromagnetic Compatibility. He is senior member of the IEEE Electromagnetic Compatibility Society. Carlo received the Best Paper Award from the IEEE Electromagnetic Compatibility Society during the 2017 IEEE International Symposium on Electromagnetic Compatibility in Washington, DC for an investigation aimed at improving the reproducibility of automotive components testing.

A. Ciccomancini Scogna (M’00–SM’09) received the Ph.D. degree in Electrical Engineering from the University of L’Aquila, L’Aquila, Italy, in 2004. In 2004–2013, he was with Computer Simulation Technology of America, Framingham, MA, USA, first as Application Engineer and later as EDA Market Development Manager. He has worked with the Silicon Engineering Group (SEG) at Apple, Cupertino, CA, USA as a Signal and Power Integrity (SIPI) Scientist. Currently he is Principal Engineer with Samsung Electronics Mobile Division. His research interests include SIPI for high speed digital and mixed signal, EMC/EMI, new technologies for power consumption and noise mitigation and RFI/desense. In 2004, he received the CST publication award for the use of the Finite Integration Technique in SI applications. In 2009 and 2017, he received the DesignCon Best Paper Award in the category measurement and modeling and EMC. In 2010, he was awarded the Best Paper in the IEEE Transactions of Advanced Packaging. He received the Best Paper Presentation Awards from IMAPS in 2010 and 2011. He is currently technical chair of the SIPI co-simulation Subcommittee, past chair of TC-10 and he serves as Associate Editor of the IEEE Transactions on EMC.

Ian D. Flintoft (M’00–SM’14) received the B.Sc. and Ph.D. degrees in physics from the University of Manchester, Manchester, U.K., in 1988 and 1994, respectively. From 1988 to 1990, he was a Research Scientist with Philips Research Laboratories, Redhill, UK. From 1996 to 2017, he was a Research Fellow with the Department of Electronic Engineering, University of York, York, UK, where he was engaged in research on many aspects of applied electromagnetics including electromagnetic compatibility, computational electromagnetics and antenna design. He is currently a Senior Electrical Systems Engineer with Atkins, York, UK, where he provides expertise in electromagnetic compatibility, electromagnetic
exposure and electrical modelling for infrastructure design projects. He has published over 150 technical papers and articles on electromagnetic engineering topics. Dr. Flintoft is a member of The Institution of Engineering and Technology.

Flavia Grassi (M’07–SM’13) received the Laurea (M.Sc.) and Ph.D. degrees in electrical engineering from the Politecnico di Milano, Milan, Italy, in 2002 and 2006, respectively, where she is currently an Associate Professor with the Dept. of Electronics, Information and Bioengineering. From 2008 to 2009, she was with the European Space Agency (ESA), at ESA/ESTEC, The Netherlands, as a Research Fellow. Her research interests include distributed-parameter circuit modeling, statistical techniques, characterization of measurement setups for EMC testing (aerospace and automotive sectors), and the application of powerline communications technology in ac and dc lines. Dr. Grassi was awarded the International Union of Radio Science (URSI) Young Scientist Award in 2008, and the IEEE Young Scientist Award at the 2016 Asia-Pacific International Symposium on EMC (APEMC). She was a recipient of the IEEE EMC Society 2016 Transactions Prize Paper Award, and of the Best Symposium Paper Award at the APEMC 2015.

Stefano Piersanti (M’15) was born in Teramo, Italy, in 1981. He received the Laurea and Specialist degrees in Telecommunication Engineering from the University of L’Aquila, L’Aquila, Italy, in 2006 and 2009, respectively. In 2008, he received an M.S. degree in Nanotechnology from the University of L’Aquila. In 2010, he joined the Radiolabs Consortium (L’Aquila), where he was involved in European projects concerning millimeter waves and smart cities. In 2013, he joined the UAq Electromagnetic Compatibility (EMC) Laboratory and in 2017, he received the Ph.D. degree in Industrial and Information Engineering and Economics. He is currently a Postdoc Research Fellow at the UAq EMC Laboratory. He was the recipient of the Best Paper Award at the 2016 IEEE International Symposium on EMC.

William A. Radasky (S’67, M’73, SM’92, F’08, LF’12) was born in Johnstown, PA, USA, on July 23, 1948. He received the B.S. degree with majors in electrical engineering and engineering science from the U.S. Air Force Academy, Colorado Springs, CO, USA in 1968, the M.S. degree in electrical engineering from the University of New Mexico, Albuquerque, NM, USA in 1971, and the Ph.D. degree in electrical engineering from the University of California, Santa Barbara, Santa Barbara, CA, USA in 1981. From 1988 to 1972, he served as an officer in the U.S. Air Force working as a Research Engineer at the Air Force Weapons Laboratory in Albuquerque, NM, USA, beginning his work on EMP and other high power EM transients. From 1972-1975 he worked with Mission Research Corporation in Albuquerque, NM and Santa Barbara, CA as a member of the technical research staff. From 1975-1977 he worked as a Senior Research Engineer at Vector Research Associates in Santa Barbara, CA. He continued his research career as a Division Vice President at JAYCOR from 1977-1984 in Santa Barbara, CA. In 1984, he founded a new research company, MetaTech Corporation, Goleta, CA where he continues to serve as President and Managing Engineer. Throughout this period, he has continued his research work in high power transient phenomena and has published over 500 reports, papers and magazine articles during his career. Dr. Radasky was presented the IEC Lord Kelvin Award by the International Electrotechnical Commission (IEC) in 2004 and currently serves as the Chairman of TC 5 (High Power Electromagnetics) in the IEEE EMC Society. He has served as guest editor for two IEEE Special Issues: IEEE EMC Transactions in 2004 (HPEM and IEMI) and in 2013 (HEMP). He is a Professional Engineer in electrical engineering from the State of California. He is also a member ofEta Kappa Nu and Tau Beta Pi, and is an EMP Fellow of the Summa Foundation.

Vittorio Ricchiuti (M’99–SM’02) received the Laurea degree in electronic engineering from the University of Pisa in 1989. In the same year he joined ItalTransmission Laboratories in L’Aquila, then Siemens CNX Labs., then Technolabs S.p.A, where was responsible for the PCB design and SI/EMC activities. He gained wide experience in the field of high-speed data transmission. He joined Compel group and his main research activities included noise reduction in PCB, high speed links characterization and measurements. Since 2011, Vittorio was with CPE Italia group responsible for the R&D department. Currently he is responsible for the engineering department of Afflammation S.p.A., providing functional test systems for industrial applications.

Giordano Spadacini (M’07–SM’16) received the Laurea (M.Sc.) and Ph.D. degrees in electrical engineering from Politecnico di Milano, Milan, Italy, in 2001 and 2005, respectively. Since 2007, he has been an Assistant Professor in the Department of Electronics, Information and Bioengineering (DEIB) of Politecnico di Milano. His research interests are in the field of EMC and include distributed-parameter circuit modeling, statistical models for the characterization of interference effects, and EMC in aerospace, automotive and railway systems. He received the 2005 IEEE EMC Transactions Prize Paper Award, the 2016 Richard B. Schulz Best EMC Transactions Paper Award, and the Best Symposium Paper Award at APEMC 2015 (Taiwan).

Zhiping Yang (S’97–M’00–SM’04) received his B.S. and M.S. degrees in Electrical Engineering from Tsinghua University, Beijing, China, in 1994 and 1997, respectively. He received his Ph.D. degree in Electrical Engineering from the University of Missouri-Rolla in 2000. From 2000 to 2005, he worked for Cisco Systems, San Jose, CA, as a Technical Leader. From 2005 to 2006, he worked for Apple Computer, Cupertino, CA, as a Principal Engineer. From 2006 to 2012, he worked in Nuova Systems (which was acquired by Cisco in 2008) and Cisco Systems, San Jose, CA, as a Principal Engineer. From 2012 to 2015, he worked for Apple, Cupertino, CA, as a Senior Manager. He is currently a senior hardware manager in the Google Consumer Hardware Group. His research interests include signal integrity and power integrity methodology development for Die/Package/Board co-design, high-speed optical module, various high-speed cabling solution, high-speed DRAM/storage technology, high-speed serial signaling technology, and RF interference. He has published more than 40 research papers and holds 17 patents. His research and patents have been applied in Google Chromebook, Apple iPhone 5S/6/6S, Cisco UCS, Cisco Nexus 6K/4K/3K, and Cisco Cat6K products. Dr. Yang is a senior IEEE member and received the 2016 IEEE EMC Society Technical Achievement Award.