Payam Heydari

Payam Heydari is a full professor of electrical engineering at the University of California, Irvine. He is an IEEE Fellow for his contribution to silicon mm-wave IC and an IEEE SSCS DL (2014–2016). He is a recipient of numerous awards and recognitions including the 2007 IEEE Circuits and Systems Society Guillemin-Cauer Award, the 2005 IEEE Circuits and Systems Society Distinguished Faculty Research Award, the 2014 Distinguished Educator Award from the Orange County Engineering Council, the 2010 Faculty of the Year Award from University of California Engineering Student Council, the University of California 2009 Business Plan Competition First Place Award and Best Concept Paper Award both from the Paul Merage School of Business, the 2009 School of Engineering Best Faculty Research Award, the 2005 Henry Samueli School of Engineering Teaching Excellence Award, and the Best Paper Award at the 2000 IEEE International Conference on Computer Design.

His research on novel low-power multipurpose multiantenna RF front-ends received the Low-Power Design Contest Award at the 2008 IEEE International Symposium on Low-Power Electronics and Design (ISLPED). He presented a tutorial at the 2017 ISSCC and an invited distinguished speech at the 2014 IEEE Midwest Symposium on Circuits and Systems. He is currently serving on the TPC of the ISSCC (RF Subcommittee) and was a guest editor and associate editor for JSSC and IEEE Transactions on Circuits and Systems—I. He and his research group have published two books, one book chapter, and more than 130 journal and conference papers. His group demonstrated the world’s first complementary metal–oxide–semiconductor (CMOS) fundamental frequency transceiver operating at 210 GHz, the first terahertz closed-loop synthesizer source operating at 300 GHz in silicon, and a cavity-backed circularly polarized mm-wave fundamental frequency radiator with highest efficiency and lowest phase-noise on the record.

—Abira Sengupta

European Chapter Chairs Meeting at ESSCIRC-2017

The Region 8 IEEE Solid-State Circuits Society (SSCS) Chapter Chairs meeting was held on 13 September 2017 at the European Solid-State Circuits Conference (ESSCIRC) in Leuven, Belgium. This annual event brings together European Chapter chairs with the SSCS Administrative Committee leadership to discuss recent Chapter events and exchange ideas for future activities.

The meeting was opened by Prof. Bram Nauta, SSCS president, who welcomed the participants and highlighted the importance of the local

Vivienne Sze

Vivienne Sze is an associate professor in the Electrical Engineering and Computer Science Department at the Massachusetts Institute of Technology (MIT). Her research interests include energy-aware signal processing algorithms and low-power circuit and system design for multimedia applications such as machine learning, computer vision, and video coding. Prior to joining MIT, she was a member of technical staff in the R&D Center at Texas Instruments, where she developed algorithms and hardware for the latest video coding standard H.265/HEVC. She received the B.A.Sc degree from the University of Toronto in 2004 and the S.M. and Ph.D. degrees from MIT in 2006 and 2010, respectively. She is a recipient of several awards, including the Google Faculty Research Award, the MICRO Top Picks Award, the DARPA Young Faculty Award, and the Jin-Au Kong Outstanding Doctoral Thesis Prize.

She currently serves on the TPC of VLSI Symposium and MICRO and is a member of the SSCS Women in Circuits committee. Between 2014 and 2016, she was an elected member of various technical committees within the IEEE Signal Processing Society; she also served as a guest editor for IEEE Transactions on Circuits and Systems for Video Technology. She is an IEEE Senior Member.
Chapters to the growth of the Society. Stefan Rusu, SSCS Chapters coor-
dinator, presented an update on Chapter growth, including a sum-
mary of the subsidies and awards available to support and encourage
Chapter activities. The Distinguished Lecturer (DL) program is a key enabler
for bringing experts from industry and academia to present the latest
technology developments at the Chapter level.

The general cochair of ESSCIRC 2018, Dr. Frank Ellinger from TU Dres-
den, presented an overview of next
year’s conference and asked the Chap-
ter chairs to solicit papers and par-
ticipate in the Dresden meeting. The
conference will be held 3–6 Septem-
ber 2018 at the campus of TU Dres-
den, Germany. The paper submission
deadline is 3 April 2018.

Chapter activities from Poland,
Benelux, Sweden, UK and Ireland,
Switzerland, and Romania were
reviewed. Highlights included DL
visits, meetings with academia and
industry, local technical conferences
cosponsored by SSCS Chapters, and
short courses organized by the local
Chapters.

The next Chapter chairs meeting
will take place at the International
Solid-State Circuits Conference in
February 2018.

—Stefan Rusu
SSCS Chapter Coordinator

Women in Circuits Mentoring Luncheon at ESSCIRC

The IEEE Solid-State Circuits Soci-
ety (SSCS) Women in Circuits (WiC)
Committee hosted a networking lun-
cheon in conjunction with the Euro-
pean Solid-State Circuits Conference
(ESSCIRC) in Leuven, Belgium, on 12
September 2017. The event attracted
over 20 attendees. The conversation
flowed, connections were made, and
delicious food was consumed on an
open-air terrace.

The event began with opening
statements from Prof. Marian Ver-
helst, who organized the event. Ver-
helst talked about the benefits of
joining the SSCS and the WiC
initiative. She spoke about the vari-
ous networking opportunities at
SSCS conferences and local Chapter
events where women can meet and
interact with each other and the