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RESEARCH ARTICLE

Compositional Verification Using Geodesic Distance via Assume-Guarantee Reasoning

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ABSTRACT Assume-guarantee reasoning is indeed an effective compositional verification technique that can help mitigate the state explosion problem in model checking. However, the biggest challenge of applying assume-guarantee reasoning is how to best decompose a system. This paper presents a novel compositional verification frame based on geodesic distance. The proposed algorithm introduces novel techniques to decompose system components into groups. The algorithm's effectiveness and efficiency are evaluated through a comparative analysis with four state-of-the-art methods commonly used in the field. The results of the comparison consistently demonstrate that the proposed algorithm outperforms the state-of-the-art methods. This implies that the new algorithm exhibits superior performance in terms of decomposition quality and verification efficiency.

INDEX TERMS Model checking, compositional verification, assume-guarantee reasoning, group technology, combinatorial testing, geodesic distance, partition.

I. INTRODUCTION

Formal verification is a comprehensive, precise, and automated validation approach that employs formalized languages and mathematical tools to prove that a system's design meets specific specifications or requirements, while eliminating any potential errors or vulnerabilities in the design. Model checking is a prominent and widely used formal verification technique due to its ability to provide automated analysis when both the system model and the desired properties are available. Model checking is a technique in formal verification that automatically explores a finite-state model of a system to detect whether that model violates a given property. It systematically checks whether a given system satisfies a temporal logic formula, which describes a property that the system should possess. The process of model checking typically involves three main steps: constructing a model of the system, specifying the desired properties in a temporal logic formula, and running the model checker to verify whether the model satisfies the properties. However, as the scale of a system increases, particularly with the

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addition of concurrent components or processes, the number of possible system states can grow exponentially. *State space explosion* is a widely acknowledged issue, which is one of the challenges faced by model checking [1], [2], [3], [4], [5]. Compositional verification employs the principle of "divide and rule" to address this problem in system verification. The idea behind compositional verification is breaking down the verification process into smaller, more manageable parts and analyzing them separately before combining the results to verify the overall system.

Assume-guarantee reasoning (AGR) is a powerful compositional verification technique that involves breaking down a system into components and verifying their behavior based on assumptions and guarantees [6], [7], [8], [9], [10]. AGR provides rules for transforming the global verification of a system into local, more manageable verifications of individual components. These rules are based on the notion of refinement and enable us to verify the behavior of subsystems in isolation, given assumptions about their environment and guarantees about their behavior. Let M_1, M_2, φ be components and a property of a system, respectively. The notation $M_1 || M_2$ represents the composition of two concurrent components, M_1 and M_2 . Consider the following assume-guarantee



FIGURE 1. Overall flow.

reasoning rule:

$$\frac{M_1 \parallel A_2 \models \varphi \quad and \quad M_2 \preceq A_2}{M_1 \parallel M_2 \models \varphi}$$

 M_2 's abstraction is A_2 . A_2 represents a simplified or summarized version of M_2 . It captures the key characteristics or behaviors of M_2 while disregarding certain specific details. The notation $M_2 \leq A_2$ means A_2 can replicate the behavior of M_2 through simulation. Informally, the rule says that to show $M_1 || M_2 \models \varphi$, it suffices to find an assumption A_2 such that A_2 simulates M_2 , and M_1 composed with A_2 satisfies φ as well. However, the biggest challenge in applying AGR is how to split the system into two distinct parts, M_1 and M_2 . Each part represents a subset of the original system's parallel components, allowing for separate analysis or processing. If there is no good decomposition strategy, the efficiency of AGR may be lower than that of overall model checking [11].

The partition approach proposed in this work helps address the decomposition problem in AGR-based compositional verification. The heart of this approach comes from the idea of graph clustering. Figure 1 shows overall flow of AGR compositional verification based on geodesic distance. In this work, the proposed model consists of several individual components running concurrently. These components interact by accessing and modifying shared variables to exchange information and synchronize their actions, making up the overall behavior of the model. Firstly, a concurrent subsystem diagram (Definition 1) is constructed. Then the geodesic distance (Definition 2) between any two vertices in the diagram is calculated, and the eccentricity of each vertex (Definition 3) in the diagram is calculated [12]. Next, group the vertices according to the eccentricity. Based on the grouping results, the initial partition of compositional verification can be obtained. In this step, the key idea is to group the system diagram to ensure that the components in the same group are interrelated, and the components in different groups are more distant and less interrelated. Finally, the advantages of this method are demonstrated through experiments using AGR compositional verification based on geodesic distance.

This work's primary contributions can be summarized as shown below:

- A new compositional verification framework based on geodesic distance is proposed.
- A novel system decomposition method based on geodesic distance is proposed to solve the decomposition problem in AGR compositional verification.

The rest of this paper is described blow. Section II summarizes previous related work. Section III introduces the preliminaries used in this paper. Section IV describes our partition algorithm. Section V shows how to apply compositional verification based on geodesic distance. Section VI illustrates our approach with a complete example. Experimental results are presented in section VII. section VIII provides a comprehensive conclusion and highlights avenues for future research.

II. RELATED WORK

Jamieson et al. [13] introduce a framework that leverages the L^* algorithm [14] and the noncircular assume-guarantee (AG-NC) rule to automate the generation of assumptions for AGR. L^* algorithm is utilized to learn a language which is not yet identified or categorized as a regular language. L^* algorithm can generate a Deterministic Finite-State Automaton (DFA) that can recognize and accept this language. References [15], [16], and [17] improve L^* algorithm by reducing the size of learning alphabet.

The framework proposed by Shang-Wei et al. [18] applies a systematic approach to analyze the system model and identify the root causes of the counterexamples. It then performs appropriate modifications or refinements to eliminate these problematic behaviors. Whenever a model checker gives a counterexample, a class of counterexamples that exhibit similar problematic behavior will be eliminated. The process of eliminating counterexamples in the proposed framework follows a compositional approach.

By formulating the problem as a hypergraph partitioning problem and implementing a proof rule that exhibits circularity and symmetry, Wonhong et al. [19] simplify the counterexample elimination process by automatically grouping components and minimizing the number of premises and assumptions required. The hypergraph partitioning algorithm used by them decomposes the set of variables in the system model into n disjoint subsets, enabling more efficient processing and analysis of the counterexample elimination problem. Each of these variable partitions X_i resulting from the hypergraph partitioning algorithm corresponds to a specific component M_i within the system.

By providing a systematic approach to identifying and addressing blocking behavior in a compositional verification framework, the algorithms proposed by Robi et al. [20] contribute to improving the reliability and correctness of discrete event systems. They introduce the counterexample calculation process of two kinds of common abstract rules. David et al. [21] present *CSim*², a framework for top-down validation using a compositional rely-guarantee-based approach. The Isabelle/HOL theoremproving machine is used. They create a new concurrent imperative language called CSimpl. To maintain soundness and assurance properties across different CSimpl standards or abstract levels, a simulation-based framework is introduced.

Yang et al. [22] contribute a random and meristic assume guarantee compositional verification framework for probabilistic automata by utilizing the NL^* algorithm. The framework can rapidly identify and conclude when the system fails to satisfy the expectant properties. They use PRISM tool to implement the framework.

By combining deep learning, Ruiyang et al. [23] aim to address the limitations of traditional model checking approaches and enhance their performance. Using first-order recursive logic, the model checking issue is transformed into a game for loss and win with complete information. By improving the neural MCTS algorithm to handle loops, Xu et al. address a significant challenge in searching the state space. Their enhancements enable more robust and efficient exploration, improving the overall performance and effectiveness of the algorithm in complex environments. Experiments were conducted based on two labeled transition systems, numerical game and Dining Philosophers.

Mbarka et al. [24] use the CSP language and PAT model checker to verify properties of Hadoop schedulers. The analysis of the OpenCloud scheduler serves as a practical case study to demonstrate the real-world applicability of their work. They verify schedulability, resources-deadlock freeness and fairness of Hadoop by integrating model checking techniques and simulation.

Asma et al. [25] use the SPIN model checker and Linear Temporal Logic to verify the peer to peer system called WebRTC. They utilize an intermediate format called IF as an intermediate language for generating a Promela model. Promela is a modeling language. By expressing desired properties as LTL properties, the verification and validation process provides a formal and systematic approach to ensure that the model behaves as intended and complies with specified requirements.

Kenji et al. [26] utilize two different strategies to verify the reflective system. To elaborate, a metalevel labeled transition system (MLTS) is a formalism used for modeling a reflective system. SPIN is used as model checker to verify a system. They demonstrate that symmetry reduction and divergence-sensitive stutter bisimulation techniques can be used effectively in the context of MLTS models. For the reconnaissance robot system, MLTS is used to model its selfadaptation capabilities. In the case of the internet-of-things system, MLTS is employed to model its dynamic evolution.

AlSobeh [27] integrates techniques such as statistical model checking, AOP modularity, and formal methods, and employes a propositional model to observe the properties and constraints of Electronic Human Resource (EHR) systems,

providing a rigorous approach for verifying the reliability of EHR systems.

AlSobeh et al. [28] utilize Aspect-Oriented Programming (AOP) to formally verify the dynamic behaviors of blockchain systems. Through a case study of a cryptocurrency system, they outline the workflow that ranges from analyzing contextual data, to conducting statistical model checking using aspects, and finally to evaluating service quality.

The most relevant method is that of Lin [1]. This method solves the partition problem in AGR by dissatisfied cores of bounded model checking (BMC) formulas. The components associated with the unsatisfied core are grouped into M_1 . They refine the abstraction of M_2 based on interpolants. This method is effective, but it does not work well for some systems. Here, geodesic distance is used to solve the partition problem.

III. PRELIMINARIES

This section presents concise explanations of important terms.

Definition 1 (Concurrent Subsystem Diagram): The concurrent subsystem diagram is denoted by G(V,E). V is the set of vertices and E is the set of edges. If two components share variables, there is an undirected edge between the vertices of the two components.

Definition 2 (Geodesic Distance): For vertex $v_1 \in V$, $v_2 \in V$, the geodesic distance of any two vertices is the number of edges in the shortest path between two vertices, denoted as geodis(v_1 , v_2). The geodesic distance between two non-connected vertices in the diagram is defined as infinity.

Definition 3 (Eccentricity): For vertex $v \in V$, the eccentricity of v is the maximum geodesic distance between v and the other vertices $u \in V$ -{v}, denoted as eccen(v). The eccentricity of v captures how far v is from the farthest vertex in the diagram.

Definition 4 (Diameter of Diagram): The diameter of a diagram is the maximum eccentricity for all vertices of the diagram. The diameter represents the maximum distance between all vertex pairs in the diagram.

Definition 5 (Peripheral Vertices): Peripheral vertices are vertices on the diameter.

IV. GROUP THE VERTICES TO GET INITIAL PARTITION

Our grouping objectives are as follows:

- The vertices in each group are connected.
- The similarity (Definition 7) of each pair of vertices within each group is as high as possible.
- The eccentricity of the vertices in each group has at most two values (This is to ensure that each group does not contain too many vertices).
- At most one group has only one vertex.

The following illustrates how to group the vertices according to the eccentricity and get the initial partition.

To group vertices, follow these steps: (1) Get the set of peripheral vertices (Definition 5). Neighbors of peripheral vertices are grouped with peripheral vertices. If multiple peripheral vertices have the same immediate neighbors, they are merged into a group. (2) Get the ungrouped set of vertices. If the ungrouped set is empty, the algorithm ends. (3) Find the vertex set with the greatest eccentricity (denoted as MAXECC) among the ungrouped vertex sets. For vertex i (eccen(i) = MAXECC), j is ungrouped vertex, if geodis(i, j) = 1 (that is to say, j is the immediate neighbor of *i*), combine *j* and *i* into a group. For vertex *i* (eccen(*i*) = MAXECC), vertex m (eccen(m) = MAXECC), if i and mhave the same immediate neighbor, combine i and m into a group. Repeat the process until all vertices are grouped. Algorithm 1 offers a clear and concise representation of the proposed grouping algorithm's logic and operations. The time complexity of Algorithm 1 is $O(n^4)$.

Next, initial partition M_1, M_2 can be obtained according to the group number of each component. First, put component C_i that is related to the verified property into M_1 . If component C_j and C_i have the same group number, i.e., group(j)= group(i), then put component C_j into M_1 . If Component C_k is the neighbor of C_i (i.e. geodis(C_k, C_i)=1) and C_k is not a member of M_1 , then put component C_k into M_1 as well. Components that do not belong to M_1 are placed in M_2 . Algorithm 2 shows the procedure for obtaining the initial partition. The time complexity of Algorithm 2 is O(n).

Take a simple system as an example. Figure 2(a) is the system diagram and Figure 2(b) is the result of its grouping. The details of obtaining the initial partition M_1, M_2 is as follows:

- 1) For a given diagram G(V,E), $i \in V$, $j \in V$, calculate the geodesic distance geodis(i, j) between vertex i and j.
- 2) Calculate the eccentricity of each vertex. eccen(a) = 3, eccen(b) = 2, eccen(c) = 4, eccen(d) = 4, eccen(e) = 3, eccen(f) = 4, eccen(g) = 3, eccen(h) = 3, eccen(i) = 4, eccen(j) = 3. Vertices c, d, f and i have the greatest eccentricity, so they are peripheral vertices.
- 3) Get one vertex out from vertices with the greatest eccentricity: vertex c. Set group(c) = 1. Because a and d are immediate neighbors of c (for geodis(c, a) = 1, geodis(c, d) = 1), set group(a) = 1 and group(d) = 1.
- 4) Get one vertex with the greatest eccentricity from ungrouped vertices: vertex f. Set group(f) = 2. Because e and g are immediate neighbors of f (for geodis(f, e) = 1, geodis(f, g) = 1), set group(e) = 2 and group(g) = 2.
- 5) Get one vertex with the greatest eccentricity from ungrouped vertices: vertex *i*. Set group(*i*) = 3. Because *h* and *j* are immediate neighbors of *i* (for geodis(*i*, *h*) = 1, geodis(*i*, *j*) = 1), set group(*h*) = 3 and group(*j*) = 3.
- 6) Vertex *b* is the unique ungrouped vertex, so group(b) = 4.

So far, each vertex has a group number. Group 1 has the component a, c and d. Group 2 has the component e,

f and g. Group 3 has the component h, i and j. Group 4 has the component b.

- 7) If the verified property has relation with component *a*, put components *a*, *c* and *d* into M₁ (Because component *a* is in a group that contains *c* and *d*). M₁ = {a, c, d}. Component *b* is the immediate neighbor of *a* (for geodis(*a*, *b*) = 1), so put *b* into M₁ as well. M₁ = {*a*, *b*, *c*, *d*}.
- 8) In the end, we get the initial partition: $M_1 = \{a, b, c, d\}, M_2 = \{e, f, g, h, i, j\}$

The benefits of such grouping are explained below through the similarity between vertices.

Definition 6 (Neighbourhood of Vertex): Given undirected graph G=(V,E), for vertex $u \in V$, the neighbourhood of u is defined as:

$$\Gamma(u) = \{v \mid (u, v) \in E\} \cup \{u\}$$

Definition 7 (Similarity between Two Vertices): Use normalized common neighborhood sizes to measure the similarity between two vertices $u, v \in V$:

$$\sigma(u, v) = \frac{|\Gamma(u) \cap \Gamma(v)|}{\sqrt{|\Gamma(u)| \times |\Gamma(v)|}}$$

The greater $\sigma(u, v)$, the greater similarity between vertices u and v.

The neighborhood of each vertex in Figure 2 is as follows: $\Gamma(a) = \{abcd\}, \Gamma(b) = \{abegh\}, \Gamma(c) = \{acd\}, \Gamma(d) = \{acd\}, \Gamma(e) = \{bef\}, \Gamma(f) = \{efg\}, \Gamma(g) = \{bgf\}, \Gamma(h) = \{bhi\}, \Gamma(i) = \{hij\}, \Gamma(j) = \{bij\}.$

$$\sigma(a, c) = \frac{\sqrt{3}}{2}, \sigma(a, d) = \frac{\sqrt{3}}{2}, \sigma(a, b) = \frac{1}{\sqrt{5}}.$$

Two vertices with the same eccentricity have the same similarity to their common neighbor if their neighborhood is the same, as shown in Figure 2, vertex c and d.

In Figure 2, vertices *a*, *c*, and *d* are grouped together while vertex *b* is not grouped with *a*. This is because $\sigma(a, c) = \sigma(a, d)$ and $\sigma(a, c) > \sigma(a, b)$.

For each group obtained by algorithm 1, the similarity of the vertices within the group is high, that is to say, the components within the group are the most relevant.

V. COMPOSITIONAL VERIFICATION BASED ON GEODESIC DISTANCE

In this section, we introduce the application of AGR compositional verification using geodesic distance. The verification process, shown in Figure 3, are as follows:

- 1) *K*, the geodesic distance of the vertex, the initial value is 2.
- A₂, an over-approximation of M₂, the initial value is TRUE. Then check whether the condition M₁ || A₂ ⊨ φ holds. If it holds, then the conclusion M ⊨ φ can be got. If it does not hold, assume it reaches ¬φ in *m* steps.
- 3) Use the *m*-step bounded model checking formula. If the formula is satisfiable, then the conclusion $M \models \varphi$ does not hold. If the formula is unsatisfiable, go to step 4.
- 4) Analyze whether $k \leq s-1$ (s is the number of subsystems). If k > s-1, the conclusion $M \models \varphi$ holds.

Algorithm 1 GROUPING





FIGURE 2. A Simple System. The yellow vertices c, d, f, and i in the figure are peripheral vertices.

Algorithm 2 PARTITION

```
Input: C_1, C_2, ..., C_n; // a set of components

Output: M_1, M_2

M_1 \leftarrow \emptyset;

M_2 \leftarrow \emptyset;

Suppose Component C_i is related to the property \varphi;

for j = 1; j <= n; j + + \mathbf{do}

if group(j)=group(i) \lor geodis(C_j, C_i)=1 then

\mid M_1 \leftarrow M_1 \cup \{C_j\};

end

// \text{ group (j)}=\text{group (i) denotes } C_j \text{ and}

C_i \text{ belong to the same group};

geodis (C_j, C_i)=1 denotes C_j is a

direct neighbor of C_i;

end

M_2 \leftarrow \text{AllComponents} - M_1;
```

If $k \leq s-1$, find the neighbors (k-NN) with geodesic distance k of the subsystems related to the property.

If *k*-NN is empty, the conclusion $M \models \varphi$ holds. If *k*-NN is not empty, change M_1 to the union of M_1 and *k*-NN. k = k + 1.

- 5) If M_1 unchange, go to step 4.
- 6) If M_1 change, go to step 2.

VI. A COMPLETE EXAMPLE

The following illustrates how the proposed approach works using a complete example. First, use the approach to get the initial partition of the system. Then, verify the system using compositional verification based on geodesic distance. In [1], the authors employ a technique called interpolation to estimate the behavior of a system as it transitions from one state to another.

We develop a model of a six-bit counter that is composed of six individual components, as shown in Figure 4. Each component *unit_i* for $i \in \{1,2,3,4,5,6\}$ has three variables which are all of Boolean type: *bit_i*, *in_i*, *out_i*. *bit_i* represents the current binary value of *unit_i*. *in_i* represents whether to assert the carry value of *unit_i*. *out_i* is a signal that determines



FIGURE 3. Verification Process. M $\models \varphi$ denotes the system M satisfies the property φ . M $\nvDash \varphi$ denotes the system M does not satisfy the property φ .

whether the current bit value of *unit_i* should be propagated or carried over to the next stage. I_i is the initial condition and T_i denotes the relation that describes how each of the two components transition from one state to another in the system. Their encoding are as described below.

- I_1 : \neg bit₁ \wedge in₁
- $I_i: \neg bit_i \quad //i=2, \cdots, 6$ $T_1: (bit_1' \leftarrow bit_1 \oplus in_1) \land (out_1' \leftarrow bit_1 \land in_1) //\oplus is$ XOR
- $T_i: (in_i' \leftarrow out_{i-1}) \land (bit_i' \leftarrow bit_i \oplus in_i) \land (out_i' \leftarrow bit_i)$ \wedge in_i) // i=2,...,6

Suppose to verify the property $\varphi = \neg (out_2 \land bit_2 \land in_2)$. First, construct the system diagram. For $i \in \{1, 2, ..., 6\}$, unit_i denotes vertex C_i of the diagram. Out₁ variable which belongs to $unit_1$ appears in the $unit_2$, so there is an edge between vertex C_1 and vertex C_2 . By analogy, the system diagram is shown in Figure 5(a).

Next, group the system. The eccentricity of each vertex is shown in Table 1. The geodesic distance of two vertices is shown in Table 2. Find the first vertex with the greatest eccentricity C_1 , and set the grouping number of C_1 to 1, that is, $\operatorname{group}(C_1) = 1$. For geodis $(C_1, C_2) = 1$, set $\operatorname{group}(C_2) =$ 1. Find vertex C_6 with the highest eccentricity among the ungrouped vertices, and set $group(C_6) = 2$. For geodis(C_5 , C_6) = 1, set group(C_5) = 2. Find vertex C_3 with the highest eccentricity among the ungrouped vertices, and set group(C_3)= 3. For geodis (C_3 , C_4) = 1, set group(C_4) = 3.

So far, the system has been divided into 3 groups, as shown in Figure 5(b). Group 1 has component C_1 and C_2 . Group 2 has component C_5 and C_6 . Group 3 has component C_3 and C_4 .

TABLE 1. Eccentricity of vertices.

Vertex	Eccentricity
C_1	5
C_2	4
C_3	3
C_4	3
C_5	4
\overline{C}_6	5

TABLE 2. Geodesic distance between vertices.

	C_1	C_2	C_3	C_4	C_5	C_6
C_1	0	1	2	3	4	5
C_2	1	0	1	2	3	4
C_3	2	1	0	1	2	3
C_4	3	2	1	0	1	2
C_5	4	3	2	1	0	1
C_6	5	4	3	2	1	0

Then obtain the initial partition of the system. Since the property φ has relation with C_2 and C_2 belongs to group 1, put C_1 and C_2 which are included in group 1 into M_1 . For geodis(C_2, C_3)=1, put C_3 into M_1 too. Therefore, the initial partition of the system is obtained: $M_1 = \{C_1, C_2, C_3\}, M_2 =$ $\{C_4, C_5, C_6\}.$

Finally, use AGR compositional verification to verify the system. Construct an abstraction A_1 for M_1 and an abstraction A_2 for M_2 .

In the initial state, make A_2 's transition relation true, and set A_1 to be M_1 . TRUE is known as the weakest overapproximation, which means that it assumes all possible transitions are valid or true. A_1 contains variables bit_1 , in_1 , out₁, bit₂, in₂, out₂, bit₃, in₃, out₃. Variables contained in A₂ denoted by X_2 . X_2 can be any values. For $i \in \{0, 1, \dots, 8\}$,

```
MODULE unit<sub>1</sub>
 var bit<sub>1</sub>:bool;
 var in<sub>1</sub>:bool;
 var out<sub>1</sub>:bool;
 init(bit<sub>1</sub>):=FALSE;
 init(in<sub>1</sub>):=TRUE;
 next(bit<sub>1</sub>):=bit<sub>1</sub> xor in<sub>1</sub>;
 next(out<sub>1</sub>):=bit<sub>1</sub> & in<sub>1</sub>;
END MODULE
```





FIGURE 5. 6-bit counter.

State_i is the state of $A_1 \parallel A_2$, as shown in Figure 6. For convenience, the value of 0 is FALSE, and the value of 1 is TRUE. State₀ is the initial state. $A_1 \parallel A_2$ is always switching between state₁, state₂, ..., and state₈. The value of bit₃, bit₂ and bit₁ is from 000, 001, 010, 011, 100, 101, 110 to 111. In these states of the system, out₂, bit₂ and in₂ are never TRUE at one time. After the first iteration, it can be concluded that $A_1 \parallel A_2 \models \varphi$. Because A_1, A_2 are abstractions of M_1, M_2 respectively, the conclusion can be drawn that $M_1 \parallel M_2 \models \varphi$.

VII. EXPERIMENTS

Use following systems as benchmarks to to highlight the advantages and strengths of our approach over existing methods.

- Flexible Manufacturing Systems (FMS). A FMS [29] is responsible for manufacturing blocks with cylindrical painted pins by transforming raw blocks and raw pegs through a series of manufacturing operations. Buffers are used to connect devices, and each buffer has a capacity of one part. Buffer overflow and underflow issues can occur within a FMS. Verify that each buffer within a system should not overflow.
- Dining Philosophers (DP). The DP [30] problem serves as an example of a resource sharing challenge in concurrent programming. In this problem, a group of philosophers are seated in a manner where they form a circle around a table, with each philosopher having a fork to their left and right. Each philosopher requires two

```
MODULE unit<sub>i</sub>
    var bit<sub>i</sub>:bool;
    var in<sub>i</sub>:bool;
    var out<sub>i</sub>:bool;
    //i=2, 3, 4, 5, 6
    init(bit<sub>i</sub>):=FALSE;
    next(in<sub>i</sub>):=out<sub>i-1</sub>;
    next(bit<sub>i</sub>):=bit<sub>i</sub> xor in<sub>i</sub>;
    next(out<sub>i</sub>):=bit<sub>i</sub> & in<sub>i</sub>;
END MODULE
```

forks to eat their meal. Verify the property that it is not possible for any two neighboring philosophers to eat at one time.

- AIP Manufacturing System (AIP). AIP [31] manufacturing system is a production system that involves the use of two distinct types of materials to produce two different products. It consists of several key components, including the I/O station, transport units, assembly stations, external loops, and a central loop. The AIP system has the problem of disordered manufacturing. Verify that the paths or directions of the two distinct types of materials should be reversed.
- Synchronous Bus Arbiters (SBA). The SBA [30], [32] refers to a specific protocol used in synchronous digital circuits for bus arbitration.. A bus consists of nodes connected in a loop. A special token is sequentially passed from one node to another in a predetermined order. The token circulates among the nodes in the network, and only the node possessing the token is permitted to use the bus for transmitting data. Verify the properties that only one node has the permission to transmit data on the bus, that is, two nodes cannot control the bus at one time.
- MSI Cache Coherence Protocol (MSI). In the MSI [30], [32] cache coherence protocol, n nodes share a memory. Each individual node is equipped with a cache. The memory and the caches of the nodes are connected by a bus. Verify the property that only one node has the permission to transmit data on the bus, that is, two nodes cannot control the bus at one time.

The PAT model checker [33] is used to obtain the provided experimental results. The experimental environment is a 64bit win7 laptop with 4GB Random Access Memory and i5-3230M processor. The language used for describing system models in our system is a simplified version of the input language used by NuSMV. In all of our experimental scenarios, we have observed that every tested property holds true and is satisfied. Compare four verification methods: McMillan's interpolation-based compositional verification (denoted as Mc-ITP), Lin's compositional verification (denoted as C-ITP), C-ITP method plus abstracting M_1 (denoted as C-ITP_A),



	M ₁									<i>M</i> ₂
	C ₃			C2			c1			C ₄ C ₅ C ₆
	bit₃	in ₃	out ₃	bit ₂	in ₂	out ₂	bit ₁	in ₁	out ₁	X ₂
State ₀	0			0			0	1		ANY
State ₁	0	0	0	0	0	0	1	1	0	ANY
State ₂	0	0	0	1	1	0	0	1	1	ANY
State₃	0	0	0	1	0	0	1	1	0	ANY
State ₄	1	1	0	0	1	1	0	1	1	ANY
State ₅	1	0	0	0	0	0	1	1	0	ANY
State ₆	1	0	0	1	1	0	0	1	1	ANY
State ₇	1	0	0	1	0	0	1	1	0	ANY
State ₈	0	1	1	0	1	1	0	1	1	ANY
State ₁	0	0	0	0	0	0	1	1	0	ANY

FIGURE 6. The state of 6-bit counter system.

TABLE 3. Verification results.

			1	2	2	4	<i>c</i>
system	n	$ \varphi $	Mc-ITP	C-ITP	C-ITP ₄	4 C-ITP _{P+4}	5 Geo-AGR
FSM 02	8	6	2.17	1.25	2	0.38	1.71
FSM 04	16	12	7.49	12.06	7.35	0.64	2.867
FSM 06	24	18	20.72	38.41	19.44	1.19	4.346
FSM 08	32	24	344	76.77	46.18	1.87	5.828
FSM_10	40	30	83.32	265.45	272.97	2.82	7.292
FSM_12	48	36	*	*	*	4.06	8.756
FSM_14	56	42	191.9	257.6	191.3	5.44	10.122
FSM_16	64	48	297.2	359.1	283.2	7.18	11.571
FSM_18	72	54	367.1	513.8	431.4	9.25	13.02
FSM_20	80	60	505.0	716.3	577.6	11.62	14.481
FSM_24	96	72	-	1018.7	884.7	17.85	17.39
FSM_30	120	90	-	-	-	30.86	21.751
DP_04	8	4	288.23	9.24	2.82	1.01	0.74
DP_06	12	6	11.62	*	4.59	2.42	1.168
DP_08	16	8	16.74	*	7.94	3.95	1.574
DP_10	20	10	24.06	*	14.04	6.05	6.692
DP_20	40	20	64.81	*	52.71	27.64	15.454
DP_30	60	30	280.36	*	306.84	37.83	29.012
AIP_01	8	2	2.34	11.61	4.8	5.35	3.267
AIP_02	16	4	7.52	172.61	14.76	42.84	7.345
AIP_04	32	8	34.20	*	94.04	86.66	19.179
AIP_06	48	12	89.78	*	309.97	132.46	24.709
AIP_08	64	16	196.5	*	651.27	185.43	31.038
AIP_10	80	20	350.3	*	1069.1	244.6	38.824
AIP_11	88	22	450.7	*	1546.2	273.71	42.707
AIP_12	96	24	613.1	*	-	315.9	46.626
SBA_02	8	12	*	137.33	12.82	3.9	4.646
SBA_03	12	18	*	593.58	211.1	7.07	7.35
SBA_04	16	24	*	1067.1	422.79	11.61	10.2
SBA_05	20	30	*	1600.9	649.6	17.2	13.313
SBA_06	24	36	*	-	898.3	24.23	16.69
SBA_07	28	42	*	-	1035.7	33.12	20.238
SBA_08	32	48	*	-	1287.2	44.3	24.153
SBA_09	36	54	*	-	1533.5	55.66	28.067
SBA_10	40	60	*	-	-	70.06	32.181
MSI_02	8	1	0.42	2.87	0.7	1.51	0.345
MSI_03	11	3	3.2	-	-	4.5	1.791
MSI_04	14	6	-	-	-	13.62	4.646
MSI_05	17	10	*	*	*	*	8.529
n: number of components; φ : number of verified properties;							

*: out of memory; -: time out(30 minutes) verification time: in seconds

and C-ITP method plus decomposition strategy and the abstraction of M_1 (denoted as C-ITP_{P+A}). Our method

is denoted as Geo-AGR. Table 3 shows the results of experiments.

Mc-ITP approach gets initial partition randomly. In C-ITP and

C-ITP_A, M_1 contains the first four components and M_2 contains the remaining components. Users can specify the input order. C-ITP_{P+A} approach uses a partition method that is derived from unsatisfiability core of the BMC theorem. The initial decomposition is obtained by running this decomposition method in two steps.

Using our geo-distance based compositional verification method, the verification time is greatly reduced. For system FSM_12, no matter which method Mc-ITP, C-ITP, or C-ITPA is adopted, it is always out of memory, but the result can be obtained within 10 seconds after using our method. For system FSM_30, all the first three methods are time out (over 30 minutes), but the result can be obtained within 25 seconds after using our method. For system DP_06, D_08, DP_10, DP_20, DP_30, AIP_04, AIP_06, AIP_08, AIP_10, AIP_11, AIP_12, SBA_06, SBA_07, SBA_08, SBA_09, and SBA_10, using method C-ITP, all of them are out of memory or time out, but the results can be obtained quickly after using our method.

C-ITP_{*P*+*A*} method uses a partition heuristic, so the validation efficiency of this method is much higher than that of Mc-ITP, C-ITP, and C-ITP_{*A*}. However, our algorithm is much more efficient than C-ITP_{*P*+*A*}, except for FSM_02, FSM_04, ...FSM_20 (See the last 2 columns in Table 3).

Assume-guarantee reasoning compositional verification needs to decompose the system into M_1 and M_2 . The efficiency of verification has a great correlation with partition. The proposed partition algorithm can ensure that M_1 contains fewer components which are related to verification properties. This is the main reason why the proposed algorithm is superior to C-ITP_{P+A}.

VIII. CONCLUSION AND FUTURE WORK

This paper introduces a new decomposition algorithm for assume-guarantee reasoning. The decomposition algorithm can greatly reduce the number of components that M_1 contains in AGR. A compositional verification framework based on geodesic distance is proposed. This framework offers a solution to mitigate the issue of state space explosion that often arises during compositional verification. In five parameterized test cases, the method takes the shortest time compared with McMillan's approach and Lin's interpolationguided compositional verification. The grouping algorithm presented in this paper does not yield a unique result when applied to a system which graph is cyclic. For systems with a very large number of components, calculating the geodesic distance for every pair of components can become computationally expensive, especially for dense graphs. Future work will aim to address the limitations of the current grouping algorithm by exploring modifications to handle cyclic graphs, implementing efficient approximations for geodesic distance calculations, and investigating alternative grouping algorithms that are more suitable for large, dense systems.

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