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An Equivalent-Circuit Topology for Lossy Non-Symmetric Reciprocal Two-Ports

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ABSTRACT An equivalent-circuit topology for two-port lossy non-symmetric reciprocal networks is proposed. The circuit topology is based on the eigenstate decomposition. The proposed circuit consists of two immittances and two transformers with a single complex turns ratio, all obtained from the eigenvalues and eigenvectors of the admittance or impedance matrix of the network in a straightforward way. The interconnection of the circuit elements is simple and compact, and the real parts of its immittances are always positive. To verify its behavior, the equivalent circuit of two different leaky-wave antenna unit cells is extracted: a series-fed coupled patch radiating element, and the complementary strip-slot. Simulated and measured data are provided for the study cases, highlighting the satisfactory behavior of the equivalent circuit over a very broad bandwidth. A simple asymmetry model is also proposed that can be used advantageously in the analysis and design of non-symmetric two-ports, like unit cells of leaky-wave antennas.

INDEX TERMS Asymmetry, eigenstates, equivalent circuits, lattice network, leaky-wave antenna, open stopband.

I. INTRODUCTION

There is a growing interest in the modeling of lossy nonsymmetric reciprocal two-port electromagnetic structures by means of equivalent circuits. This interest has been fostered, to a great extent, by periodic Leaky-Wave Antennas (LWAs), since it has been shown that the introduction of certain asymmetries in their unit cell can mitigate the so-called openstopband effect at broadside [1]–[4]. The performance of the LWA is indeed governed by the asymmetries of the unit cell and their control can lead to improved LWA design strategies and behavior. Having a proper equivalent circuit can, in general, help provide some physical insight into the involved electromagnetic phenomena [5], and can, in the particular case of LWAs, be crucial for extracting the propagation constant that determines the performance of such antennas [6].

From a purely mathematical point of view, three complex parameters are necessary for the complete specification of a dissipative reciprocal two-port structure, so the equivalent circuit must contain at least three lossy circuit elements or their equivalents [7]. Examples of general equivalent circuits like these are the Π or T networks found in the classic literature. A given equivalent circuit is characterized by its constituent elements or components (mainly immittances, transformers, and transmission line sections or reference plane shifts) and its topology (the way the elements are interconnected). In the case of non-dissipative two-ports there is a wide variety of available equivalent circuits (see, for instance, [7], [8]). However, there is not such a variety of choices for the case of dissipative reciprocal two-ports.

One of the problems with dissipative reciprocal two-ports is that, depending on the topology of the equivalent circuit, immitances with negative real parts may appear, which is not a desirable characteristic, although it does not compromise energy conservation in the two-port as a whole. This modeling problem (the appearance of negative real parts in the immittances of the equivalent circuits) was already addressed in the early days of microwave technology. For instance, Felsen and Oliner [9] proposed to separate the dissipative equivalent circuit into lossy and lossless portions. In doing so, they obtained what they called *canonical* networks or equivalent circuits composed by two transmission line sections (or two reference plane shifts), one reactance or susceptance, one ideal transformer with real turns ratio, and two non-negative resistors (a total of six elements, although only two of them are lossy). The idea of separating the dissipative two-port into lossy and lossless portions has reached our days and, very recently, Zappelli [10] has proposed equivalents circuits for dissipative reciprocal two-port devices based on this separation approach. These equivalent circuits contain non-negative resistors and, in its simpler form (*compact configuration*), has seven (real) elements and an intricate extraction procedure.

In the case of dissipative symmetric (certainly reciprocal) two-ports, the lattice network can be considered as the optimum equivalent circuit topology, since it guarantees the realizability of the two immittances required to model the response of any realizable symmetric network. Moreover, the lattice network provides a profound physical insight into the circuit behavior, since any response can be considered as a linear combination of the two *eigenstates* (even- and oddmode excitations) of the structure. Because of this, in recent years, the spotlight has been put on the quest for lossy non-symmetric equivalent circuits that mimic, to some extent, the properties of the lattice network. Unfortunately, equivalent circuits based on the lattice-network topology require the structure to be symmetric, and the derivation of an extension of this topology for non-symmetric circuits has proved not to be straightforward.

In an attempt to elaborate on some design methodology to choose the degree of asymmetry needed to cancel the broadside effect in LWAs, an equivalent circuit based on the symmetric lattice network was proposed in [4]. Although this equivalent circuit was useful for the purpose of [4], this circuit lacked the main aforementioned properties of the lattice networks which might limit its application to other problems. An alternative to this circuit is the equivalent circuit topology proposed in [11]. The latter follows an eigenstate formulation approach [12] and, unlike the circuit in [4], it preserves the property of symmetric lattice networks of being decomposable in eigenstates. This property of decomposition has been satisfactorily exploited very recently in [13] to propose the design flow of a modified microstrip Franklin unit cell for its use in LWAs.

Although the benefits of the equivalent circuit topology proposed in [11] have been already proved in specific LWA designs [13], this model has revealed to have two main

problems. The first one is that the real parts of their immittances can be negative unless power orthogonality between the eigenstates is enforced by adding a frequency-dependent shift in the reference planes. This is an undesirable behavior for an equivalent circuit of a passive network. The second problem is that, when the two ports are isolated $(y_{12}$ or z_{12} is zero or close to zero), the equivalent circuit is not able to properly degenerate into this limit case.

A novel equivalent circuit topology for non-symmetric structures is proposed here, which overcomes the main drawbacks of the equivalent circuits proposed in [4], [10], [11]. The proposed circuit topology consists of two immittances and two transformers with a single complex turns ratio. This results in three complex parameters, the minimum number of parameters required unlike [10]. Even more, the extraction procedure is absolutely straightforward, with simple explicit equations for all the circuit elements. The circuit is based on the eigenstate decomposition proposed in [11], thus, it is able to properly separate the even-like and odd-like modes in a very broad bandwidth, providing more physical insight than [4]. Apparently, the difference of the proposed topology with respect to [11] can seem not very significant; however, these modifications have a dramatic effect on the immittances: their values are the eigenvalues of the immittance matrix of the structure. As an important consequence of this, the real parts of these two immittances are always positive (regardless of the choice of reference planes). Additionally, the circuit is able to gracefully degenerate into the limit case defined by $y_{12} = 0$ or $z_{12} = 0$, which broadens its applicability and usefulness. Obviously, the topology degenerates into the well-known lattice network in the case of symmetric two-ports. The performance evaluation of the equivalent circuit is addressed by extracting its element values for two LWA unit cells: one based on the full-wave electromagnetic simulation of a series-fed coupled patch, and the other based on the measurements of a complementary strip-slot. These performance evaluations include an analysis of the influence on the equivalent circuit elements of the two-port degree of asymmetry. In addition, a model is found for the behavior of the two-ports as a function of the degree of asymmetry using the proposed equivalent circuit topology. A simple methodology is proposed to design these LWA unit cells. Unlike the methodologies proposed in [4], [13], this one is simple and general.

This paper is structured as follows. Section II shows the eigenstate decomposition of a generic two-port. Section III proposes the new topology for both the admittance and impedance parameters. Section IV shows two examples of the application of the proposed equivalent circuit to model non-symmetric structures. Section V proposes a model for the asymmetry and shows its applicability to LWA design. Finally, Section VI summarizes the main conclusions.

II. THEORETICAL BACKGROUND

In [11], it was found that a non-symmetric reciprocal two-port network can be decomposed using an eigenstate formulation. For the sake of completeness, this argument is summarized here. Two decompositions were proposed, one of them using the admittance parameters and the other using the impedance parameters. From the admittance matrix of the network (the reasoning for the impedance matrix is analogous),

$$
[Y] = \begin{bmatrix} y_{11} & y_{12} \\ y_{12} & y_{22} \end{bmatrix}
$$
 (1)

its eigenvalues can be obtained as

$$
\lambda_1 = \frac{y_{11} + y_{22} + \sqrt{(y_{11} - y_{22})^2 + 4y_{12}^2}}{2}
$$
 (2a)

$$
\lambda_2 = \frac{y_{11} + y_{22} - \sqrt{(y_{11} - y_{22})^2 + 4y_{12}^2}}{2}
$$
 (2b)

and their associated normalized eigenvectors can be obtained as

$$
\overrightarrow{v_1} = \frac{1}{\sqrt{|p|^2 + 1}} \begin{bmatrix} p \\ 1 \end{bmatrix}
$$
 (3a)

$$
\overrightarrow{v_2} = \frac{1}{\sqrt{|p|^2 + 1}} \begin{bmatrix} -1 \\ p \end{bmatrix},\tag{3b}
$$

where

$$
p = \frac{y_{11} - y_{22} + \sqrt{(y_{11} - y_{22})^2 + 4y_{12}^2}}{2y_{12}}.
$$
 (4)

By the definition of the eigenvalues and eigenvectors, it is possible to write, then,

$$
[Y]\overrightarrow{v_1} = \lambda_1 \overrightarrow{v_1} \tag{5a}
$$

$$
[Y]\overrightarrow{v_2} = \lambda_2 \overrightarrow{v_2} \tag{5b}
$$

and, by interpreting these expressions in terms of circuits, the eigenvector $\vec{v_i}$ can be seen as applied voltages in each of the ports that produce the current $\lambda_i \overrightarrow{v_i}$ (*i* = 1, 2). Unifying the expressions (5a) and (5b) and separating the result into the sum of two matrices, it can be written as

$$
[Y][\overrightarrow{v_1} \ \overrightarrow{v_2}] = [\lambda_1 \overrightarrow{v_1} \ \overrightarrow{0}] + [\overrightarrow{0} \ \lambda_2 \overrightarrow{v_2}]. \tag{6}
$$

Then, it is possible to split the [*Y*] matrix into two other admittance matrices, $[Y^a]$ and $[Y^b]$, as

$$
[Y] = [\lambda_1 \overrightarrow{v_1} \quad \overrightarrow{0}] [\overrightarrow{v_1} \quad \overrightarrow{v_2}]^{-1} + [\overrightarrow{0} \quad \lambda_2 \overrightarrow{v_2}][\overrightarrow{v_1} \quad \overrightarrow{v_2}]^{-1} \quad (7a)
$$

$$
[Y] = [Y^a] + [Y^b].
$$
 (7b)

Their values are given by

$$
[Y^a] = \lambda_1 \begin{bmatrix} \frac{p^2}{p^2 + 1} & \frac{p}{p^2 + 1} \\ \frac{p}{p^2 + 1} & \frac{1}{p^2 + 1} \end{bmatrix}
$$
 (8a)

$$
[Y^{b}] = \lambda_2 \begin{bmatrix} \frac{1}{p^2 + 1} & \frac{-p}{p^2 + 1} \\ \frac{-p}{p^2 + 1} & \frac{p^2}{p^2 + 1} \end{bmatrix}.
$$
 (8b)

FIGURE 1. Proposed eigenstate-based equivalent circuit using the admittance matrix.

Analogously, for the impedance formulation, the separation into submatrices is the following:

$$
[Z] = [Z^a] + [Z^b] \tag{9a}
$$

$$
[Z^a] = \frac{1}{\lambda_1} \begin{bmatrix} \frac{p^2}{p^2 + 1} & \frac{p}{p^2 + 1} \\ \frac{p}{p^2 + 1} & \frac{1}{p^2 + 1} \end{bmatrix}
$$
 (9b)

$$
[Z^{b}] = \frac{1}{\lambda_{2}} \begin{bmatrix} \frac{1}{p^{2}+1} & \frac{-p}{p^{2}+1} \\ \frac{-p}{p^{2}+1} & \frac{p^{2}}{p^{2}+1} \end{bmatrix}.
$$
 (9c)

Since the admittance matrix is the inverse matrix of the impedance matrix, by the properties of the eigenvalues, the impedance-matrix eigenvalues are the inverse of the admittance-matrix eigenvalues and their eigenvectors are the same. For this reason, in this derivation, the same eigenvalues of the immittance matrix, λ_1 and λ_2 , and the same relation between the coefficient of their eigenvectors, *p*, have been used, regardless of whether the admittance or impedance matrix is considered.

Several circuit topologies for the subnetworks $[Y^a]$, $[Y^b]$, $[Z^a]$ and $[Z^b]$ were proposed in [11]. However, they were the cause of its two main problems. The first one is that a frequency-dependent shift in the reference planes must be added to obtain positive real parts for the circuit immitances. The second problem is that when ports 1 and 2 are isolated from each other $(y_{12}$ or z_{12} are close to zero), none of the topologies in [11] is capable to reproduce simultaneously the input impedances at the two circuit ports.

III. PROPOSED EQUIVALENT-CIRCUIT TOPOLOGY

A new circuit topology for the subnetworks is proposed which solves the aforementioned drawbacks. The key point is to place a complex-turns-ratio transformer between two identical admittances, as depicted inside the dashed rectangles of Fig. 1. This modification has a dramatic impact on the performance of the equivalent circuit.

FIGURE 2. Proposed eigenstate-based equivalent circuit using the impedance matrix.

A suitable analogous topology for the impedance matrix case is also proposed and it is shown inside the dashed rectangles of Fig. 2. Note that, in both cases, the equivalent circuit is defined by three complex parameters, namely λ_1 , λ_2 , and *p*. The extraction of these parameters over frequency is straightforward, since they are, respectively, the eigenvalues of the admittance matrix, given by (2a) and (2b), and the ratio between the components of its eigenvectors (4).

A relevant characteristic of these topologies is that the values of the immittances are the same as the eigenvalues of the admittance/impedance matrices. As proved in the Appendix, the real parts of the eigenvalues of the admittance matrix of a lossy reciprocal network are always positive or zero. Thus, every admittance of the equivalent circuit will never have negative real parts, provided the two-port is passive and reciprocal. Regarding the impedance-matrix-based equivalent circuit, the real parts of its impedances are always positive, since the inverse of a complex number with a positive real part will also have a positive real part.

Note that the turns ratios of the transformers are defined by a single parameter *p*, which is, in general, a complex number. It is important to note that complex-turns-ratio transformers are elements used in the synthesis of networks [14]. It is also important to realize that these transformers are two-ports that, depending on the load conditions at their ports, can be lossy or active. If the turns ratio is real, the transformer is lossless. The role played by these two transformers in the equivalent circuit is clearly illustrated by computing the dissipated power in the two-port under arbitrary excitation. This power is given by

$$
P_{dis} = \frac{1}{2}Re{V_1^* I_1 + V_2^* I_2}
$$

= $\frac{1}{2}|A|^2 Re{\lambda_1} + \frac{1}{2}|B|^2 Re{\lambda_2}$
+ $\frac{1}{2}Re{\frac{(p - p^*)(A^* B \lambda_2 - AB^* \lambda_1)}{|p^2| + 1}}$, (10)

which leads to

$$
P_{dis} = |A|^2 P_{dis}^a + |B|^2 P_{dis}^b + \frac{Im\{p\}}{|p^2|+1} Im\{AB^*\lambda_1 - A^*B\lambda_2\}, \quad (11)
$$

where *A* and *B* are the complex amplitudes of the linear combination of eigenvectors that describe an arbitrary excitation, i.e.,

$$
\begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = \frac{1}{\sqrt{|p^2|+1}} \Big\{ A \begin{bmatrix} p \\ 1 \end{bmatrix} + B \begin{bmatrix} -1 \\ p \end{bmatrix} \Big\}.
$$
 (12)

The first and second terms of (11) are, respectively, the powers dissipated (P_{dis}^a and P_{dis}^b) in each subcircuit when only the corresponding eigenmode is excited. It is important to highlight that P_{dis}^a and P_{dis}^b are always positive or zero as $Re{\{\lambda_1\}}$ and $Re{\{\lambda_2\}}$ are non-negative. It is straightforward to prove that, when only one of the eigenmodes is excited, the voltages at both ports of the corresponding transformer are zero (virtual short-circuit) and, thus, the dissipated power in the transformer is zero, even if the turns ratio is complex. The third term of (11) is the so-called interaction term and accounts for the non-orthogonality of the eigenmodes. Note that this term is zero if the turns ratio is real, i.e., the eigenmodes are orthogonal. On the contrary, if the turns ratio is complex, the eigenmodes are non-orthogonal and the interaction term is different from zero (positive or negative). In this case the complex-turns-ratio transformers are active or lossy and provide the required power to account for the energy conservation. The physical origin of the non-orthogonality of the eigenmodes is the asymmetry of the two-port. However, the asymmetry of the circuit does not necessarily imply the non-orthogonality of the eigenmodes. In particular, if the twoport is either resistive or lossless, the resulting turns ratios are real, which implies that the eigenmodes are orthogonal although the two-port is asymmetric.

Two limit cases should be mentioned, because of the capacity of the proposed circuit to properly represent both situations. The first one is when $y_{12} = 0$ ($z_{12} = 0$) and the ports are isolated. The turn ratios of the transformers tend either to zero or infinity ($p = 0$ or $p \to \infty$) and therefore, the two transformers exhibit an open circuit and a short circuit in their ports, which isolates the two ports of both subnetworks. As a result, the input admittance seen at each port will be λ_1 and λ_2 , which are the theoretical values of the input admittances of the isolated two-port. Given the value of *p*, the interaction term of (11) vanishes, which corresponds to the fact that there is no interaction between one port and the other.

The second limit case is the symmetric network, with $p =$ 1. The transformers then act either as a direct connection or as a crossover, and the resulting circuit topology becomes the classic lattice network with series admittances of value λ_2 and cross admittances of value λ_1 . Once again, the interaction term of (11) vanishes (in this case because *p* is real). This is an expected result, since the modes of the lattice network are

FIGURE 3. The SFCP unit cell proposed in [4]. (a) Top view. (b) Side view.

always orthogonal. Consequently, the lattice network becomes a particular case of the proposed equivalent circuit.

IV. CASE STUDIES

In this section, the performance of the proposed equivalentcircuit topology is evaluated by extracting its component values for two, previously proposed, different unit cells of LWAs. The component values are extracted from the simulated or measured admittance parameters using the procedure specified in Section III. The addition of a suitable amount of asymmetry to these structures was already proposed in [4] and [15] to solve the problem of the open-stopband effect at broadside.

A. SERIES-FED COUPLED PATCH

The first analyzed structure is the so-called Series-Fed Coupled Patch (SFCP) [4]. It consists of an on-top-stacked patch with gap coupling to the feeding line, as shown in Fig. 3. In [4], an equivalent circuit and its design methodology were proposed to identify the optimum level of asymmetry (in this case, moving the patch along the feeding line axis a distance *d*). The process, however, is elaborated, complex, and does not provide much in terms of physical insight.

With the aim of illustrating the advantages of the equivalent-circuit topology proposed in this work, several SFCP structures with the same dimensions as in [4], but with different degrees of asymmetry, *d*, were simulated using the commercial software HFSS. The eigenvalues and associated eigenvectors of the resulting admittance matrices were computed, which form the equivalent circuit from Fig. 1, and are shown in Fig. 4. One of the targets of this study is to check how the parameters change when increasing its degree of asymmetry. To have a reference for the degrees of asymmetry analyzed, if $d = 1650 \,\mu \text{m}$ the patch and microstrip would have no overlapping area in one of the sides, whereas $d = 0$ corresponds to the symmetric case.

In all cases, even with the numerical errors expected from a simulation, the real parts of the admittances are positive, and the equivalent circuit behaves smoothly also at lower frequencies, where *y*¹² is low. Additionally, it can be seen that the turns ratios of the transformers are almost real, which means that the even-like and odd-like modes are also almost orthogonal, as explained in Section III. It is also noteworthy that the values of the immittances barely change when the level of asymmetry is changed. This is a very relevant result, since it means that the information about the asymmetry of the structure is mainly in the magnitude of the turns ratio of the transformers. From the physical point of view, this is not surprising as can be understood as the almost no alteration of the eigenmodes of the structure when a slight asymmetry is introduced. This asymmetry affects mainly and almost uniquely the interaction between the two eigenmodes, thus corroborating the powerful physical insight of the proposed topology.

It is worth mentioning that the obtained immittances for this case can be represented with frequency-independent lumped components over a relatively narrow band (8.5 GHz to 11 GHz). Admittance λ_1 can be represented as a parallel GLC shunt resonator ($C = 48.1$ pF, $L = 0.539$ nH, and $G =$ 0.274 mS) and λ_2 using a series RLC resonator ($C = 60.0$ fF, $L = 4.35$ nH, and $R = 7.82 \Omega$).

B. COMPLEMENTARY STRIP-SLOT

In order to validate the new circuit topology with actual measurements, the so-called *complementary strip-slot* has been chosen as the second case study. This structure was proposed in [16] as a planar radiating element with outstanding broadband matching. It consists of a slot etched on the ground plane of a microstrip line. The complementary stub (strip) of the slot is placed on the microstrip layer, just above and aligned to the slot, as a matching element. The cell structure is symmetric, with its input-output symmetry plane determined by the longitudinal axes of both the strip and the slot. This structure is especially interesting for the present analysis because the contribution of the strip is associated with the even mode, and that of the slot, with the odd mode. A consequence of the eigenstate separation property of the proposed equivalent circuit is that it can separate the contribution of these two modes and, thus, the contribution of the slot and stub, even if some asymmetry is added. This introduction of asymmetry to the structure was proposed in [15] to reduce the broadside

FIGURE 4. Circuit parameters of the artificial SFCP unit cell for several asymmetries (*d* **parameter) using the proposed equivalent circuit. (a) Magnitude of** *p***.** (b) Real part of $λ_1$, (c) Real part of $λ_2$, (d) Phase of *p*. (e) Imaginary part of $λ_1$, (f) Imaginary part of $λ_2$.

FIGURE 5. Backlit photograph of a prototype of the complementary strip-slot with certain misalignment *d***.**

open-stopband effect when building an LWA with this element.

Several prototypes of the strip-slot element with different degrees of misalignment *d* have been fabricated. Fig. 5 shows a partially backlit photograph of one of them. This misalignment is introduced by moving the strip and the slot a distance *d*/2 towards Port 2 and Port 1, respectively. In this case, the element values of the equivalent circuit have been extracted from the measured S-parameters with respect to $Z_0 = 50 \Omega$, and the reference planes have been symmetrically placed 0.7 mm away from the center of the slot. To have a reference for the degrees of misalignment analyzed, the case of $d = 540 \mu$ m corresponds to the minimum value for which the

strip and the slot have no overlapping area (they are adjacent elements loading the microstrip), whereas $d = 0$ corresponds to the symmetric case. The lengths of the strip and the slot are such that their first resonant frequencies (total length equal to $\lambda/2$) appear around 5.4 GHz. The structure is fabricated on GML 1032 substrate with $\epsilon_r = 3.2$ and 30 mil thickness. The TRL calibration technique was used to obtain the measurements. The calibration kit consisted of three "Lines" to cover the whole band of interest. Some anomalies occurred at the frequencies where the calibration changed from one "Line" to another, with the largest errors between 9.4 and 9.8 GHz.

These structures were already analyzed using the previous eigenstate-based equivalent circuit from [11]. However, in order to obtain admittances with positive real parts, power orthogonality had to be enforced using frequency-dependent shifts in the reference planes. Furthermore, when these different reference planes were used, the variation of the admittances with the asymmetry was higher. The equivalent circuit of these measured structures using the topology proposed in this work is shown in Fig. 6. With this topology, the real parts of the admittances are positive in the measured frequency range, but between 9.4 GHz and 9.8 GHz. At these frequencies, the condition (20c) from the Appendix is not fulfilled as a consequence of the anomalies introduced by the calibration kit. As in the case of the SFCP, the variation of the admittances with the asymmetry is small, even though they are different physical structures. Thus, the λ_1 and λ_2 values still represent properly the slot and the strip admittances, separately, with the transformation ratio of the transformer absorbing the effect of

FIGURE 6. Circuit parameters of the measured strip-slot unit cells for several asymmetries (*d* **parameter) using the proposed equivalent circuit.** (a) Magnitude of 1/p. (b) Real part of λ_1 . (c) Real part of λ_2 . (d) Phase of 1/p. (e) Imaginary part of λ_1 . (f) Imaginary part of λ_2 .

the asymmetry. With this case study, the eigenstate-separation capabilities of the equivalent circuit are illustrated, in addition to its robustness in the presence of errors and inaccuracies from measurements.

In the case of the complementary strip-slot, the immittances can be properly modeled over broad bandwidth using transmission lines (open-ended for the stub and short-ended for the slot) as discussed in [16].

V. ASYMMETRY MODEL AND ITS USE IN LWA UNIT-CELL DESIGN

It is apparent that, at least for some structures, the *p* parameter seems to contain all or most of the information about the asymmetry. If this is the case, it may be possible to obtain a relation between the misalignment and *p*. After the results of Fig. 4 and Fig. 6, it has been found out, heuristically, that it is possible to reproduce *p* as:

$$
p = 1 - g(f)d^n \tag{13}
$$

where *d* is the misalignment of the different layers of the structure causing the asymmetry, *n* is a frequency-independent positive real value, and $g(f)$ is a complex function of the frequency. Note that both $g(f)$ and *n* do not change with the misalignment of the structure. To obtain the function $g(f)$ and the exponent *n* of a particular structure, the simulation/measurement results of *p* for at least two different asymmetry values must be obtained first. From those data and using (13), $g(f)$ and *n* are obtained. Once the value of *p* is

FIGURE 7. *g***(***f***) function of the asymmetry model for the SFCP unit cell.**

given in terms of $g(f)$ and *n*, the model allows predicting the behavior of the complete structure for any value of *d*, since the eigenvalues are assumed invariant with the asymmetry level. However, this model will be only valid for values of *d* that are not too high in relation to the values of *d* of the simulated/measured cases.

The model of the *p* parameter of the SFCP case has been computed. From the 300 μ m and the 600 μ m cases, the mean of the function $g(f)$ and parameter *n* have been found. The first one can be seen in Fig. 7, and the value found for the exponent is $n = 1$. The same values are obtained if other two different asymmetry cases are used, which shows the consistency of the model. To prove that (13) properly

 (b)

FIGURE 8. Simulated values of *p* **of the SFCP unit cell for several asymmetries versus those computed by the model. (a) Magnitude. (b) Phase.**

models the *p* parameter, the computed values using the model and the original simulated values are shown in Fig. 8, where more asymmetry cases ranging from 150 and 600 μ m were included. It can be seen that the model can both interpolate and extrapolate results. However, it was not able to predict cases with more asymmetry than 600 μ m as accurately. Also, it is especially noteworthy the fact that the same dependence with frequency of $g(f)$ is valid for different asymmetry values over such a wide bandwidth.

Additionally, the model of the complementary strip-slot case has been also computed using the results of Fig. 6 from the 150 μ m and the 270 μ m misalignment values. The function $g(f)$ can be seen in Fig. 9, and the value of *n* is 0.45. The values of *p* predicted by the model and the measured values are shown in Fig. 10. Using the values of *p* given by the model, the eigenvalues for any single case and equations (7b), (8a), and (8b), it is possible to predict the admittance parameters of the structure with other asymmetry levels. To show this concept, the magnitude and phase of the S parameters of the measured cases are plotted in Fig. 11 along with those obtained from the modeled *p* and the eigenvalues of the symmetric case. It can be seen that the predicted and measured values are very similar in a very wide range of frequencies, even though each measurement is from a different manufactured board. This further proves that, once the asymmetry model is obtained, it is possible to get the two-port parameters of the

FIGURE 9. *g***(***f***) function of the asymmetry model for the complementary-strip-slot unit cell.**

FIGURE 10. Measured values of *p* **of the complementary-strip-slot unit cell for several asymmetries versus those computed by the model. (a) Magnitude. (b) Phase.**

structure for other asymmetry levels without more simulations or measurements.

Furthermore, the advantages of the new circuit topology are used to give a design example of the previously analyzed SFCP unit cell. LWAs suffer from the open stop-band effect at broadside, which deteriorates the radiation performance at this angle. One of the approaches to mitigate this undesirable effect is to minimize the reflection of the unit cell at the broadside frequency, since, at that frequency, the reflections of each element are added in phase, leading to a strong mismatching and a drop in the radiation efficiency [17]. The asymmetry level of this unit cell was already designed in [4] to eliminate

FIGURE 11. S parameters of the measured strip-slot unit cells for several asymmetries versus those computed by the model. (a) Magnitude of *S***11. (b) Magnitude of** *S***22. (c) Magnitude of** *S***21. (d) Phase of** *S***11. (e) Phase of** *S***22. (f) Phase of** *S***21.**

the open-stopband effect following this approach, however, a more general, different design procedure is illustrated here using the proposed asymmetry model. The goal is to find the best asymmetry level, the parameter d , to minimize the reflection at broadside by changing its Bloch impedance at its first port, Z_{B+} , without changing the radiation properties of the unit cell.

The following method would not be valid if the broadside frequency changed with the level of asymmetry. However, it can be shown that, if the immittances of the structure do not change with the asymmetry and provided that the parameter *p* is real, the broadside frequency will be the same for those asymmetries. The broadside condition for a unit cell can be expressed as

$$
\beta L = 2\pi n,\tag{14}
$$

with β*L* the phase introduced by a unit cell of length *L*, and $n \in \mathbb{Z}$. Taking into account that

$$
\cos \gamma L = \frac{A+D}{2},\tag{15}
$$

then, the broadside condition in terms of the *ABCD* parameters of the unit cell can be written as

$$
\frac{A+D}{2} \in \mathbb{R}.\tag{16}
$$

The *ABCD* parameters can be obtained from the parameters of the proposed topology and, thus, the condition can be expressed anew as:

$$
\frac{p^2 + 1}{p} \frac{\lambda_2 + \lambda_1}{\lambda_2 - \lambda_1} \in \mathbb{R}.\tag{17}
$$

This way, since λ_1 and λ_2 do not change with *d*, and *p* is real for small levels of asymmetry in the presented cases, the broadside frequency does not change either.

The proposed procedure is as follows. First, it is assumed (supported by the results from Section IV) that all the information about the asymmetry of the structures are absorbed by the turns ratio of the transformers (*p*) and, thus, the immittances $(\lambda_1$ and $\lambda_2)$ do not change (significantly) with the asymmetry level (*d*). It is possible, then, to find the dependence of the parameter *p* with *d* at the broadside frequency, f_0 , using (13). Afterwards, the value of the Bloch admittance at the first port of the cell, $1/Z_{B+}$, as a function of *d* is calculated as following:

$$
Y_{B+} = \frac{p^2 - 1}{p^2 + 1} \frac{\lambda_1 - \lambda_2}{2} \pm \sqrt{\left(\frac{\lambda_1 + \lambda_2}{2}\right)^2 - p^2 \left(\frac{\lambda_1 - \lambda_2}{p^2 + 1}\right)^2}.
$$
\n(18)

From this expression, the optimum value of *d* can be found so the reflection coefficient is minimum at *f*0.

Since this procedure uses the simulation of a single unit cell, it does not take into account the inter-cell coupling. In the presented cases, neglecting the inter-cell coupling resulted in a reasonable approximation. However, if more accurate results

FIGURE 12. Computed reflection coefficient at broadside frequency (10 GHz) of the SFCP unit cell in function of the asymmetry level using the proposed model.

FIGURE 13. Simulated reflection coefficient of the SFCP unit cell for different asymmetry levels.

are needed, a cascaded set of cells can be simulated and, using the method proposed in [18], the circuit parameters of a unit cell, including the inter-cell coupling, can be obtained.

The procedure has been applied to the SFCP unit cell $(f₀ = 10 \text{ GHz})$. Using (18), the magnitude of the predicted reflection coefficient in function of *d* is shown in Fig. 12, where it is possible to see that the minimum value is obtained when $d = 220 \mu$ m. To verify the result, the simulation results of the same coefficient for the different levels of asymmetry are shown in Fig. 13, where it is possible to appreciate that the lowest coefficient is obtained when $d = 220 \mu m$, which coincides with the result from Fig. 12 at 10 GHz. This means that, among the simulated cases, the value predicted by the model is the one with the lower reflection coefficient.

Given the low complexity of the presented unit cells, the computation-time reduction that is obtained after using this procedure may not be significant. However, it might be more useful in more sophisticated unit cells, or when the cell characterization comes from measurements. In any case, this procedure offers some physical insight and provides a specific methodology.

VI. CONCLUSION

In this paper, a general eigenstate-based equivalent circuit topology for lossy non-symmetric reciprocal two-ports is proposed. The circuit has only three complex parameters, namely two lossy immittances and the complex turns ratio of the

transformers. It has been rigorously proved that the proposed circuit topology guarantees immittances with non-negative real parts. Moreover, the proposed equivalent circuit can efficiently handle two-ports with isolated ports ($y_{12} = 0$ or $z_{12} =$ 0). Explicit and simple instructions for the straightforward extraction of the equivalent circuit element values are provided.

The equivalent circuit has been validated by analyzing two different structures. In both cases, the results have proved and illustrated its capabilities and robustness against numerical and measurement uncertainties in a very broad band. An important feature of the proposed equivalent circuit is that it is capable of displaying the structure asymmetry explicitly. As a consequence, it is most convenient when the two-port has two identifiable eigenstates, since it easily models the underlying physics of the structure. This capability has been illustrated by both cases; the turns ratio practically absorbs the asymmetry of the network leaving the immittances almost unperturbed. When this happens, it is possible to model the turns ratio with a simple expression in a very wide bandwidth as a function of the asymmetry. The model is able to accurately replicate the parameters of the structure when the asymmetry level is not too high. This feature allows the design of the asymmetry in unit cells to maximize the mitigation of the stopband in LWAs, but could be easily applied to other electromagnetic structures in which slight asymmetries play an important role.

The excellent simplicity, eigenstate-based derivation and extraordinary behavior in modeling structures of different nature makes the proposed equivalent circuit a good candidate for modeling lossy non-symmetric reciprocal two-ports, especially when physical insight is pursued. Indeed, the highlighted capabilities of the proposed network suggest that it can be regarded as a generalization of the symmetric lattice network for lossy non-symmetric two-ports.

Let [*A*] be a square matrix with complex elements and let $\pi\{[A]\},\nu\{[A]\}$ and $\delta\{[A]\}\$ be the number of eigenvalues of [A] with positive real part, negative real part and zero real part, respectively. The triple pair $(\pi\{[A]\}, \nu\{[A]\}, \delta\{[A]\})$ is called the *inertia* of [*A*]. According to [19], there exists a Hermitian matrix $[H]$ so that the matrix $[A][H] + [H][A]^*$ is positive definite if and only if $\delta = 0$. In this case, [A] and [H] have the same *inertia*.

Let us apply this theorem to the admittance matrix of a passive and reciprocal circuit. First, it is possible to define the matrix formed by the real parts of the elements of [*Y*] as

$$
Re\{[Y]\} = \frac{[Y] + [Y]^*}{2} = \begin{bmatrix} g_{11} & g_{12} \\ g_{12} & g_{22} \end{bmatrix}.
$$
 (19)

According to the energy theorem, the components of the admittance matrix must fulfill the following conditions [20]:

- $g_{11} > 0$ (20a)
- $g_{22} \ge 0$ (20b)

$$
g_{11}g_{22} - g_{12}^2 \ge 0. \tag{20c}
$$

Since the circuit is reciprocal, *Re*{[*Y*]} is a symmetric matrix and, thus, its eigenvalues are real. Furthermore, if the circuit is lossy, $g_{11} > 0$ and $g_{22} > 0$, and then, by using the properties of the eigenvalues,

$$
tr\left\{ \begin{bmatrix} g_{11} & g_{12} \\ g_{12} & g_{22} \end{bmatrix} \right\} = g_{11} + g_{22} = \lambda_{g1} + \lambda_{g2} > 0 \quad (21a)
$$

$$
det\left\{ \begin{bmatrix} g_{11} & g_{12} \\ g_{12} & g_{22} \end{bmatrix} \right\} = g_{11}g_{22} - g_{12}^2 = \lambda_{g1}\lambda_{g2} > 0, \quad (21b)
$$

which implies that the *Re*{[*Y*]} matrix is definite positive if the circuit is reciprocal and lossy, since its eigenvalues, λ_{g1} and λ_{g2} , are real and positive.

It is possible to write (19) as

$$
Re\{[Y]\} = \frac{1}{2} \left([Y][I] + [I][Y]^* \right) \tag{22}
$$

where [*I*] denotes the identity matrix, which is Hermitian, and so, it is possible to apply the theorem from [19]. This means that the *inertia* of [*Y*] and [*I*] are the same. The eigenvalues of [*I*] are $\lambda_{I1} = \lambda_{I2} = 1$, real and positive, then,

$$
\pi\{[I]\} = 2 \Rightarrow \pi\{[Y]\} = 2. \tag{23}
$$

This way, it is proved that the real parts of the eigenvalues of the [*Y*] matrix are always positive if the circuit is lossy and reciprocal.

If the circuit is lossless, it is straightforward to prove that the eigenvalues of $[Y]$ are purely imaginary numbers. The admittance parameters of a lossless circuit are always purely imaginary and, thus, it is possible to define a symmetric and real matrix *Im*{[*Y*]} as

$$
Im\{[Y]\} = \begin{bmatrix} b_{11} & b_{12} \\ b_{12} & b_{22} \end{bmatrix} = -j[Y] \tag{24}
$$

where b_{ij} are the imaginary parts of the circuit admittance parameters. Let us write the eigenstate decomposition as a function of this matrix:

$$
j\begin{bmatrix} b_{11} & b_{12} \\ b_{12} & b_{22} \end{bmatrix} \overrightarrow{v_b} = j\lambda_b \overrightarrow{v_b}
$$
 (25)

where λ_b is the eigenvalue of *Im*{[*Y*]} and $\overrightarrow{v_b}$ the eigenvector of the [*Y*] matrix. Since the *Im*{[*Y*]} matrix is real and symmetric, its eigenvalues are real and, consequently, the eigenvalues of [*Y*] are purely imaginary numbers.

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