

Corrections to “A Design of Input-Decimation Technique for Recursive DFT/IDFT Algorithm”

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IN [1], there are minor mistakes including the misuse of notations, the related description, the footnote and the reference. These minor mistakes do not influence on the discussion and the conclusions in the paper. The related mistakes are described as follows:

1. In page 4725, the citation [13] should be revised as listed in the reference [2] given here.
2. In page 4714, the misuse of notations and the caption as shown in Fig. 1 should be revised as below.

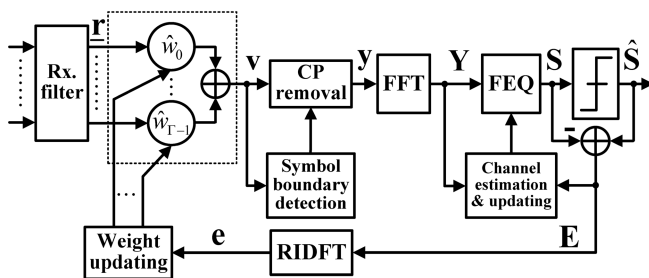


Fig. 1. Beamforming technique using RIDFT for broadband communication systems [2], where \hat{w}_γ and Γ are the γ th weight and the number of taps for the beamforming, respectively.

3. Before (27) in page 4718, the description should be revised as “Referring to (1) and (2) in [2], the N -point RIDFT with decimation-by-4 can be expressed as”.

4. In page 4721, the beamforming weight \hat{w}_m should be corrected as \hat{w}_γ that represents the γ th weight of beamforming.
5. The footnote should be corrected to add in the end of the first paragraph as “This paper was presented in part at the 27th European Signal Processing Conference (EUSIPCO), A Coruña, Spain, September 2-6, 2019.”
6. In page 4725, the “Acknowledgment” should be revised as below.

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REFERENCES

- [1] C.-F. Wu, C.-H. Chen, and M.-T. Shiue, “A design of input-decimation technique for recursive DFT/IDFT algorithm,” *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 66, no. 12, pp. 4713–4726, Dec. 2019.
- [2] C.-F. Wu, C.-H. Chen, and M.-T. Shiue, “A low-computation-cycle design of input-decimation technique for RIDFT algorithm,” in *Proc. 27th Eur. Signal Process. Conf. (EUSIPCO)*, A Coruña, Spain, Sep. 2019, pp. 1–5.

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