

Corrections for “A Mixed-Signal Circuit Technique for Cancellation of Multiple Modulated Spurs in 4G/5G Carrier Aggregation Transceivers”

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In [1], the description of Fig. 8 incorrectly presents the line plots in relation to the legend entries. The correct Fig. 8 and caption are presented here.

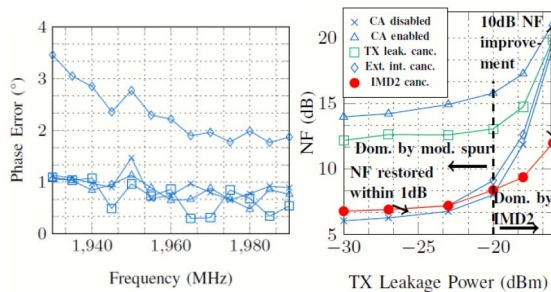


Fig. 8. Phase error of the DLL (left) and illustration of the cancellation performance (right).

REFERENCE

- [1] S. Sadjina, R.S. Kanumalli, K. Dufrière, and M. Huemer, “A mixed-signal circuit technique for cancellation of multiple modulated spurs in 4G/5G carrier aggregation transceivers,” *IEEE Solid-State Circuits Lett.*, vol. 2, no. 11, pp. 260–263, Nov. 2019, doi: [10.1109/LSSC.2019.2929025](https://doi.org/10.1109/LSSC.2019.2929025).

Manuscript received May 29, 2019; accepted July 6, 2019. Date of current version January 30, 2020. This article was approved by Associate Editor Sudhakar Pamarti. This work was supported in part by the Austrian Federal Ministry for Digital and Economic Affairs and in part by the National Foundation for Research, Technology and Development. (Corresponding author: Silvester Sadjina.)

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Digital Object Identifier 10.1109/LSSC.2020.2967620