

# Introduction to the IEEE SOLID-STATE CIRCUITS LETTERS

**T**HE fast-evolving nature of the semiconductor industry has created a need for a publication with a short turnaround time so that the latest developments can be reported with minimal delay. The IEEE SOLID-STATE CIRCUITS LETTERS (SSCL) is introduced to fulfill this need. Aiming for a submission-to-publication period of about three months, the SSCL serves as a peer-reviewed, archival journal having acceptance standards similar to those of the IEEE JOURNAL OF SOLID-STATE CIRCUITS, but with a four-page limit for the papers. The SSCL invites submissions containing new ideas whose efficacy is demonstrated by experimental results.

In order to achieve a fast turnaround, the SSCL draws upon an editorial board consisting of 30 associate editors and an editorial review board (ERB) consisting of about 100 experts who are committed to providing timely reviews. Each paper is reviewed

anonymously by two ERB members and a third, independent expert.

It is my honor to serve as the SSCL's first Editor-in-Chief. It has been an exhilarating experience to start a journal from scratch. I wish to acknowledge Dr. John Long for working hard to have the SSCL proposal approved by the IEEE, Abira Sengupta for her invaluable support, and the associate editors and the ERB members for their rapid response.

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**Behzad Razavi** (F'03) received the B.S.E.E. degree from the Sharif University of Technology, Tehran, Iran, in 1985 and the M.S.E.E. and Ph.D.E.E. degrees from Stanford University, Stanford, CA, USA, in 1988 and 1992, respectively.

He was an Adjunct Professor with Princeton University from 1992 to 1994, and Stanford University in 1995. He was with AT&T Bell Laboratories, Murray Hill, NJ, USA, and Hewlett-Packard Laboratories, Palo Alto, CA, USA, until 1996. Since 1996, he has been an Associate Professor and subsequently a Professor of electrical engineering with the University of California at Los Angeles, Los Angeles, CA, USA. He has authored the books entitled *Principles of Data Conversion System Design* (IEEE Press, 1995), *RF Microelectronics* (Prentice Hall, 1998, 2012) (translated to Chinese, Japanese, and Korean), *Design of Analog CMOS Integrated Circuits* (McGraw-Hill, 2001, 2016) (translated to Chinese, Japanese, and Korean), *Design of Integrated Circuits for Optical Communications* (McGraw-Hill, 2003, Wiley, 2012), and *Fundamentals of Microelectronics* (Wiley, 2006) (translated to Korean and Portuguese), and has edited the books entitled *Monolithic Phase-Locked Loops and Clock Recovery Circuits* (IEEE Press, 1996), and *Phase-Locking in*

*High-Performance Systems* (IEEE Press, 2003). His current research interests include wireless transceivers, frequency synthesizers, phase-locking, and clock recovery for high-speed data communications, and data converters.

Prof. Razavi was a recipient of the Beatrice Winner Award for Editorial Excellence at the 1994 International Solid-State Circuits Conference (ISSCC), the Best Paper Award at the 1994 European Solid-State Circuits Conference, the Best Panel Award at the 1995 and 1997 ISSCC, the TRW Innovative Teaching Award in 1997, the Best Paper Award at the IEEE Custom Integrated Circuits Conference in 1998, the McGraw-Hill First Edition of the Year Award in 2001, the Lockheed Martin Excellence in Teaching Award in 2006, the UCLA Faculty Senate Teaching Award in 2007, the CICC Best Invited Paper Award in 2009 and in 2012, the 2012 Donald Pederson Award in Solid-State Circuits, the American Society for Engineering Education PSW Teaching Award in 2014, and the 2017 IEEE CAS John Choma Education Award. He was a co-recipient of both the Jack Kilby Outstanding Student Paper Award, the Beatrice Winner Award for Editorial Excellence at the 2001 ISSCC, the 2012 and the 2015 VLSI Circuits Symposium Best Student Paper Awards and the 2013 CICC Best Paper Award. He was also recognized as one of the top ten authors in the 50-year history of ISSCC. He served on the Technical Program Committees of the ISSCC from 1993 to 2002 and VLSI Circuits Symposium from 1998 to 2002. He has also served as the Guest Editor and an Associate Editor of the IEEE JOURNAL OF SOLID-STATE CIRCUITS, the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS, and the *International Journal of High Speed Electronics*. He is a member of the U.S. National Academy of Engineering. He has served as an IEEE Distinguished Lecturer.