

# Communication

## Rotated Feed-Combined Reconfigurable Transmit RIS With Disparate Deployment of 1-bit Hybrid Units for B5G/6G

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**Abstract**—This communication presents an initial disparate deployment of 1-bit hybrid units using p-i-n diodes in transmit reconfigurable intelligent surface (TRIS) combined with a rotated feed source to address the gain limitation of the existing 1-bit TRIS. Two types of hybrid units, with a radiation phase difference of  $90^\circ$  by the delay line, are proposed in this study. The TRIS aperture is divided into two sections based on two different radiation centers by rotated feed, and the hybrid units in each section are initially configured to implement optimized quasi 2-bit beam collimation in the  $0^\circ$  and  $40^\circ$  for enhanced quantization loss. It is found that the array envelope is higher even in radiation directions other than the optimized directions, despite the initial disparate deployment of each section. Beamforming for  $-\theta$  direction in the H-plane is only considered for the proof of concept with the smallest size of TRIS aperture. A  $16 \times 16$  ( $7.6 \times 7.6 \lambda_0$  at 28 GHz) 1-bit hybrid TRIS offers gain enhancement of 0–5.6 dB in  $-70^\circ$  to  $-0^\circ$  directions of the array envelope compared with the conventional 1-bit TRIS. The proposed 1-bit hybrid TRIS yields an improved aperture efficiency of 24.4%.

**Index Terms**—Array envelope, beamforming, hybrid, p-i-n diodes, quantization, transmit reconfigurable intelligent surface (TRIS).

### I. INTRODUCTION

The era of 5G has arrived with the commercialization of mmWave in response to consumer demand for high-speed and low-latency communication. As a result, several studies have focused on high-gain antennas operating in low-loss systems to improve communication capacity since the mmWave band has critically degraded RF path loss. The development of an electronically reconfigurable antenna that can dramatically reduce the lossy components in the RF integrated circuit (RFIC) is attracting attention due to the inherent characteristics of the RF system, in which the insertion loss increases rapidly as the frequency increases [1]. In particular, reconfigurable intelligent surface (RIS) operating as a spatial beamformer is considered a promising beamforming solution since it can eliminate the beamforming IC and implement beam steering by using a low-loss dc control circuit [2], [3], [4], [5], [6], [7]. Liquid crystal, varactor diode, and p-i-n diode are typically used as reconfigurable components constituting an electrically controllable unit cell. In contrast to liquid crystals, p-i-n diodes can enable the implementation of beam switching at the nanosec level, in which high-speed switching is limited by the reorganization speed of molecular arrangement. In addition, the p-i-n diode has a relatively high Q-factor at high frequency compared

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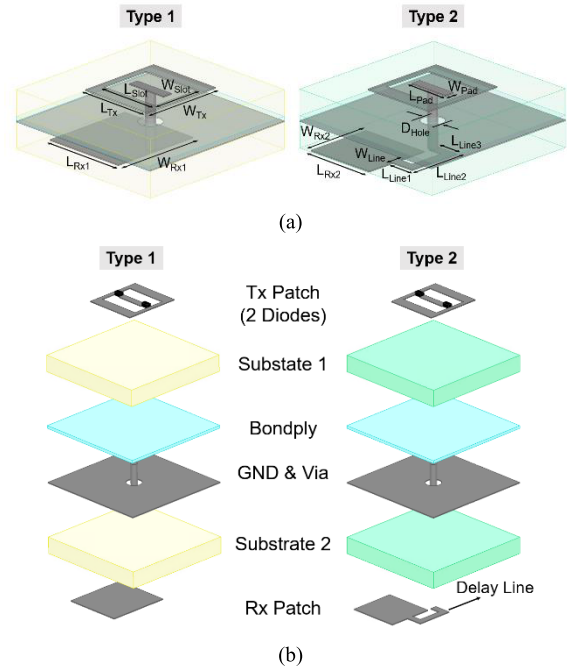


Fig. 1. (a) 1-bit hybrid units including design parameters and (b) its exploded view.

to the varactor diode, which makes it suitable for realizing a low-loss unit cell. Most reconfigurable unit cells based on p-i-n diodes employ the 1-bit current inversion method [8], [9]. Although this has the advantage of implementing a compact unit cell topology, it exhibits large gain degradation due to the fundamental limitation of 1-bit quantization loss [11]. Clemente et al. [2] presented a numerical analysis of p-i-n diode-based transmit reconfigurable intelligent surface (TRIS) successfully but reported high quantization loss of 4 dB. Diaby et al. [3] tried to improve the quantization loss by employing a 2-bit unit cell, but only reported an aperture efficiency of 15.9%. Wang et al. [5] reported a relatively high aperture efficiency of 22.6% by using asymmetric dipole elements. However, the implementation of a large area is limited since four bias lines are required per unit cell. Wang et al. [6] reported improved scan loss by using an equivalent magnetic dipole element but cannot overcome low broadside gain. Wang et al. [7] designed a subwavelength unit cell to enhance the stability of oblique incidence but eventually reported a low aperture efficiency of 14%.

Therefore, in this communication, we propose an initial disparate deployment of 1-bit hybrid units combined with a rotated feed source to overcome the low aperture efficiency of conventional 1-bit TRIS based on p-i-n diodes. Two types of 1-bit hybrid units with a radiation phase difference of  $90^\circ$  are arranged disparately in two sections according to the radiation centers of rotated feed

TABLE I  
DESIGN PARAMETERS AND THEIR VALUES OF 1 BIT  
HYBRID UNITS IN FIG. 1

Parameters & Values					
$L_{Rx1}$	2.6	$L_{Rx2}$	2.25	$L_{Line2}$	1.7
$W_{Rx1}$	2.85	$W_{Rx2}$	2.1	$L_{Line3}$	0.75
$L_{Tx}$	2.35	$L_{pad}$	1.3	$W_{Line}$	0.3
$W_{Tx}$	2.6	$W_{pad}$	0.3		
$L_{Slot}$	1.95	$D_{Hole}$	0.7		
$W_{Slot}$	1.6	$L_{Line1}$	0.75		

(Unit : mm)

and operate as quasi 2-bit for improved array envelope. This quasi 2-bit beamforming demonstrates improved array envelope compared to conventional 1 bit one despite the disparate deployment. The configuration, operating principle, and transmit characteristics of 1-bit hybrid units by incident angle are presented in Section II. Then, the initial deployment of 1-bit hybrid units according to the radiation center of the rotated feed source is discussed. In Section III, simulation and measurement results are presented. Finally, Section IV concludes this communication.

## II. DESIGN OF 1-BIT HYBRID TRIS

### A. Hybrid Unit Cell Design

Fig. 1 shows the drawings of 1-bit hybrid units, including design parameters and an exploded view. The values of design parameters are shown in Table I. The Rx layer is composed of a square patch and the Tx layer is composed of a U-slot patch based on two p-i-n diodes. The Tx and Rx layers are connected by metallic via through GND layer. A delay line extending from the type 2 Rx patch creates a  $90^\circ$  phase difference between the two hybrid units. The two substrates are HF-350F from Taconic, with a thickness of 0.76 mm and a dielectric constant and loss tangent of 3.5 and 0.0029, respectively. The bondply is HB-360 from Taconic, with a thickness of 0.13 mm and a dielectric constant and loss tangent of 3.65 and 0.0032, respectively. The unit cell period is 5.1 mm ( $0.48 \lambda_0$  at 28 GHz). Fig. 2(a) shows the unit cell configuration with asymmetrically mounted two p-i-n diodes and a compact dc bias topology. Two p-i-n diodes are mounted 87 asymmetrically between a rectangular ring and a center pad on the Tx layer. Therefore, the two diodes operate in different states by the dc voltage  $V_{DC}$  applied to the rectangular ring. A diode of the MADP-000907-14020 model is used, which operates as a series circuit of  $5.2 \Omega$  and  $30 \text{ pH}$  under positive bias and a parallel circuit of  $0.025 \text{ pF}$  and  $30 \text{ pH}$  under negative bias [10]. The center pad is in contact with the GND layer through the center via and the GND via. dc via is located at the middle point of the rectangular ring, which has the lowest electric field intensity for preventing RF leakage and is in contact with the dc line. Fig. 2(b) shows the surface current density of two types of hybrid units according to the state of two p-i-n diodes. The two diodes operate asymmetrically and realize a  $180^\circ$  phase difference by controlling the current distribution in each state. Two types of hybrid units have a  $90^\circ$  phase difference in the same state by the delay line in Fig. 1(b). Consequently, the two types of hybrid units have four transmission phases of an equally spaced  $90^\circ$  difference.

Fig. 3 shows the  $S_{21}$  and  $S_{21}$  phase of two 1-bit hybrid units with dc bias configuration according to the Floquet incident angle of the TE wave, as shown in Fig. 2(a). Two types of hybrid units have an insertion loss of less than 1 dB at 28 GHz in  $\theta = 0^\circ$  (normal incidence) condition. However, the insertion loss of type 2 with delay line increases to 2 dB in  $\theta = 40^\circ$  (steered incidence)

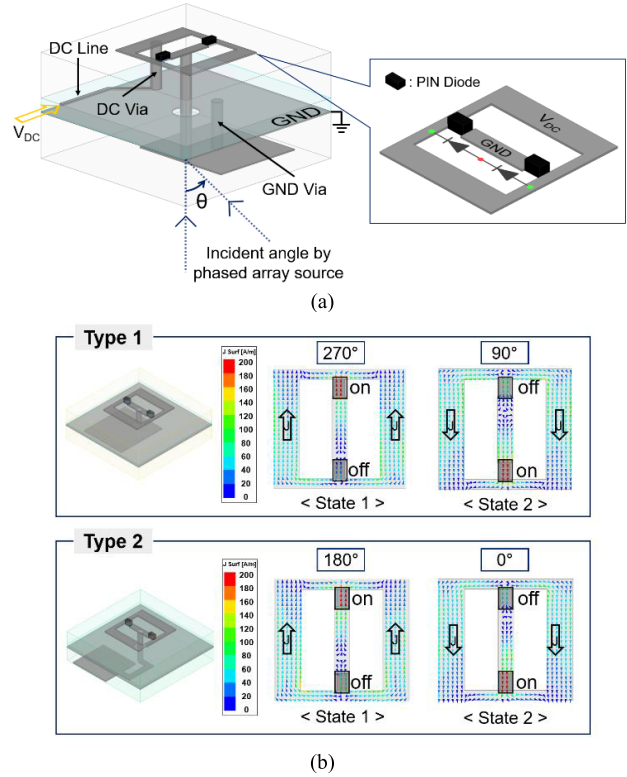


Fig. 2. (a) Unit cell with asymmetrically mounted two p-i-n diodes and compact dc bias configuration and (b) surface current density of two types of hybrid units according to the state of two p-i-n diodes.

TABLE II

EFFICIENCY OF THE 1 BIT HYBRID TRIS APERTURE ACCORDING TO THE ROTATED ANGLE OF FEED HORN

Angle of Horn	Spillover Efficiency	Illumination Efficiency	Total Efficiency
$0^\circ$	0.69	0.73	0.51
$40^\circ$	0.53	0.76	0.40

condition. In addition, type 2 deviates the transmission phase under the condition of  $\theta = 40^\circ$ , which impairs the equally spaced four transmission phases. In conclusion, the type 2 with a delay line topology is sensitive to the incident angle and has deteriorated transmission characteristics with respect to insertion loss and phase when combined with a rotated feed source.

### B. Deployment of 1 Bit Hybrid Units in Transmit RIS

Fig. 4(a) shows the rotated feed combined TRIS classified into two sections based on two different radiation centers by beam steering source. In this study, only two sections are employed to verify the unidirectional ( $-\theta$ ) beamforming cases. The TRIS aperture is shifted by  $1.9 \lambda_0$  from the feed center in the  $-y$ -direction to minimize the decrease in spillover efficiency due to the beam steering of the rotated feed source. The TRIS aperture is classified into two sections based on two different radiation centers by beam steering angle of  $\theta = 0^\circ$  and  $\theta = 40^\circ$ . The focal length (F) based on Section I is  $5 \lambda_0$ , and the aperture efficiency by the beam steering angle is calculated as Table II [12]. It should be noted that the total efficiency of TRIS with  $\theta = 40^\circ$  horn is 1 dB lower than that of TRIS with  $\theta = 0^\circ$  horn. Fig. 4(b) shows the electric field intensity of the TRIS aperture according to the rotated angle  $\theta$ . The 1-bit hybrid units in Sections I and II can be deployed differently to have respective

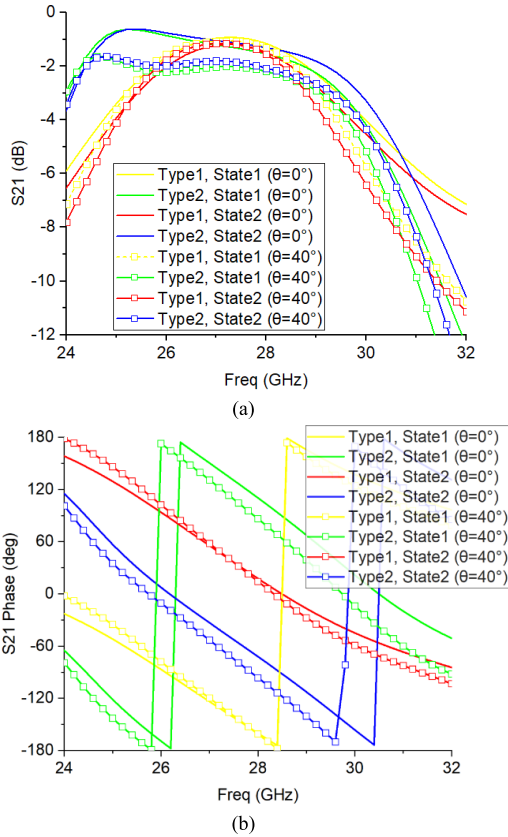


Fig. 3. (a)  $S_{21}$  and (b)  $S_{21}$  phase of two 1-bit hybrid units with dc bias configuration according to the Floquet incident angle of TE wave.

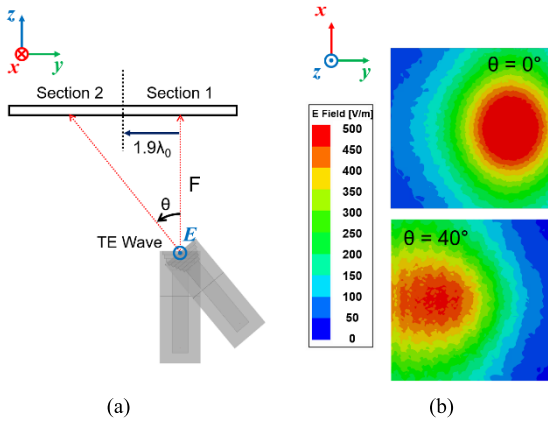


Fig. 4. (a) Rotated feed combined TRIS classified into two sections based on two different radiation centers by beam steering source and (b) electric field intensity of TRIS aperture according to the rotated angle  $\theta$ .

radiation characteristics since the radiation center to which most of the power from the source is incident is moved by the rotated angle of horn, as shown in Fig. 4(b).

Fig. 5 shows the ideal distribution of a 2-bit quantized unit cell for  $0^\circ$  (broadside) radiation with  $\theta = 0^\circ$  horn and  $-40^\circ$  radiation with  $\theta = 40^\circ$  horn. The deployment of the 2-bit quantized unit cell follows (1), and  $\angle S_{21}$  is the required phase for each mainbeam collimation

$$\text{phase}_{2\text{-bit}} = \begin{cases} 270^\circ, & \text{if } 225^\circ \leq \angle S_{21} < 315^\circ \\ 180^\circ, & \text{if } 135^\circ \leq \angle S_{21} < 225^\circ \\ 90^\circ, & \text{if } 45^\circ \leq \angle S_{21} < 135^\circ \\ 0^\circ, & \text{otherwise.} \end{cases} \quad (1)$$

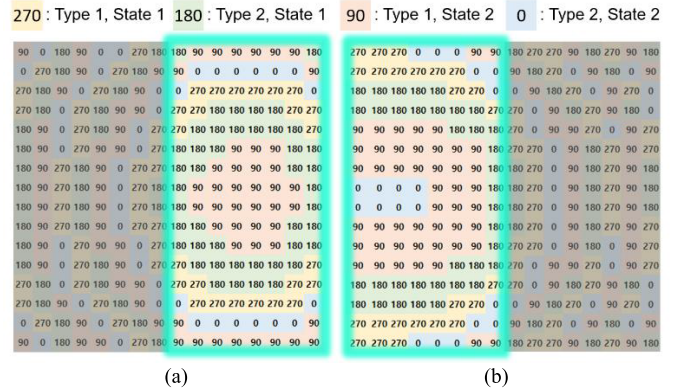


Fig. 5. Ideal distribution of a 2-bit quantized unit cell for (a)  $0^\circ$  (broadside) radiation with  $\theta = 0^\circ$  horn and (b)  $-40^\circ$  radiation with  $\theta = 40^\circ$  horn.

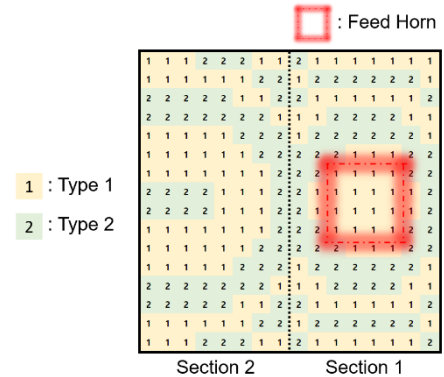


Fig. 6. Optimally deployed 1-bit hybrid units in each section for enhanced  $0^\circ$  (broadside) and  $-40^\circ$  collimated beam.

The initial deployment of 1-bit hybrid units is proposed in each section, as shown in Fig. 6, to implement the phase mapping of Fig. 5(a) in Section I and Fig. 5(b) in Section II for optimized gain of  $0^\circ$  and  $-40^\circ$  radiation. It should be noted that optimized quasi-2 bit quantization in two mainbeam directions can be implemented with 1-bit hybrid units. The deployment of the 1-bit hybrid unit cell follows

$$\text{phase}_{\text{Type 1}} = \begin{cases} 270^\circ, & \text{if } 180^\circ \leq \angle S_{21} < 360^\circ \\ 90^\circ, & \text{otherwise.} \end{cases}$$

$$\text{phase}_{\text{Type 2}} = \begin{cases} 180^\circ, & \text{if } 90^\circ \leq \angle S_{21} < 270^\circ \\ 0^\circ, & \text{otherwise.} \end{cases} \quad (2)$$

All beamforming cases include optimized directions. However, numerical analysis of array factor (AF) with 1-bit hybrid TRIS is required essentially since the section not including the radiation center also affects the beam collimation. Furthermore, for even array envelope, the analysis of beam scanning capability should be performed except for the optimized directions of  $0^\circ$  and  $-40^\circ$ .

### III. SIMULATION AND MEASUREMENT RESULTS

Fig. 7 shows the AF normalized by incident power in the  $16 \times 16$  aperture of a 1-bit hybrid TRIS with a beam steering source and a conventional 1-bit TRIS without a beam steering source. The conventional 1-bit TRIS has a  $16 \times 16$  aperture that is shifted by  $1.9\lambda_0$ , which is the same condition as the 1-bit hybrid one, and only type 1 is deployed on the entire aperture. As expected, the gain of  $0^\circ$  and  $-40^\circ$  is significantly increased due to 2-bit quantized phase mapping. Also, it is demonstrated that beam collimation except for

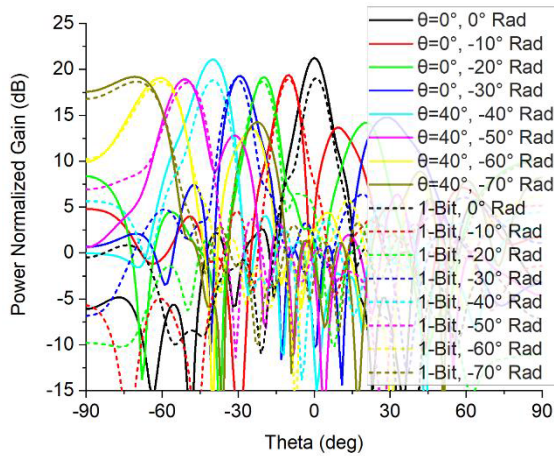


Fig. 7. AF normalized by incident power in  $16 \times 16$  aperture of 1-bit hybrid TRIS with beam steering source and conventional 1 bit TRIS without beam steering source.

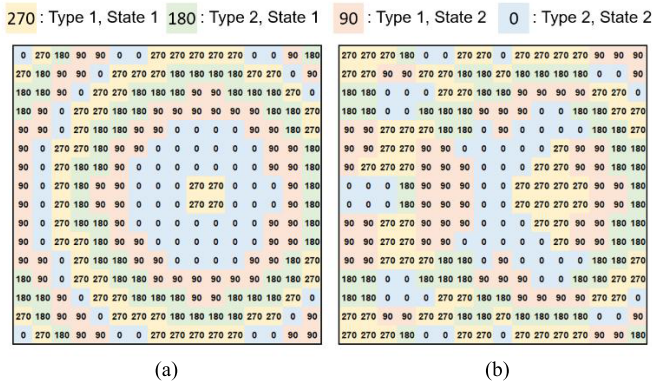


Fig. 8. (a) Ideal 2-bit and (b) quasi 2-bit phase mapping configurations for unoptimized  $-10^\circ$  radiation.

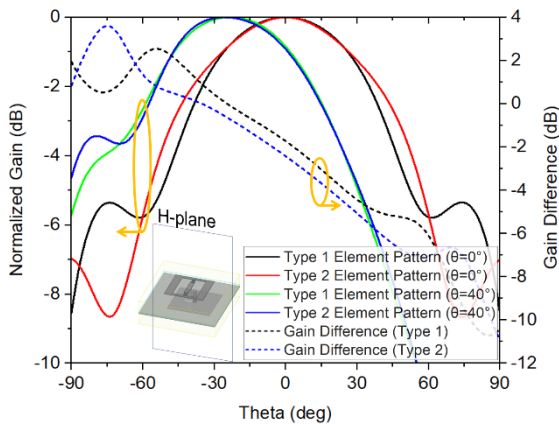


Fig. 9. Normalized element patterns of 1-bit hybrid units according to Floquet incident angle and realized gain difference between  $40^\circ$  and  $0^\circ$  incident angles.

the optimized directions of  $0^\circ$  and  $-40^\circ$  is successfully implemented with comparable gain. Fig. 8 shows ideal 2-bit and quasi 2-bit phase mapping configurations for unoptimized  $-10^\circ$  radiation. Relatively large minor lobe level, which is particularly prominent in  $-10^\circ$  to  $-30^\circ$  AF of Fig. 7, is caused by the uneven phase compensation by disparate deployment. Nevertheless, the array envelope is significantly improved for the overall scan angles. However, this AF is a power

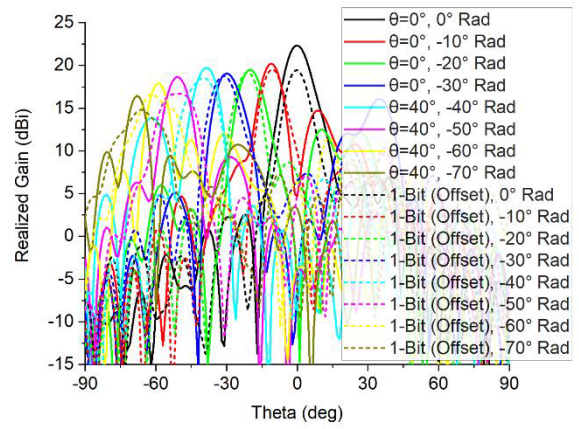


Fig. 10. Simulated radiation patterns of rotated feed combined 1-bit hybrid TRIS and conventional 1-bit one with offset aperture on the H-plane.

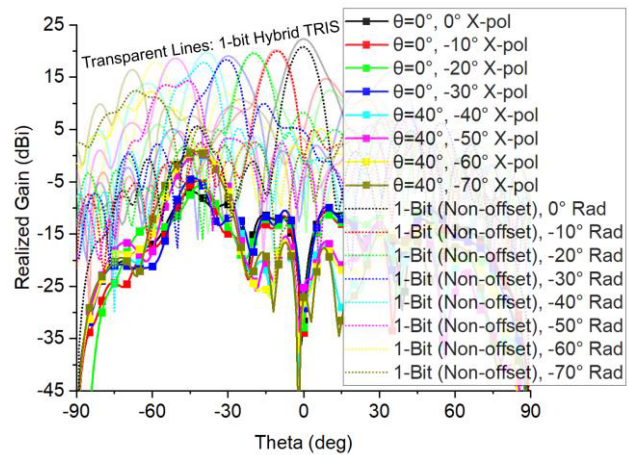


Fig. 11. Simulated co-polarized and cross-polarized radiation patterns of rotated feed combined 1-bit hybrid TRIS and radiation patterns of conventional 1-bit one with nonoffset aperture on the H-plane.

normalized value, in which the insertion loss of the unit cell and the total efficiency of the TRIS aperture are not considered. Therefore, a lower array envelope in  $-40^\circ$  to  $-70^\circ$  directions can be formed compared to the conventional 1 bit TRIS when the deteriorated transmission characteristics of type 2 and the reduced total efficiency of TRIS aperture combined with  $\theta = 40^\circ$  horn are considered. Nevertheless, the additional loss factor for  $-40^\circ$  to  $-70^\circ$  beam collimation is successfully compensated by the offset of the element patterns of 1-bit hybrid units with rotated horn. Fig. 9 shows the normalized element patterns of 1-bit hybrid units according to Floquet incident angle and the difference in realized gain between  $\theta = 40^\circ$  and  $\theta = 0^\circ$  incident angles. It should be noted that the peak gain of the Floquet element pattern under the condition of an  $\theta = 40^\circ$  incident angle is formed at  $30^\circ$ . That is, the gain difference between  $\theta = 40^\circ$  and  $\theta = 0^\circ$  shows that the element pattern gain of the offset pattern is greater than that of the nonoffset pattern for angles below  $-40^\circ$ . It is found that the gain difference shows a tendency to increase further as the angle decreases from  $-40^\circ$ . Fig. 10 shows the simulated radiation patterns of the rotated feed combined 1-bit hybrid TRIS and the conventional 1-bit one without beam steering source on the H-plane. It is demonstrated that the array envelope of 1-bit hybrid TRIS for all beam steering angles is higher than that of conventional 1-bit TRIS. The broadside gain of 1-bit hybrid TRIS is increased by approximately 2.9 dB compared

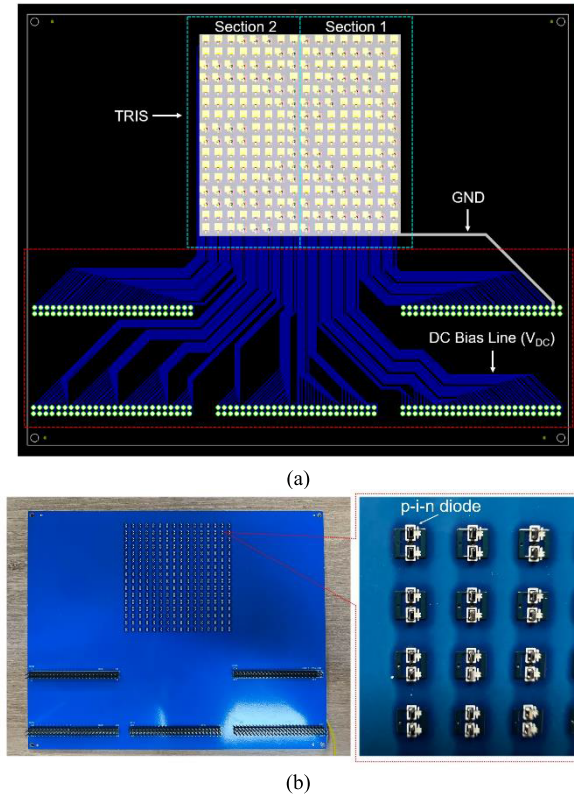


Fig. 12. (a) Design of 1-bit hybrid TRIS with dc bias topology. (b) Fabricated TRIS with mounted p-i-n diodes.

to the conventional 1-bit one. The improved value of 1.2 dB in  $-40^\circ$  radiation is lower than that of broadside. As shown in Fig. 9, this is estimated by deteriorated unit cell properties, reduced total efficiency of TRIS aperture, and relatively low gain compensation of offset element pattern in  $-40^\circ$ . The enhanced value of each direction from broadside to  $-70^\circ$  between two TRIS is 2.9, 0.7, 0.3, 0.3, 1.2, 1.9, 2.3, and 1.6 dB, respectively. Fig. 11 shows the simulated co-polarized and cross-polarized radiation patterns of rotated feed combined 1-bit hybrid TRIS and radiation patterns of conventional 1-bit one with nonoffset aperture on the H-plane. Conventional 1-bit TRIS may not require an offset aperture, so the array envelope is compared for this case additionally. Due to the increase in illumination efficiency, the gain between  $0^\circ$  and  $-40^\circ$  slightly increases, but the gain for angles lower than  $-40^\circ$  decreases compared to that of the offset aperture. The enhanced value of each direction from broadside to  $-70^\circ$  between two TRIS is 2.9, 0, 0, 0.8, 1.9, 4.4, 5.6, and 4 dB, respectively.

Fig. 12 shows the design of a 1-bit hybrid TRIS with dc bias topology and a fabricated one with mounted p-i-n diodes. The dc bias lines connected to each center pad and the dc GND line are in contact with the header pin at the bottom side of the PCB. As shown in Fig. 12(b), 512 diodes are used to control 256 1-bit hybrid units individually. Fig. 13 shows the conceptual drawings of the measurement setup of 1-bit hybrid TRIS and measured TRx antennas. An Arduino microcontroller is used to supply dc bias voltages of 0 or 5 V. Therefore, a potential difference of 2.5 or  $-2.5$  V is realized by applying a voltage of 2.5 V to dc GND. The Rx is a standard horn antenna with a gain of 13 dBi at 28 GHz operating in K-band. Fig. 14 shows the measured radiation patterns of rotated feed combined 1-bit hybrid TRIS. The measured patterns

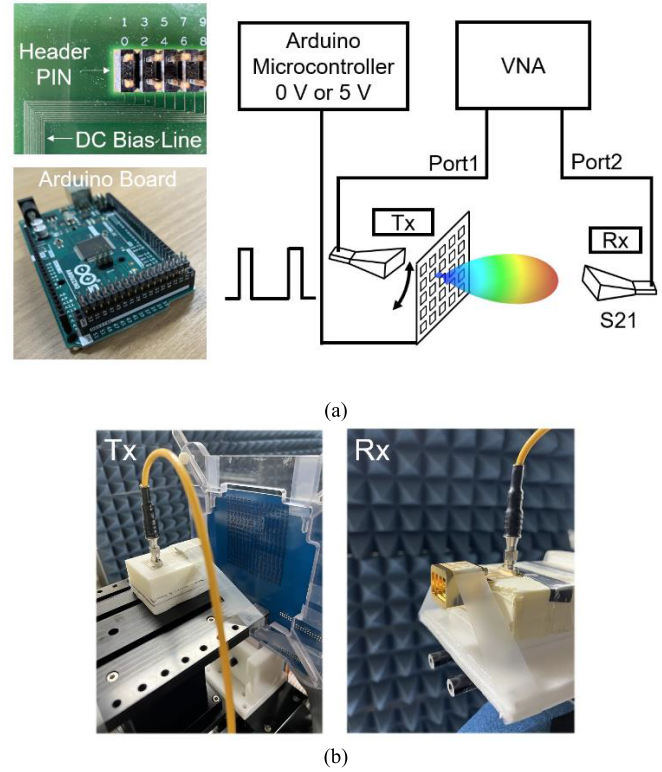


Fig. 13. (a) Schematic of measurement setup of 1-bit hybrid TRIS and (b) measured TRx antennas.

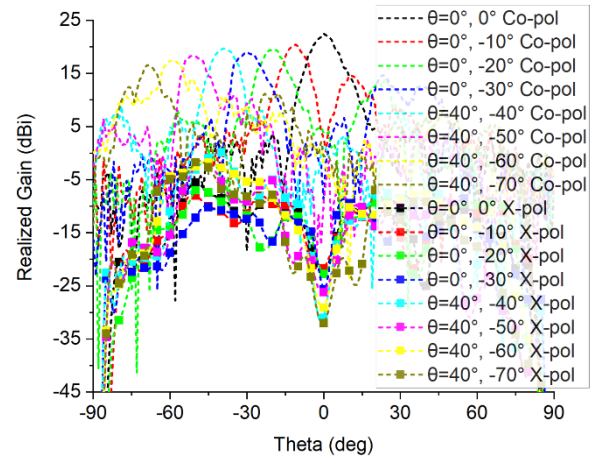


Fig. 14. Measured radiation patterns of rotated feed combined 1-bit hybrid TRIS.

correspond well with the simulation results, and the broadside gain is 22.5 dB, which has an aperture efficiency of 24.4%. The increase of broadside gain of the feed antenna by TRIS is 9.5 dB. Fig. 15 shows the simulated and measured realized gain of 1-bit hybrid TRIS in optimized  $0^\circ$  and  $-40^\circ$  directions. Based on the measured values, the 3 dB radiation bandwidths for the two optimized directions are 24.9–30.1 (18.6% in  $0^\circ$ ) and 24–30.2 GHz (22.1% in  $-40^\circ$ ), respectively. Table III shows the comparison between the prior 1-bit TRIS and the proposed 1-bit hybrid one. It should be noted that the proposed 1-bit hybrid TRIS combined with rotated feed source reports higher aperture efficiency compared to that of the previous 1-bit TRIS.

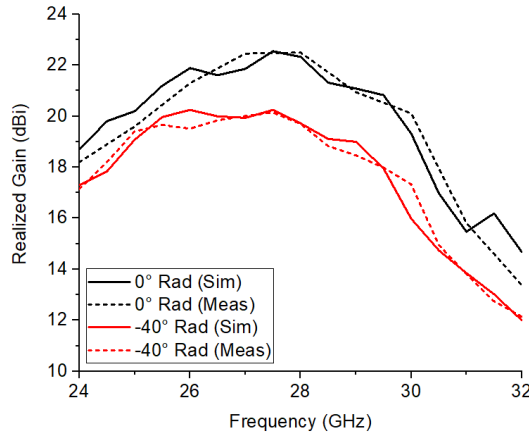


Fig. 15. Simulated and measured realized gain of 1 bit hybrid TRIS in optimized 0° and -40° directions.

TABLE III  
COMPARISON BETWEEN PRIOR 1 BIT TRIS AND A PROPOSED  
1 BIT HYBRID ONE

Reference	Frequency (GHz)	Scan Range (H-plane)	Aperture Size ( $\lambda_0^2$ )	Measured Gain (dBi)	Aperture Efficiency (%)
[2]	10	-70° to 70°	10×10	22.7	16
[3]	29	-60° to 60°	7×7	19.8	15.9
[5]	17.5	0° to 50°	4.9×4.9	18.3	22.6
[6]	28	-30° to 30°	8.6×8.6	21.4	14.7
[7]	28.4	0° to 50°	5.3×5.3	17	14
This Work	28	-70° to 0°	7.6×7.6	22.5	24.4

#### IV. CONCLUSION

This communication presented an initial disparate deployment of 1-bit hybrid units of a p-i-n diode-based TRIS combined with a rotated feed source for enhanced array envelope. Two types of 1-bit hybrid units with an equally spaced phase difference of 90° are designed based on delay line topology. The hybrid units are creatively deployed in the TRIS aperture classified into two sections by the radiation centers of the rotated feed source to realize an improved array envelope in all directions, even in radiation directions other than the optimized ones. The proposed 1-bit hybrid TRIS yields an aperture efficiency of 24.4%. It will be possible to attain improved beamforming performance with a larger area in TRIS applications, wherein the reduction of spillover efficiency by the beam steering source can be minimized.

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