

TSKARNA-norm Adaption Based NLMS with Optimized Fractional Order PID Controller Gains for Voltage Power Quality

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Abstract: The operation of a dynamic voltage restorer (DVR) is studied using a three-phase voltage source converter (VSC)-based topology to alleviate voltage anomalies from a polluted supply voltage. The control algorithm used included two components. The first is an adaptive Takagi-Sugeno-Kang (TSK)-based adaptive reweighted L_1 norm adaption-based normalized least mean square (TSK-ARNA-NLMS) unit, which is proposed for the extraction of fundamental active and reactive components from the non-ideal supply and is further employed to generate the load reference voltage and switching pulse for the VSC. The step size was evaluated using the proposed TSK-ARNA-NLMS controller, and the TSK unit was optimized by integration with the marine predator algorithm (MPA) for a faster convergence rate. The second, a fractional-order PID controller (FOPID), was employed for AC- and DC-link voltage regulation and was approximated using the Oustaloup technique. The FOPID ($PI^\gamma D^\mu$) provides more freedom for tuning the settling time, rise time, and overshoot. The FOPID coefficients (K_i , K_d , K_p , γ , and μ) were optimized by employing an advanced ant lion optimization (ALO) meta-heuristics technique to minimize the performance index, namely, the integral time absolute error (ITAE) and assess the accuracy of controllers. The DVR performance was validated under dynamic- and steady-state conditions.

Keywords: Takagi-Sugeno-Kang (TSK), ARNA-NLMS, FOPID-ALO, unbalances, gain optimization, approximation Oustaloup technique

1 Introduction

Recent advancements in solid-state technologies have led towards comfortable and peaceful lives. These solid-state devices cause power quality (PQ) issues in distribution networks (DNs) [1]. PQ issues are exponentially increasing owing to the rapid inventions and applications of sophisticated equipment based on solid-state devices or loads. Recently, researchers have focused on the mitigation of PQ issues, especially voltage related issues, such as sag, swell, imbalance, and distortions. These voltage anomalies and harmonic compensation are efficiently addressed using a series filter called a dynamic voltage restorer (DVR) [2]. Owing to unexpected system expansions, three-phase four-wire DN are experiencing significant power quality PQ issues, such as harmonics, the burden of reactive power, and excessive neutral current [3]. A

DVR is employed to reimburse the voltages, along with a voltage source converter (VSC) at the load side [4]. A study on different PQ issues and their mitigation using a DVR [5] showed the fast response of voltage compensators, such as the static series compensator (SSC), and voltage (sag and swell) correctors, such as the DVR. For the DVR operation, a split-capacitor topology based on the VSC was implemented, and a flux-charge model based on a feedback algorithm was employed to minimize the fault ride [6]. Jimichi et al. [7] studied the different architectures of a DVR, where series and shunt converters were connected in an adjacent manner, and described different DC capacitor ratings implemented with various topologies. To compensate for this imbalance, a split capacitor with a battery-supported DVR setup was designed and tested using a transformation-based control algorithm [8]. Another study [9] implemented a cascade-based multilayer inverter DVR setup that operated at medium voltage without employing an injection transformer. It is based on a self-supported DC bus capacitor, with the major

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issue of balancing and controlling the voltage of the DC link.

Numerous control schemes have been actively studied for extracting harmonic components from polluted load currents. Instantaneous reactive power theory (IRPT) involves the transformation of voltage and current to compute the active and reactive powers. The drawback is that it becomes difficult to operate under a non-ideal supply voltage^[10]. The synchronous reference frame theory (SRFT) versatile control technique estimates the DC quantity from three-phase components and realizes harmonics by employing a low-pass filter (LPF)^[11].

Adaptive filtering is a well-known control method for estimating the reference sinusoidal component from a disturbed load current. Adaptive filters have numerous applications in signal processing and control engineering. However, adaptive control algorithms offer optimal solutions over classical control approaches, particularly when the load is uncertain, which involves complex systems. The least mean square (LMS) control scheme is often used as an adaptive controller in a DSTATCOM^[12]. An appropriate step size selection is required to optimize the error, which increases the accuracy and convergence rate. The step size is directly proportional to the convergence rate and inversely proportional to the accuracy, which means that a higher step size value results in faster convergence but leads towards instability. Considering the convergence rate, step size, and control performance, a tradeoff is required to resolve this contradiction while selecting the objective. The contradiction between objectives is solved by selecting an appropriate convergence coefficient^[13]. Researchers have been interested in improving control setting optimization over the past several years. The frequently used LMS algorithms for improving PQ are normalized-LMS, variable step-size, and leaky LMS^[14]. Several control algorithms based on step size selection LMS^[15] and variable LMS (VLMS)^[16] have been recommended. A study^[17] illustrated the PQ concerns in distributed generation and mitigation using various custom power devices (CPDs). With the power normalization concept based on the selected input elements, an adaptive family of the LMS, that is, NLMS, was proposed because it offers an optimized

solution to select the step size and resolve the stability issues of the classical LMS^[18]. Other methods, such as the fuzzy-inference-based LMS^[19] and Kalman filter, have complex implementations in real time. The aforementioned methods include the adaptive filter theory. The adaptive technique estimates filter coefficients by optimizing the errors. The key drawback of the classical LMS is its early convergence. The proposed Takagi-Sugeno-Kang (TSK)-based adaptive reweighted L_1 norm adaption-based NLMS (TSK-ARNA-NLMS) belongs to an extended family of the ZA-LMS, and its control scheme is simpler than that of the LMS in terms of computational complexity, but it is relatively complex to analyze.

The DVR control is combined with two fractional-order PID (FOPID) controllers for DC- and AC-link voltage stabilization and regulation. The FOPID has more flexibility in tuning the system coefficients than the classical PID controller owing to its five degrees of freedom. The benefits of implementing the FOPID include less overshoot, low oscillations, and robustness against disturbances. Tuning of the FOPID controller coefficients is a challenging task; therefore, metaheuristic techniques such as BBO^[20], particle swarm optimization (PSO), genetic algorithm (GA)^[21], and artificial bee colony (ABC)^[22] have been employed for FOPID coefficient tuning. This reduces the computational effort and error, with a better response to the time characteristics. The optimal coefficient of the FOPID was obtained using the integral time absolute error (ITAE) as a performance index. The aforementioned metaheuristic techniques have the disadvantages of local stagnation, premature convergence, difficulty in choosing controller coefficients, and extended computational time^[23]. Therefore, parameter settings are made using an advanced algorithm, namely, the ant lion optimization (ALO)-based FOPID, which converges around the optima and has fewer algorithm-specific parameters to obtain the gain coefficients in less evaluation time. It also avoids local entrapment and achieves an objective function by optimizing the search space^[24].

In this study, a TSK-ARNA-based NLMS with three single-phase VSC-based control strategies was recommended. The main motivation for using unipolar

modulation instead of bipolar switching is to reduce switching losses because it has a lower switching frequency and low-voltage ripples than bipolar switching. The proposed TSK-based ARNA-NLMS control scheme was developed to update and select the optimal step-size coefficient. This resolves the conflict between convergence rate and stability by determining the step size using the marine predator algorithm (MPA) heuristic method. The advantages of integrating the MPA into the proposed control algorithms are self-adjustment to improve the tracking capability, robustness against system parametric variation, and improved response under a dynamic state. The control algorithm achieved a faster convergence response with fewer static errors. The DC- and AC-link voltages were maintained constant at the set values using the FOPID-ALO control. The FOPID gains were estimated using the ALO optimization technique. Finally, the TSK-ARNA-NLMS controller evaluated the gate signal using the generated reference load voltage component. Simulation and hardware results confirmed the effectiveness of the proposed control scheme. The main contributions of this study are as follows.

(1) A 3-single phase VSC-based DVR topology was proposed for the restoration of load voltage under a highly polluted supply voltage.

(2) The trained TSK-ARNA-NLMS model accelerated the computation of the direct and quadrature weight components and leveraged the MPA to obtain the optimal step size. The forecasted optimized results were evaluated using statistical criteria.

(3) A FOPID was proposed, whose coefficients were optimized by the ALO to maintain a constant AC- and DC-link voltage at the PCC. The accuracy was measured using a performance index (ITAE).

2 System layout description

The 3P4W DVR configuration is shown in Fig. 1. The presented configuration provides the complete details of the main components connected to the DVR topology. A non sinusoidal supply voltage is introduced to represent all electrical voltage disturbances (sag, swell, imbalance, and distortion).

The connected sensitive load was modeled using a combination of resistive (R_L) and inductive (L_L) elements. The interfacing inductor (L_f) is used to filter out the ripples, and the VSC is connected through (L_f) at the PCC [4]. On the other side of the PCC, an RC filter (R_f, C_f) is provided to suppress the ripples in the voltage [5, 7].

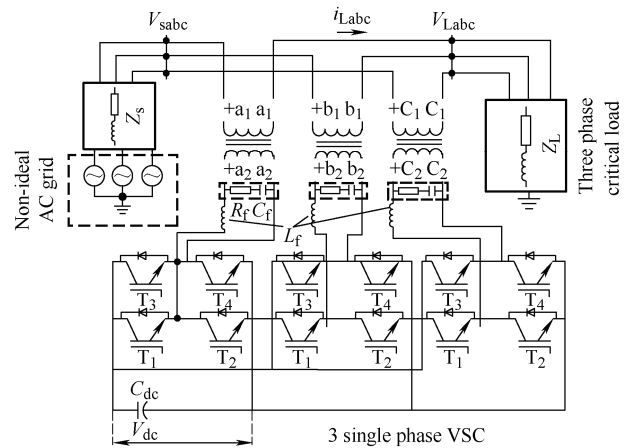


Fig. 1 Schematic of the proposed DVR

3 Control methodology

The key purpose of proposing the TSK-ARNA-NLMS controller is to resolve the contradiction between step size and convergence rate. The stability and convergence rate depend on the step size [1, 14-15]. The step size, $\lambda(z)$ depends on the operating coefficients and should be adaptively updated with the loading and dynamics present in the system to achieve a better tracking performance of the filter. Therefore, the step size needs to be automatically adjusted using the TSK unit and is optimized using the MPA technique to obtain the optimal FIS to reduce the error. This improves the estimation process for extracting the fundamental quantity from a non sinusoidal supply. The estimated fundamental components were averaged to obtain the values for the direct and quadrature units. The load voltage (V_{Labc}^*) in reference was obtained by taking the product of the load current template with the in-phase and quadrature unit vectors. The FOPID ($PI^{\nu}D^{\nu}$) coefficients were tuned using the ALO technique to control the DC and AC-link voltages. The subsequent gate pulse for the VSC was generated by processing the extracted error via pulse-width modulation (PWM). The complete TSK-ARNA-NLMS with FOPID-ALO control is shown in Fig. 2. A

detailed description of the mathematical formulation is provided in the following section.

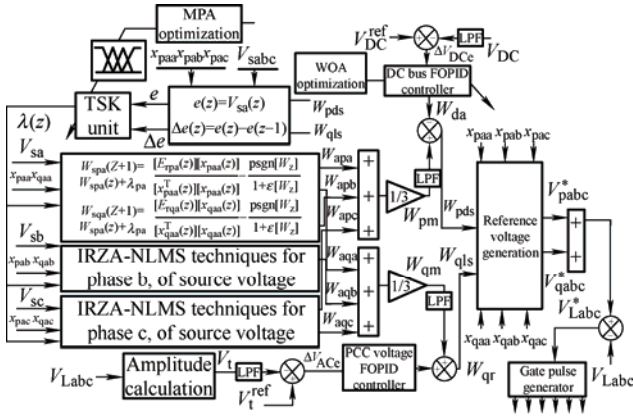


Fig. 2 Control architecture of combined TSK-ARNA-NLMS algorithm

3.1 Extraction of fundamental weight components of active power

Initially, the fundamental component extraction was derived using the active unit template components (x_{paa} , x_{pab} , and x_{pac}) for the three phases, where the magnitude of the load current (i_{lp}) is computed as follows

$$i_{lp} = \sqrt{0.67(i_{lpa}^2 + i_{lpb}^2 + i_{lpc}^2)} \quad (1)$$

The unit template in-phase component $x(z)$ is computed as

$$\begin{cases} x_{paa}(z) = \frac{i_{la}}{i_{lp}} \\ x_{pab}(z) = \frac{i_{lb}}{i_{lp}} \\ x_{pac}(z) = \frac{i_{lc}}{i_{lp}} \end{cases} \quad (2)$$

Electrical voltage disturbances, such as imbalance or distortion, are caused by positive, negative, and zero sequences, along with harmonic components in a three-phase system [21-22].

$$v_{st}(t) = v_{st+}(t) + v_{st-}(t) + v_{st0}(t) + \sum v_{sh}(t) \quad (3)$$

The components of Eq. (4) represent the positive, negative, and zero sequences, respectively, denoted by the subscripts +, -, and 0. The fundamental components of the system voltage frequency are the positive, negative, and zero sequences.

$$\begin{cases} v_{st+} = v_{st+}(\omega t + \phi_+) \\ v_{st-} = v_{st-}(\omega t + \phi_-) \\ v_{st0} = v_{st0}(\omega t + \phi_0) \end{cases} \quad (4)$$

$$\sum v_h(t) = \sum v_h \sin(\omega t + \phi_h) \quad (5)$$

Normally, the voltage at the load terminal is expected to be sinusoidal with set amplitude V_1 .

$$V_1(t) = V_1 \sin(\omega t + \phi_+) \quad (6)$$

Hence, series-based converters must compensate for the following components.

$$V_{dvr}(t) = V_L(t) - V_{st}(t) =$$

$$(V_L - V_{st+})\sin(\omega t + \phi_+) - V_{st-}(t) - V_{st0}(t) - \sum V_h(t) \quad (7)$$

The TSK-ARNA-NLMS control algorithm determines the fundamental weight quantity of the active and reactive from the non-sinusoidal input voltage at the z^{th} sample, which is computed as

$$V_{sa} = V_{sf} \sin(\omega z + \phi_f) + \sum_{h=5,7}^{\infty} V_{sh} \sin(h\omega z + \phi_h) \quad (8)$$

Periodic and disturbed voltages can be expressed by the algebraic summation of the fundamental quantity of frequency and multiple quantities of the fundamental frequency.

$$V_{s\alpha}(z) = V_{sf}(z) + V_{sh}(z) \quad (9)$$

The voltage corresponding to the fundamental and harmonic components of the supply voltage can be expressed as

$$V_{sf}(z) = [a_q \sin \omega z + b_q \cos \omega z] \quad (10)$$

where ω represents the frequency of the q^{th} component, $a_q = V_{sf} \cos \phi_q$, $b_q = V_{sf} \sin \phi_q$. Then, Eq. (10) can be written as

$$V_{sf}(z) = V_{sf} \sin \omega z \cos \phi_f + V_{sf} \cos \omega z \sin \phi_f \quad (11)$$

$$V_{sf}(z) = V_{sf} \sin \omega z \cos \phi_f + V_{sf} \cos \omega z \sin \phi_f \quad (12)$$

The $V_{sf} \cos \phi_f$, $V_{sf} \sin \phi_f$, $V_{sh} \cos \phi_f$, and $V_{sh} \sin \phi_f$ terms are replaced with w_{a1} , w_{b1} , w_{ah} , and w_{bh} .

The new equations can be written as follows

$$V_{sf}(z) = w_{a1} \sin \omega z + w_{b1} \cos \omega z \quad (13)$$

$$v_{sh}(z) = w_{ah} \sinh \omega z + w_{bh} \cosh \omega z \quad (14)$$

where 'h' represents the order of the harmonic. The estimated grid voltage is computed from the optimal weight values (w_{a1} , w_{b1} , w_{ah} , and w_{bh}), and is expressed as

$$V_{est}(z) = \mathbf{w}^T \mathbf{x}(z) \quad (15)$$

where $\mathbf{x}(z)$ is the input vector and is represented as $\mathbf{x}(z) = [\sin \omega z \cos \omega z \sin 5\omega z \cos 5\omega z \dots]^T$, which represents the unit template of the in-phase and quadrature

components, $[y_{pa} \ y_{qa} \ y_{5pa} \ y_{5qa}]^T$. The unit quadrature vector components are described in the following subsection. The weight vector \mathbf{w}^T is defined as follows

$$\mathbf{w}^T = [w_{a1} \ w_{b1} \ w_{a5} \ w_{b5} \ \dots] \quad (16)$$

The error $E_{rpa}(z)$ between the actual source voltage and the estimated voltage component is computed as

$$E_{rpa}(z) = V_s(z) - \mathbf{w}^T x(z) \quad (17)$$

The cost function of MSE $\varepsilon[E_{rpa}^2(z)]$ is minimized by optimal weight selection. These weights are continuously optimized by the proposed TSK adaptive reweighted L_1 norm adaption-based LMS control algorithm and are estimated at the $(z+1)^{th}$ instant expressed in Eq. (18)

$$w_{sp}(z+1) = w_{spa}(z) + \lambda_{TSK-ARNA-NLMS} E_r(z)x(z) - \left\| \rho \xi_{RL_1}(w) \right\| \quad (18)$$

L_1 norm with respect to 'w'

$$\xi_{RL_1}(w) = \frac{\text{sgn}|\hat{W}(z)|}{1 + \varepsilon|\hat{W}(z)|} \quad (19)$$

where $\|\bullet\|$ represents the l_1 norm and ε is a positive constant. w_{spa} denotes the reference active weight component. The updated value λ_{pa} is taken from the TSK-MPA intelligent algorithm for weight updating. This intelligent TSK unit is optimized by the MPA to achieve the optimal step size, which overcomes the contradiction between stability, accuracy, and convergence rate.

3.2 TSK acclimation and development of a Sugeno model based on the marine predator algorithm

The proposed TSK unit is a multilayer network, and no defuzzification is required to obtain the output response. Circle and square nodes were incorporated into the network to reflect the adaptive behavior of the model. The input-output data sets were mapped by employing triangular and trapezoidal membership functions (MFs), and their behavior depended on specific components. The main task was to automatically tune the MFs to adapt to uncertain variations in the system and achieve an optimal FIS. This is obtained using MPA learning, which avoids the arbitrary selection of MFs that alters the accuracy of the target's response. The TSK unit was fed with two

input signals, represented as

$$\begin{cases} x_1 = e(z) = V_s(z) - \mathbf{w}^T x(z) \\ x_2 = \Delta e(z) \end{cases}$$

The detailed layer-wise function is as follows.

Layer 1 feeds two inputs x_1 and x_2 and each input is assigned three MFs. The nodes or MFs are designated as square brackets and their internal control structures are represented by trapezoidal and triangular MFs. This process is called fuzzification, and the equations used to represent each MF are Eq. (20).

$$\begin{cases} \mu_{A1}(x_1) = \mu_{B1}(x_2) = \begin{cases} 0 & x > m_1 \\ \frac{m_1 - x}{m_1 - n_1} & n_1 \leq x \leq m_1 \\ 1 & x < n_1 \end{cases} \\ \mu_{A2}(x_1) = \mu_{B2}(x_2) = \begin{cases} 0 & x \leq m_1 \\ \frac{x - n_2}{p - n_2} & n_2 < x \leq p \\ \frac{m_2 - x}{m_2 - p} & p < x \leq m_2 \\ 0 & x \geq m_2 \end{cases} \\ \mu_{A3}(x_1) = \mu_{B3}(x_2) = \begin{cases} 0 & x < m_1 \\ \frac{x - m_3}{n_3 - m_3} & m_3 \leq x \leq n_3 \\ 1 & x > n_3 \end{cases} \end{cases} \quad (20)$$

Layer 2 multiplies the incoming voltage signals at each node and follows the fuzzy rules. Each node forwards the output resulting from the next node and has weights W_1 and W_2 .

$$w_z = \mu_{Aij}(x_1)\mu_{Bij}(x_2) \quad (21)$$

Layer 3 collects the training dataset from the controller and implements the learning algorithm. This includes the detection and correction of errors during training.

$$\bar{w}_z = \frac{w_z}{w_1 + w_1 + \dots + w_9} \quad (22)$$

where $\{m_i, n_i\}$ are parameter sets and $i=1, 2,$ and 3 . These antecedent parameters $\{m_i, n_i\}$ change the error value and desired linguistic variables created for each selected MF.

Layer 4 involves the defuzzification process, and the following parameters are used to derive the inference rule

$$O_4 = \overline{w_z} f_z = \overline{w_z} (b_{01}x_1 + b_{02}x_2 + r_1) \quad (23)$$

where b_{01} , b_{02} and r_1 are the consequence parameters. As a result of the learned neurons being inserted into Layer 5, an optimal output is produced as the desired signal. Consequently, the TSK model supervises the absolute control strategy.

$$Y = O_5 = \sum_{z=1}^9 \overline{w_z} f \quad (24)$$

The overall output signal from TSK optimization begins with the initial assigned random weight value and is continuously updated under an iterative process. The updated weight vector changes the value function estimated using the collected training dataset. Finally, the summing node evaluates the deterministic value in terms of the positive and negative weighted neurons in the interconnected layer. Possible linguistic rules are listed in Tab. 1. The inference engine system incorporates these rules to achieve the required target output. The proposed TSK unit was employed to evaluate the step size $\lambda(z)$ and determine the optimal weight required for the ARNA-LMS algorithm to reduce error. The convergence curve of TSK for step size selection ' λ ' is depicted in Fig. 3. The fuzzy subset for inputs e and Δe is determined using three variables (NB, Z, and PB) and $3 \times 3 = 9$ rules to emphasize the capability of the TSK controller. Tab. 2 describes the performance evaluation tools for the TSK unit used for step size tuning.

Tab. 1 Rule base with 9 rules

Δe	e		
	NB	Z	PB
NB	NB	NB	Z
Z	NB	Z	PB
PB	Z	PB	PB

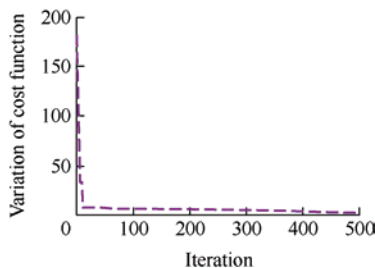


Fig. 3 Convergence curve of TSK for step size selection ' λ '

Tab. 2 Statistical performance tools of TSK unit for step size (λ) selection

Statistical tools	MSE	RMSE	ME	SD	R
Train TSK	7.027 2	2.650 9	-0.275 26	2.636 6	0.999 93
Test TSK	7.045 5	2.654 3	-0.319 4	2.635 2	0.999 93

3.3 Implementation of MPA-TSK optimization

The error signal $e(z)$ was optimized by learning the antecedent and consequence parameters of the TSK unit using the MPA technique. Statistical metrics, namely, mean square error (MSE), root mean square error (RMSE), standard deviation (SD), mean error (ME), and coefficients of correlation (R), were considered to investigate the accuracy of the prediction results of the TSK-MPA. The MPA is mainly inspired by the foraging mechanism of predators in the ocean and simulates this mechanism by employing Levy- and Brownian-based movements between the predator and prey. These are adopted by ocean predators to determine the optimal foraging in terms of the FIS parameter of the TKS unit. The step size $\lambda(z)$ was computed using the TSK structure based on the ARNA-LMS method expressed in Eq. (20). This estimates the active fundamental component for phase 'a' at the $(Z+1)^{\text{th}}$ instant. It is estimated as

$$w_{\text{spa}}(z+1) = w_{\text{spa}}(z) + \lambda_{\text{pa}} \frac{E_{\text{rpa}}(z)x(z)}{x^{\text{T}}(z)x(z)} - \left\| \rho_{\xi_{\text{RL}_1}}^{\xi}(w) \right\|_{\parallel} \quad (25)$$

Similarly, active components for phase 'b' and phase 'c' are represented as

$$w_{\text{s pb}}(z+1) = w_{\text{s pb}}(z) + \lambda_{\text{pb}} \frac{E_{\text{rpb}}(z)x(z)}{x^{\text{T}}(z)x(z)} - \left\| \rho_{\xi_{\text{RL}_1}}^{\xi}(w) \right\|_{\parallel} \quad (26)$$

$$w_{\text{s pc}}(z+1) = w_{\text{s pc}}(z) + \lambda_{\text{pc}} \frac{E_{\text{rpc}}(z)x(z)}{x^{\text{T}}(z)x(z)} - \left\| \rho_{\xi_{\text{RL}_1}}^{\xi}(w) \right\|_{\parallel} \quad (27)$$

where E_{rpa} , E_{rpb} , and E_{rpc} represent the actual components of the error vectors of phases a, b, and c, respectively, for the recommended TSK-ARNA-NLMS-based controller. The mean output value corresponding to the fundamental active power weight components is computed using Eqs. (25)-(27).

$$w_{pm} = \frac{w_{spa} + w_{spb} + w_{spc}}{3} \quad (28)$$

Similarly, the reactive power weight components extracted for phases a, b, and c were computed as follows

$$w_{sqa}(z+1) = w_{sqa}(z) + \lambda_{qa} \frac{E_{rqa}(z)x(z)}{x^T(z)x(z)} - \|\rho_{\xi_{RL_1}}^{\xi}(w)\| \quad (29)$$

$$w_{sqb}(z+1) = w_{sqb}(z) + \lambda_{qb} \frac{E_{rqb}(z)x(z)}{x^T(z)x(z)} - \|\rho_{\xi_{RL_1}}^{\xi}(w)\| \quad (30)$$

$$w_{sqc}(z+1) = w_{sqc}(z) + \lambda_{qc} \frac{E_{rqc}(z)x(z)}{x^T(z)x(z)} - \|\rho_{\xi_{RL_1}}^{\xi}(w)\| \quad (31)$$

where x_{qaa} , x_{qab} , and x_{qac} are quadrature components computed using Eq. (32).

$$\begin{cases} x_{qaa} = 0.577(x_{pac} - x_{pab}) \\ x_{qab} = 0.577(3x_{pac} + x_{pab} - x_{pac}) \\ x_{qac} = 0.289(-3x_{pac} + x_{pab} - x_{pac}) \end{cases} \quad (32)$$

Because the weights are adaptive, they are updated continuously. Therefore, a precise value is obtained by taking the average value of each phase of the reactive weight (w_{qm}), which is evaluated as

$$w_{qm} = \frac{w_{sqa} + w_{sqb} + w_{sqc}}{3} \quad (33)$$

4 FOPID controller-based voltage regulation

A significant improvement in the voltage at the DC and AC buses is achieved by maintaining the voltage fluctuations within an acceptable level if the actual voltage value is kept closer to the set value. This minimizes the error level by implementing the intelligent FOPID integrated with ALO. The ALO meta-algorithm reduces the computational effort required for fine-tuning to achieve optimal coefficients, and it accurately determines the active and reactive estimated load components.

The FOPID controller is operated by feeding error components (V_{DCe}) and regulates the voltage error to enhance tracking to reach to the reference level. The error values of the DC-link voltage are expressed as follows

$$\Delta V_{DCe} = V_{DC}^{ref} - V_{DC} \quad (34)$$

The FOPID controller response (w_{da}) is subtracted from the mean of the direct component (w_{pm}) to obtain the desired active fundamental (w_{pds}), as shown in

Eq. (35).

$$w_{pds} = w_{pm} - w_{da} \quad (35)$$

The terminal voltage (V_t) is computed using Eq. (37) and compared with the reference value (V_t^{ref}). The estimated error values are used as inputs to the FOPID to maintain the desired voltage.

$$\Delta V_{ACe} = V_t^{ref} - V_t \quad (36)$$

$$V_t = \sqrt{0.667(V_{LA}^2 + V_{LB}^2 + V_{LC}^2)} \quad (37)$$

The outcome of the FOPID (w_{qr}) is the sum of the quadrature mean value (w_{qm}) required to achieve the desired quadrature fundamental quantity (w_{qls}), as shown in Eq. (38).

$$w_{qls} = w_{qm} + w_{qr} \quad (38)$$

The template vectors in Eqs. (2) and (32) are implemented to estimate the reference direct and quadrature elements.

$$\begin{cases} V_{pabc}^* = w_{pds}x_{paa} + w_{pds}x_{pab} + w_{pds}x_{pac} \\ V_{qabc}^* = w_{qls}x_{qaa} + w_{qls}x_{qab} + w_{qls}x_{qac} \end{cases} \quad (39)$$

The terms W_{pds} and W_{qls} are the adaptive direct and quadrature quantities, respectively. These weights are used to determine the reference quantity of the load-side voltage. The reference load voltage is derived using Eq. (40).

$$V_{Labc}^* = V_{pabc}^* + V_{qabc}^* \quad (40)$$

4.1 FOPID controller approximation and its gains optimization using ALO

The FOPID is employed as the DC and AC bus voltage regulator for the proposed system. The FOPID control is an extended-family member of classical PID controllers. This control mechanism has five degrees of freedom (K_i , K_d , K_p , γ , and μ) and therefore, compared with the classical PID controller, this feature offers more flexibility and robustness in the tuning of FOPID coefficients by employing the fractional calculus^[20]. The mathematical time-domain representation of the FOPID controller can be defined as

$$u(t) = K_p e(t) + K_i D^{-\gamma} e(t) + K_D D^{\mu} e(t) \quad \gamma, \mu > 0 \quad (41)$$

The Laplace form of Eq. (41) is obtained as

$$C(s) = K_p + K_i s^{-\gamma} + K_D s^{\mu} \quad (42)$$

where K_p , K_i , K_D , γ , and μ represent the tunable coefficients of the FOPID controllers. The performance of the FOPID controller can be assessed by considering the ITAE as an objective function [22]. To simplify the FOPID control scheme, a well-known approximation, the CRONE approximation, was proposed by Oustaloup. This approximation is realized by considering the 'N' zeros and 'P' poles and results in an integer-order approximation function [23]. To simplify the fractional functions, infinite zeros and poles were added to the system to convert them into an integral order. Oustaloup provides an approximation for a fractional-order differentiator S^β , which is expressed by Eq. (43).

$$S^\beta = G \prod_{k=1}^K \frac{1 + (s/w_{zm})}{1 + (s/w_{pm})} \quad \beta > 0 \quad (43)$$

where K denotes the adjustable transient gain. M is the approximation order. w_{zm} and w_{pm} denote the approximated frequencies of the poles and zeros, respectively, and their values lie in the interval of the w_l (lower) and w_h (higher) transitional frequency fitting range. Here, ALO optimization is integrated with the FOPID controller to fine-tune the coefficients, which accelerates the time-response characteristics of the systems in terms of rise time (t_r), settling time (t_s), and overshoot (M_p).

4.2 ITAE as an objective function

The ITAE evaluation metric was used to measure the performance of the designed ALO-FOPID controller.

$$\text{Cost function } (J) = \text{ITAE} = \int_0^{t_{\text{sim}}} t \cdot |e_a(t)| \cdot dt \quad (44)$$

where, $e_a(t) = \max |r(t) - z_{\text{act}}(t)|$ denotes the computed value of the error between the reference and actual measured voltages. The term t_{sim} is the computation

$$S(k) = [0, \text{cumsum}(2r(k_1) - 1), \text{cumsum}(2r(k_2) - 1), \dots, \text{cumsum}(2r(k_n) - 1)] \quad (46)$$

where k represents the random stroll, cumsum denotes the cumulative sum, n is the max iterations, $r(k)$ is the stochastic, and

$$r(k) = \begin{cases} 1 & \text{rand} > 0.5 \\ 0 & \text{rand} \leq 0.5 \end{cases} \quad (47)$$

where 'rand' represents the randomly generated number in the interval [0, 1]. The locations of the ant

runtime. The controller design is a constrained optimization-based problem with the $(PI^\gamma D^\mu)$ controller parameter boundaries supplied as constraints given as

$$\begin{cases} K_{pi}^{\min} \leq K_{pi} \leq K_{pi}^{\max} \\ K_{Di}^{\min} \leq K_{Di} \leq K_{Di}^{\max} \\ \mu_{pi}^{\min} \leq \mu_{pi} \leq \mu_{pi}^{\max} \\ \gamma_{li}^{\min} \leq \gamma_{li} \leq \gamma_{li}^{\max} \end{cases} \quad (45)$$

where K_{pi}^{\min} , K_{Di}^{\min} , and μ_{pi}^{\min} represents the lower values, K_{pi}^{\max} , K_{Di}^{\max} , and μ_{pi}^{\max} indicates the upper values of the controller parameters. γ_{li}^{\min} , μ_{pi}^{\min} , γ_{li}^{\max} , and μ_{pi}^{\max} are the lower and upper range values of integral and derivative order.

4.3 Implementation of ALO-FOPID gain optimization

The control parameters (K_p , K_i , K_d , γ , μ) are tuned by minimizing the selected performance index (ITAE). To optimize the controller and gain values, the ALO technique was employed to obtain the optimal values of the FOPID. The proposed ALO is summarized in the following five steps that describe the hunting strategy of the antlion: ① randomized walking of ants, ② building the traps, ③ entrapping of ants, ④ catching prey, and ⑤ rebuilding the traps for hunting. Detailed mathematical modeling of the hunting behavior and entrapment is described in reference [29]. The gain tuning of the FOPID coefficients using ALO is illustrated in the block diagram in Fig. 4. The ALO was employed for FOPID gain tuning, and the error quantity was used to derive the cost function defined in Eq. (45). The basic ALO equations are based on ant and ant lion behaviors. Random walk was selected to model the ant motion and search for food based on stochastic movement.

(A) and antlion (A_k) were modeled using the matrices M_{ant} and M_{antlion} , respectively. The terms $A_{i,j}$ and $Ak_{i,j}$ represent the location of i^{th} ant and antlion, respectively, in the j^{th} dimension.

$$\begin{cases} M_{\text{ant}} = [A_{i,j}] \\ M_{\text{antlion}} = [Ak_{i,j}] \end{cases} \quad (48)$$

where $i=1, 2, \dots, n/m$, $j=1, 2, \dots, d$, n is the ant

number, m is the antlion number, and d is the dimension of the variables considered. The cost functions defined in Eqs. (49)-(50) was used to evaluate the fitness at each iteration of the optimization process. The terms M_{Fita} and M_{Fital} are the matrix representations of each ant and antlion for saving the cost function, respectively, and are expressed as

$$M_{\text{Fita}} = [f(A_{i,j})] \quad (49)$$

$$M_{\text{Fital}} = [f(Ak_{i,j})] \quad (50)$$

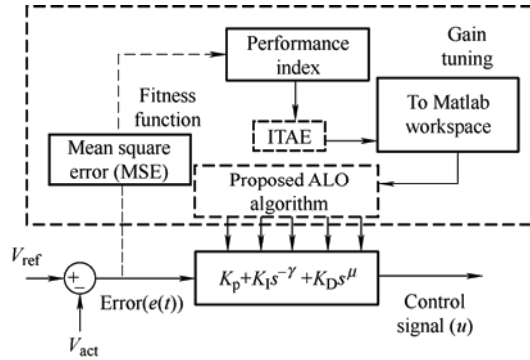


Fig. 4 Proposed gain tuning of DC and AC FOPID controller optimized with ALO

The mathematical modeling with boundaries for random search space is given as

$$X_i^k = \frac{(X_i^k - A_i) \times (D_i^k - C_i^k)}{D_i^k - A_i} \quad (51)$$

$$\begin{cases} C_i^k = Ak_j^k + C^k \\ D_i^k = Ak_j^k + D^k \end{cases} \quad (52)$$

A roulette wheel was used to select the optimum antlion. Complete entrapment is performed by the antlion, which slides the sand to the pit center when it realizes that the ant is in a trap or pit and tries to escape from the trap. This trapping is performed by throwing the sand into the pit using its mouths, and this mechanism leads to the reduction of the ratio pit, as expressed by Eq. (53).

$$\begin{cases} C^k = \frac{C^k}{M} \\ D^k = \frac{D^k}{M} \end{cases} \quad (53)$$

Here, $M = 10^w \times (k/l)$, where k is the current iteration, l is the total number of iterations, and w is an iteration-dependent constant. The final stage of hunting the prey is mathematically expressed as Eq. (54)

$$Ak_j^k = A_i^k \quad f(A_i^k) > f(Ak_i^k) \quad (54)$$

Here, antlion eats the ant when an ant is a fitter compared to antlions means ants are trapped by pulling the sand into the pit. This equation indicates that the new location of the antlion was updated for the next hunt. During the optimization process for finding the coefficients of the FOPID controller, the parameter values considered are as follows: population size is equal to 12, maximum trials are 50, and total number of variables equal to 10 (K_{p1} , K_{i1} , K_{D1} , γ_1 , μ_1 and K_{p2} , K_{i2} , K_{D2} , γ_2 , μ_2). The tuning performance of the FOPID controller is illustrated in Fig. 5. Fig. 5a shows that the ALO-FOPID cost function converges to 43.233. Figs. 5b-5f depict the performance characteristics for the estimation of the FOPID controller gain coefficients. The recorded and converged responses of the DC-link voltage with the tuned value of the FOPID controller gain coefficient obtained using a trial-and-error approach are listed in Tab. 3. The estimated value of the FOPID using ALO outperformed the others, and it was observed that the error regulation was

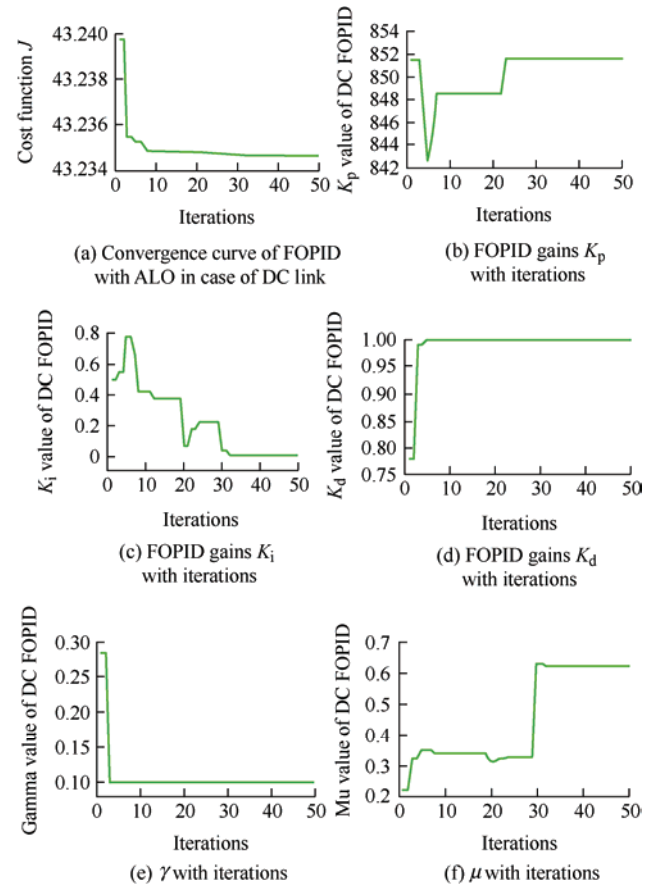


Fig. 5 Tuning performance of FOPID controller

better for DC-link voltage. Similarly, the AC-FOPID controller gain coefficient (K_{i2} , K_{d2} ,

K_{p2} , γ_2 , and μ_2) variations against iterations are presented in Tab. 3.

Tab. 3 DC-link FOPID gain tuning response

Methods	Parameters									
	DC FOPID controller					AC FOPID controller				
	K_{p1}	K_{i1}	K_{d1}	γ_1	μ	K_{p2}	K_{i2}	K_{d2}	γ_2	μ_2
FOPID (Trial & Error)	500	0.8	0.5	0.4	0.5	1	13.8	0.1	1	1
ALO-POPID	851	0.01	1	0.1	0.6213	0.0237	11.8483	2.5684	0.9438	0.9547

4.4 Reference estimation and gate pulses

The required load quantity of the reference voltage (V_{Labc}^*) was evaluated by summing the direct (V_{pabc}^*) and quadrature (V_{qabc}^*) reference components

$$V_{Labc}^* = V_{pabc}^* + V_{qabc}^* \quad (55)$$

The switching signals for the three-phase VSC were evaluated by processing the errors generated by comparing the reference value of load voltage (V_{Labc}^*) with the actual measured load voltage (V_{Labc}).

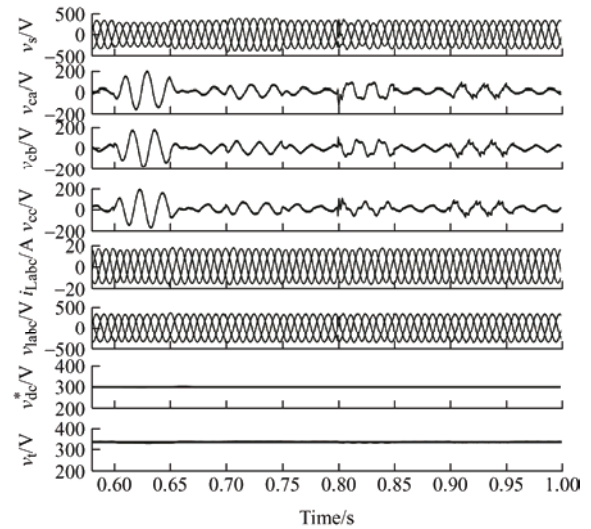
5 Results and discussions

The Matlab environment was employed for the integrated TSK-ARNA-LMS control algorithm of a 3 single-phase DVR system. The DVR model was simulated using a sampling time (t_s) of 20 μ s. The optimized TSK-ARNA-NLMS-based controller was corroborated under all considered voltage imperfections in the grid. The performance was analyzed by employing the DVR compensator and explained using the simulation and experimental responses.

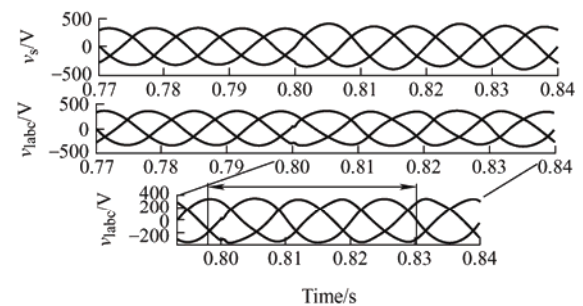
5.1 Voltage disturbance compensation capability of DVR using RNA-NLMS-based algorithm

All of the aforementioned voltage concerns in the supply voltage are considered when analyzing the overall DVR effectiveness in Fig. 6. The supply voltage (v_s) is shown in Fig. 6a with disturbances such as voltage sag, swelling, distortions, and unbalances. The subplots (2)-(4) represent the three-phase compensation voltages (V_{ca}), (V_{cb}), and (V_{cc}), respectively. Subplot (5) presents the sinusoidal and balanced load currents (i_{Labc}). Subplot (6) in Fig. 6a represents the constant load voltage (V_{labc}) irrespective of imperfections in the supply. The wave signals of the DC-link (V_{dc}) and AC-link voltages (V_t) are shown in subplots (7) and (8) of Fig. 6a. From Fig. 6a, it is observed

that the magnitudes of the terminal voltage (V_t) and DC-link voltage (V_{DC}) are retained at their set levels, even during dynamic scenarios. Fig. 6a shows that the integrated TSK-ARNA-NLMS-based control effectively compensates for the above-mentioned voltage PQ electrical disturbances created in the supply voltage, and the corresponding load voltage reaches the required level. Fig. 6b illustrates the DVR response during the voltage-imbalance state of the supply voltage. Here the learning rate is selected by the TKS optimized unit and shows that the DVR takes 0.03 s for compensating the voltage imbalance from the supply.



(a) DVR simulation responses with optimized TSK-ARNA-NLMS control algorithm



(b) DVR response in the case of unbalanced voltage with TSK-ARNA-NLMS

Fig. 6 Compensation of voltage-based power quality problem

5.2 Power quality harmonics analysis of DVR under distorted supply voltage

The injected harmonics is analyzed for phase ‘a’ in the supply voltage which helps to retain the harmonics at load under an acceptable limit of 5%.

Thus, the spectral analysis of the distortions in the case of the supply (v_s) and load voltage components (v_l) is depicted under a steady scenario. Fig. 7 shows the load voltage THD after compensation with the optimized TSK-ARNA-NLMS control; its value is 3.93%. Tab. 4 and Fig. 8 provide a detailed comparative analysis of the DC link behavior after evaluating the dynamic performance indicators, such as the rise and settling times with overshoot. The statistical values are listed in Tab. 4 and Fig. 8. The performance indices confirm that the proposed TKS-ARNA-LMS requires less time to stabilize with small oscillations and prove its performance effectiveness and efficacy.

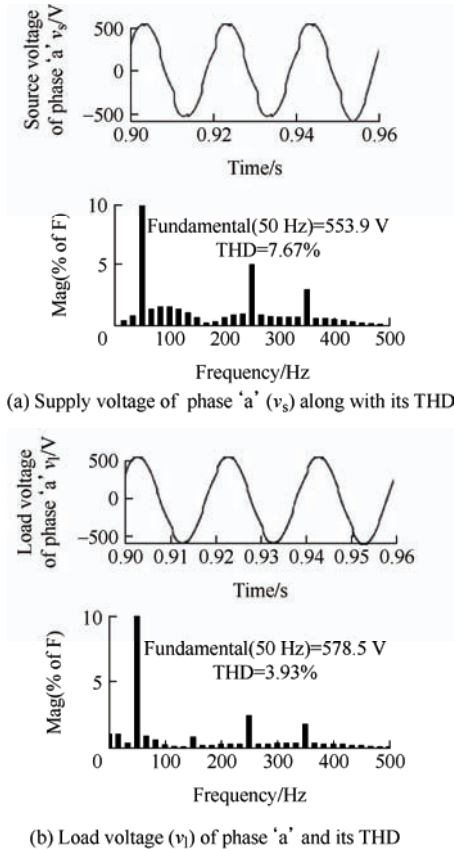


Fig. 7 Harmonics FFT during voltage distortions in supply-side voltage

Tab. 4 Comparative performance of the TSK-ARNA-NLMS-BASED algorithm with SRF-PI

Operation	SRF-PI	TSK-ARNA-NLMS-FOPID
Type	Parks transformation	Adaptive filter
PLL requirement	Required	Not required
Complexity	More	Less
Order of optimization	NA	Second
Oscillation during transient	High	Low
Step size μ	Not required	Variable
Sampling time	20 μ s, fixed step	20 μ s, variable step
Rise time T_r /s	0.094	0.089
Settling time/s	0.28	0.15
Overshoot M_p (%)	9.3	5
Accuracy	Moderate	Better
Static error	High	Low
Dynamic oscillations	High	Low

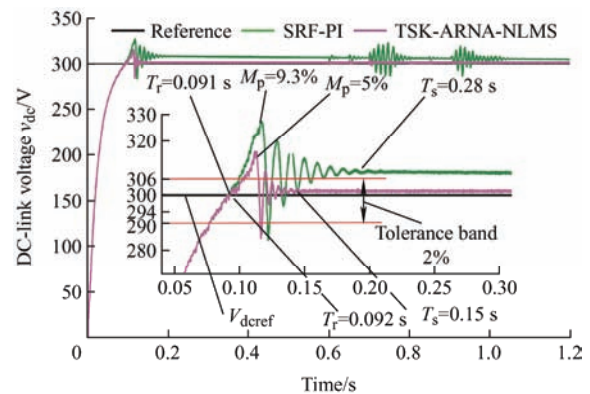


Fig. 8 Dynamics of DC-link voltage using SRF-PI and TSK-ARNA-NLMS-FOPID

6 Experimental performance

The proposed TSK-ARNA-NLMS-based DVR control algorithm was corroborated by its steady-state and dynamic nature. Using a d-SPACE-based Micro Lab Box CPU, a prototype with scaled-down factors was evaluated in the laboratory at sampling time (t_s) equal to 40 μ s to measure the accuracy of DVR with the considered PQ concerns. The dynamic results were captured with a DSOX2004A, which is four-channel-based. Fluke-43B was employed to save the results of the voltage imbalance and distortion under steady-state conditions. These experimental results were presented using a DSO display window, with half of them shown in a normal working state and the other half in a dynamic state with voltage-related PQ concerns. The simulation and experimental data

are provided in Appendix A and B.

6.1 Experimental extensive internal control signal response

The TSK-ARNA-NLMS-based control signals are shown in Fig. 9. The reference load voltage is derived at PQ events in the grid for ‘a’ phase. Fig. 9a includes

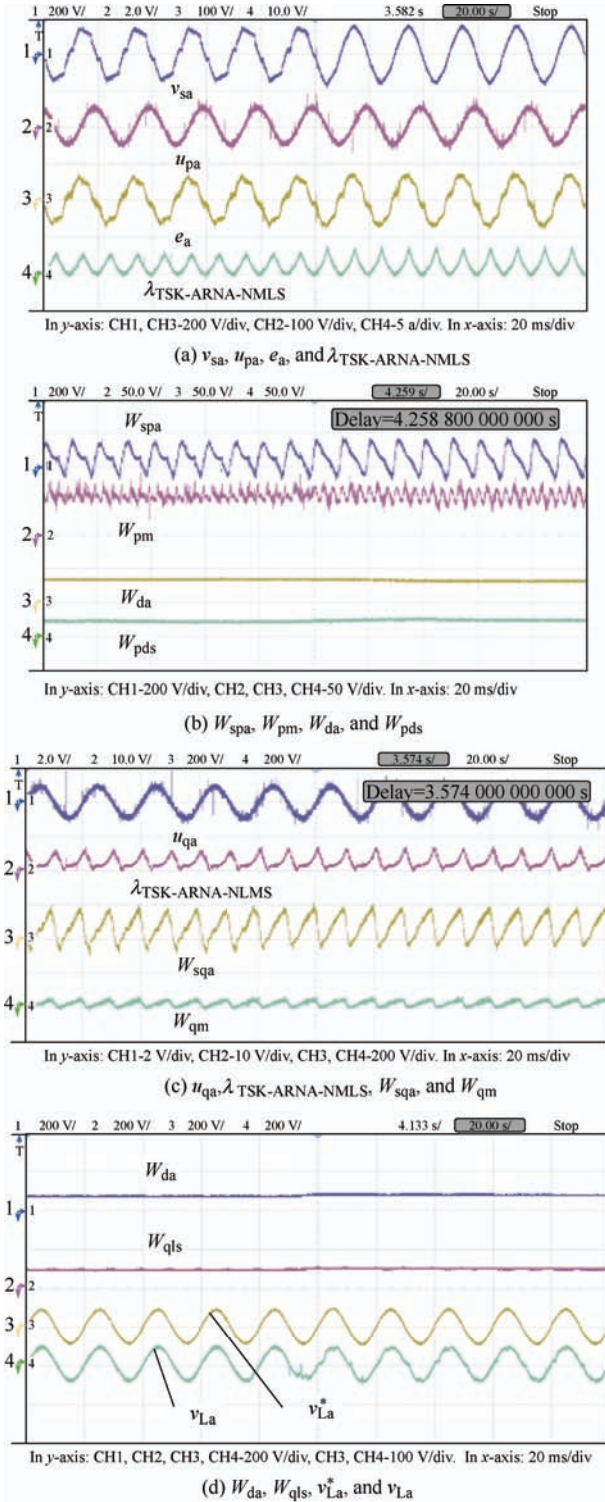


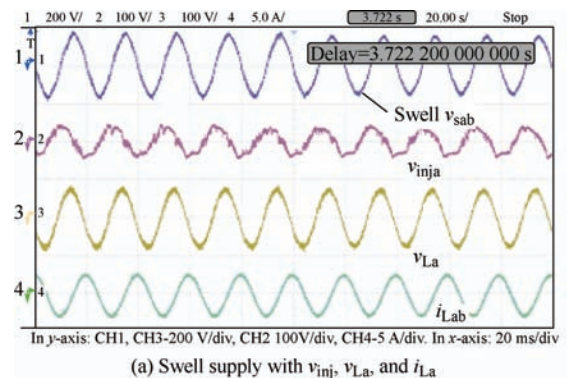
Fig. 9 Internal signals of the TSK-ARNA-NLMS-based controller for phase ‘a’ of distortion PQ event in supply

the signals of phase ‘a’: supply side like supply voltage (v_{sa}), unit weight vector of direct quantity (u_{pa}), actual computed error (e_a), and the TSK-ARNA-NLMS term, denoted by $\lambda_{\text{TSK-ARNA-NLMS}}$, which is employed in the direct weight updating under distortion state. The fundamental direct quantity (W_{spa}), mean fundamental direct quantity (W_{pm}), maintained and stabilized DC voltage (W_{da}) responses, and total direct weight (W_{pds}) are shown in Fig. 9b.

Similarly, for the reactive quantity, Fig. 9c presents the phase ‘a’ response, which includes the quadrature vector (u_{qa}) and the TSK-ARNA-NLMS term, denoted by $\lambda_{\text{TSK-ARNA-NLMS}}$, which help in the reactive weight updating, with the fundamental quadrature quantity (W_{sqa}) and mean reactive weight (W_{qm}); these are described with the dynamics during a case of distortion. The effectiveness of the control scheme is illustrated by the tracking ability of the load voltage, even at voltage imperfections in the supply, as shown in Fig. 9d. The captured waveforms show the outcome of the AC-bus voltage (W_{qr}), total quadrature weight (W_{qls}), and reference computed load-end voltage (v_{La}^*) effectively tracked with the actual measured load-side voltage (v_{La}).

6.2 Experimental DVR results with proposed methodology TSK-ARNA-NLMS

Fig. 10 shows the compensation achieved by employing the TSK-ARNA-NLMS in the case of a voltage swell. The signals illustrated in Fig. 10a are voltage swells in the supply (v_{sabc}) and the restored 110 V RMS with a balanced voltage (v_{Labc}) at the load.



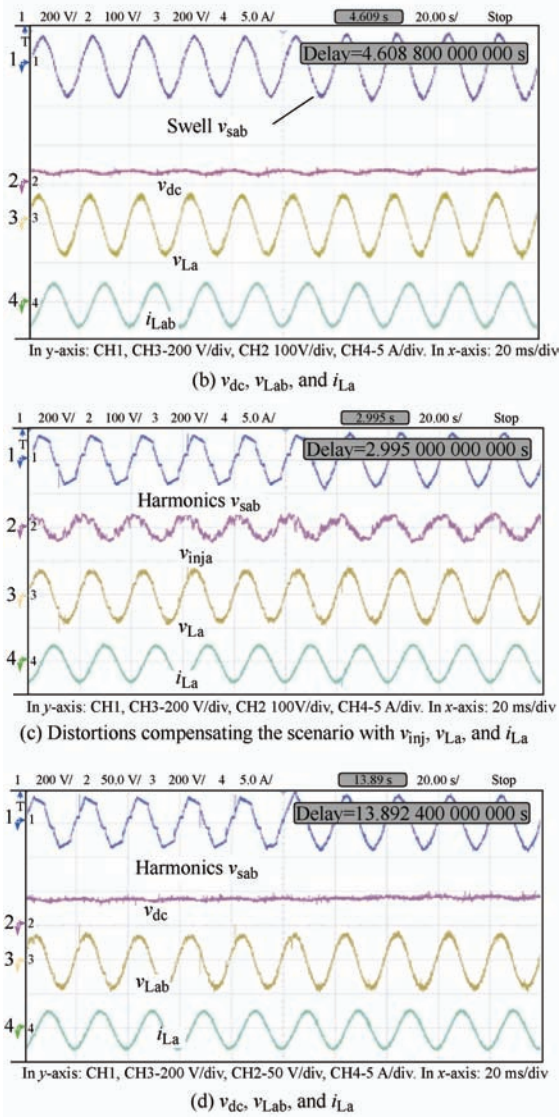
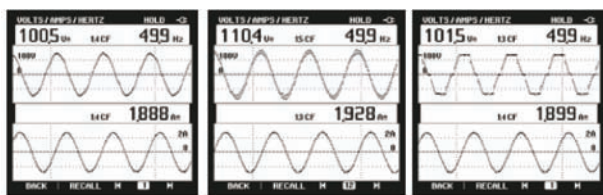


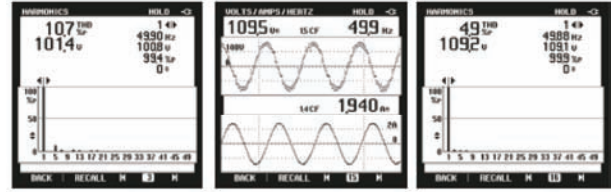
Fig. 10 Compensation behavior

6.3 Steady state performance

The steady-state results under sag and distortion are shown in Fig. 11 and their statistical values are listed in Tab. 5 for a detailed analysis. A sag of -10% that is injected into v_s is shown in Fig. 11a, and the restoration of v_1 after compensating for the voltage disturbances is demonstrated in Figs. 11b and 11e. Figs. 11c and 11d show the harmonics injected into v_s , and Fig. 11f shows that the fundamental was efficiently filtered to 4.9%.



(a) Supply side (v_{sa}) with source current (i_{sa}) (b) Voltage (v_{La}) at load side with load current (i_{La}) (c) v_{sab} and i_{sa}



(d) THD v_{sa} (e) v_{lab} and i_{la} (f) THD v_{lab}
Fig. 11 Performance experimental response under sag

Tab. 5 Experimental statistical values for steady state

Sr. No.	Parameters	Sag/V	Swell/V	Unbalance/V	Distortion with (%THD)
1	Supply voltage v_{sa}	100.5	120.8	99.2	101.4, 10.7%
2	Supply voltage v_{sb}	100.3	119.8	103.4	100.9, 10.9%
3	Supply voltage v_{sc}	100.9	121.0	112.2	102.5, 10%
4	Load voltage v_{La}	110.8	110	109.7	109.2, 4.9%
5	Load voltage v_{Lb}	110.1	110.5	110.2	109.8, 4.5%

7 Conclusions

This paper presents the implementation of an optimized TSK-ARNA-NLMS-based DVR control scheme integrated with metaheuristic techniques, namely MPA and ALO. This optimized TSK-ARNA-NLMS controller was developed to extract the fundamental weights from the polluted supply voltage, and the step size was determined by employing a TSK unit that adapts to the changes quickly, and rapidly updates the weight vector. This was used to estimate the reference load-voltage component. The meta-algorithm ALO-integrated FOPID controls the voltage stabilization and dynamics in DC- and AC-link voltage, and this intelligent control reduces the computation efforts required to tune the five coefficients (K_i , K_d , K_p , γ and μ) of the FOPID controller. The DVR enhanced the PQ performance and after compensation, the THD in load voltage was within the IEEE-519 limit. Under steady-state and unbalanced load state, the magnitude of voltages at the PCC and DC-link was maintained at their reference set levels. The compensatory time response was investigated under an imbalanced state of voltage and the capability of the DVR was observed in terms of the fast compensation of the voltage disturbance, in 0.03 s. The control experiment on a DVR prototype using d-SPACE showed satisfactory results under both operating states.

Appendix A

Voltage at supply $V_{L-L}=410$ V, 50 Hz with $15 \text{ kV} \cdot \text{A}$ load, 0.8 p.f; load current (i_L)= 21.12 A; transformer DVR rating= $5 \text{ kV} \cdot \text{A}$, 200/200 V; $V_{DC}=300$ V; $C_{DC}=1200$ μF ; filter/interface inductor (L_f)= 1.5 mH; filtering component with 6Ω of resistance (R_f) and capacitor (C_f) of $10 \mu\text{F}$. LPF with a cut of frequency= 10 Hz; sample time (t_s)= $20 \mu\text{s}$.

Appendix B

Non-sinusoidal grid supply of $110 V_{L-L}$, 50 Hz with a load of $0.352 \text{ kV} \cdot \text{A}$; load current (i_L)= 2 A; transformer rating for DVR: $4 \text{ kV} \cdot \text{A}$, 125/125 V; DC-bus voltage (V_{dc})= 35 V; DC-bus capacitor (C_{dc})= $4700 \mu\text{F}$; and interface inductor (L_f)= 1 mH; components of ripple filter: $R_f=10 \Omega$ and $C_f=120 \mu\text{F}$, sample time (t_s)= $40 \mu\text{s}$. The coefficients of the FOPID for the DC link are $k_{p1}=0.1$, $k_{i1}=0.1$, $k_{d1}=0.1$, $\gamma_1=0.1$ and $\mu_1=0.6$. AC link coefficients are $k_{p2}=0.1$, $k_{i2}=0.1$, $k_{d2}=0.1$, $\gamma_2=0.8$ and $\mu_2=0.8$.

References

- [1] A Moreno-Munoz. Power quality: Mitigation technologies in a distributed environment. London: Springer Science & Business Media, 2007.
- [2] M H Antchev. Classical and recent aspects of power system optimization. Cambridge: Academic Press, 2018.
- [3] X Mei, W Wu, X Chen. Analysis of power quality variations. *2003 IEEE PES Transmission and Distribution Conference and Exposition*, September 7-12, 2003, Dallas, TX, USA. IEEE, 2003, 1: 41-44.
- [4] R Pandya, F Runihavsar. Study on compensation of voltage sag and voltage swell by using DVR (dynamic voltage restorer). *2008 International Conference on Current Trends towards Converging Technologies (ICCTCT)*, March 1-3, 2018, Coimbatore, India. IEEE, 2018: 1-4.
- [5] N Abas, S Dilshad, A Khalid, et al. Power quality improvement using dynamic voltage restorer. *IEEE Access*, 2020, 8: 164325-164339.
- [6] S Agalar, Y A Kaplan. Power quality improvement using STS and DVR in wind energy system. *Journal of Renewable Energy*, 2018, 118: 1031-1040.
- [7] T Jimichi, H Fujita, H Akagi. Design and experimentation of a dynamic voltage restorer capable of significantly reducing an energy-storage element. *IEEE Transactions on Industry Applications*, 2008, 44(3): 817-825.
- [8] R Omar, N A Rahim. Voltage unbalanced compensation using dynamic voltage restorer based on super capacitor. *International Journal of Electrical Power and Energy Systems*, 2012, 43(1): 573-581.
- [9] S Galeshi, H Eini. Dynamic voltage restorer employing multilevel cascaded H-bridge inverter. *Journal of IET Power Electronics*, 2016, 9(1): 2196-2204.
- [10] R S Herrera, P Salmeron. Instantaneous reactive power theory: A comparative evaluation of different formulations. *IEEE Transactions on Power Delivery*, 2007, 22(1): 595-604.
- [11] B Singh, V Verma. Selective compensation of power-quality problems through active power filter by current decomposition. *IEEE Transactions on Power Delivery*, 2008, 23(2): 792-799.
- [12] M Srinivas, I Hussain, B Singh. Combined LMS-LMF-based control algorithm of DSTATCOM for power quality enhancement in distribution system. *IEEE Transactions on Industrial Electronics*, 2016, 63(7): 4160-4168.
- [13] M Badoni, A Singh, B Singh. Variable forgetting factor recursive least square control algorithm for DSTATCOM. *IEEE Transactions on Power Delivery*, 2015, 30(5): 2353-2361.
- [14] A K Panda, M Mangaraj. DSTATCOM employing hybrid neural network control technique for power quality improvement. *Journal of IET Power Electronics*, 2017, 10(4): 480-489.
- [15] P Trilochan, M Mangaraj, A K Panda, et al. Sparse LMS control algorithm for fuel cell based SAPF. *2016 IEEE International Conference on Electrical, Computer and Electronics Engineering (UPCON)*, December 9-11, 2016, Varanasi, India. IEEE, 2016: 72-77.
- [16] I D Landau, R Lozano, M M Saad, et al. Adaptive control: Algorithms in analysis and applications. Berlin: Springer-Verlag, 2011.
- [17] E Hossain, M R Tür, S Padmanaban, et al. Analysis and mitigation of power quality issues in distributed generation systems using custom power devices. *IEEE Access*, 2018, 6: 16816-16833.
- [18] B Widrow, S D Stearns. Adaptive signal processing. New Jersey: Prentice Hall, 1985.
- [19] Y Qu, W Tan, Y Dong, et al. Harmonic detection using fuzzy LMS algorithm for active power filter. *2007*

International Power Engineering Conference (IPEC 2007), December 3-6, 2007, Singapore. IEEE, 2007: 1065-1069.

- [20] M Mallareddy, D Prasad Chinta, B Rajpathak, et al. Application of bio geography based fractional order PID controller in DC motor drive speed control. *2020 IEEE International Conference on Electronics, Computing and Communication Technologies*, July 2-4, 2020, Bangalore, India. IEEE, 2020: 1-6.
- [21] S Yousaf, A Mughees, M G Khan, et al. A comparative analysis of various controller techniques for optimal control of smart nano-grid using GA and PSO algorithms. *IEEE Access*, 2020, 8: 205696-205711.
- [22] K Vanchinathan, S Narayanasamy. Adaptive fractional order PID controller tuning for brushless DC motor using artificial bee colony algorithm. *Journal of Results in Control and Optimization*, 2021, 4: 100032.
- [23] S Ekinici, A Demiroren, B Hekimoglu. Parameter optimization of power system stabilizers via kidney-inspired algorithm. *Transactions of the Institute of and Control*, 2019, 41(5): 1405-1417.
- [24] S Mirjalili. The ant lion optimizer. *Journal of Advances in Engineering Software*, 2015, 83: 80-98.



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