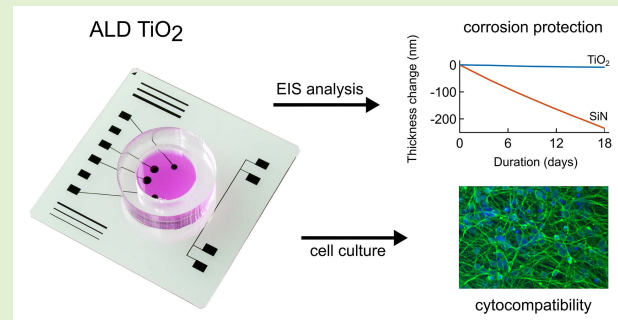


# Corrosion and Protection of Silicon Nitride Insulators in Microelectrode Array Applications

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**Abstract**—In the study of the electrophysiological activity of cultured cells, the integrity of the electrical insulation layer is crucial in ensuring the reliability of microelectrode array (MEA) sensor measurements. The materials used are required to withstand the challenging culture conditions for up to several months. The commonly used silicon nitride (SiN) has been reported to corrode in cell cultures with very little attention in the scientific community. Herein, we show that plasma-enhanced chemical vapor deposited (PECVD) SiN is subject to substantial wear when in contact with different culture media, with the corrosion rate depending on the deposition temperature and media type. A chemically stable insulator is proposed and demonstrated as an alternative for SiN using thermally annealed atomic layer deposited (ALD) titanium dioxide (TiO<sub>2</sub>) as a transparent protective layer for SiN. Electrochemical impedance spectroscopy (EIS) was successfully applied to periodically assess the integrity and thickness of the insulation layers in two different media, with profilometer measurements performed as an endpoint analysis to confirm the actual change in thickness in four solutions. ALD TiO<sub>2</sub> not only prevented insulator corrosion but also showed excellent cytocompatibility during a four-week culture with human embryonic stem cell (hESC)-derived neurons. The results here underline the need for addressing the corrosion of SiN insulation in MEA sensors. We believe that the integrity and results of cellular measurements can be compromised, especially when reusing MEAs. We propose the ALD TiO<sub>2</sub> protected insulator as a viable solution for this problem, as it increases reliability, particularly during extended cell cultures.

**Index Terms**—Atomic layer deposition, biosensor, corrosion, cytocompatibility, electrochemical impedance spectroscopy, MEA, microelectrode array, silicon nitride, titanium dioxide.



## I. INTRODUCTION

SINCE their introduction nearly five decades ago, microelectrode arrays (MEAs) have had a broad impact on studying the extracellular electrophysiological activity of neural and cardiac cells. The noninvasive measurement

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technique of field potentials enabled by surface integrated planar MEA sensors provides tools to benefit fields of developmental biology [1], [2], pathological studies [2], [3] and pharmacological research [4]–[6].

Major benefits of using MEAs include providing cultured cells with a stable proliferation platform that can be used for continuous and simultaneous monitoring of electrical activity at multiple locations without mechanically interfering with cellular functions. The spatial information attached to the recordings can be used to assess the formation, plasticity and maturation of developing networks, as well as pathological events [4]. Due to the advent of human-induced pluripotent stem cells (hiPSCs) and their derivative cell types, MEA recordings hold increasing potential in the study of genetically specific diseases and screening of drug molecules, where the effects and interactions with human cells can already be assessed during the preclinical phase [7]. These cellular models can also be used to study cardiac cells and neural plasticity in developmental stages [2], [8] or regeneration following trauma [9]. The culture times required by human stem cells can differ from studies using rodent cells due to the significantly longer maturation time of human cells [10], which also sets increasing requirements for the stability of MEA structures.

Although several advancements have been made to the MEA structure and electrode performance, the insulation layer has received very little attention while remaining crucial for the reliability of MEAs. Issues with insulator durability were addressed in the first publication on MEAs by Thomas *et al.* [11], and the importance of the layer was emphasized early by Gross [12]. Problems with insulator stability have been reported with widely used silicon nitride (SiN) fabricated using plasma-enhanced chemical vapor deposition (PECVD) [13], [14]. These findings raise concerns about the longevity of electrical insulation in cell experiments, which can extend to several months or even up to over a year [2], [15]. The reuse of MEAs is a common practice for several subsequent experiments, which further increases the risk of insulator damage in prolonged use. The transmission of electrical activity within the network has a large role in both neuronal and cardiac studies, and in MEA measurements, spatial detection is enabled by precise electrode openings in the insulator. As a requirement for reliable biological conclusions based on the retrieved electrical signals, the insulation layer must perform as intended to prevent the leakage and attenuation [12] of the already weak signals. Extensive damage to insulation may lead to the erroneous pickup of signals outside the intended electrode area, which can easily be interpreted as false network activity during the data analysis.

SiN is routinely applied for encapsulation and passivation in semiconductor and microelectromechanical system (MEMS) fabrication [16], [17]. It is used for scratch protection [18] and is considered as an effective barrier to moisture [16], [17]. However, the typical operating conditions for semiconductors differ significantly from the harsh physiological environment that MEAs are required to endure. Whereas typical electronics may be subjected to water vapor or occasional saline spray, MEAs are exposed to continuous immersion with various ions and enzyme activity at elevated temperature (37 °C) and high humidity. Merely testing the degradation by immersion in saline or buffer solution does not represent the actual conditions during a cell culture, although it may already reveal degradation of the structures. The biological and chemical activity can be strongly dependent on the media used and should be tested with each potential material separately. PECVD SiN films are also prone to small voids in the form of pinholes, which can act as the starting points for corrosion, causing short circuits through the insulator.

Alternative materials for the insulation layer can be used, although compatibility with other parts of the microfabrication process limits the choices available. All structures and materials used must also withstand sterilization. The number of feasible options is further decreased by the need for cytocompatibility with the cultivated cells. Materials that are nontoxic may still lead to adhesion problems with cultured cells, hindering proliferation and network formation. Thin-film ceramics possessing a relatively high dielectric constant - such as SiN or silicon dioxide (SiO<sub>2</sub>) - are traditionally used as an insulation layer in MEAs [13], [19]–[23]. Instead of replacing the entire insulation layer with an alternative material, a suitable protection layer can be used to provide the required chemical stability. The use of atomic layer deposition (ALD), for example, enables the fabrication of thin

and uniform coatings, which penetrate and seal small voids resulting from the PECVD process.

Here, we bring forward the issue with the wear of PECVD SiN while in contact with cell culture media. We confirmed and quantified the corrosion behavior of both in-house and commercial PECVD SiN in a controlled physiological setting resembling cell culture conditions. A stable insulator alternative by using annealed ALD TiO<sub>2</sub> as a protective layer was developed, tested and compared with the SiN counterpart.

Electrochemical impedance spectroscopy (EIS) was applied in a novel way to study the electrical integrity and thickness changes in the insulation layers when exposed to two cell culture media. Endpoint analysis using profilometry was performed to confirm the actual changes in thickness. A second immersion test was performed on insulators with four different media to monitor the pH of the solutions and wear at the end of the experiment using profilometry. In vitro cytocompatibility of the developed insulator with a transparent ALD TiO<sub>2</sub> protective layer was evaluated by cultivating hESC neuronal cells on the surface for 28 days.

## II. MATERIALS AND METHODS

### A. Thickness and Integrity Analysis

EIS was applied as a method for estimating the corrosion rate and film integrity. The measurements can be performed with electrically conductive cell culture media acting as an electrolyte. EIS has been widely used in material sciences and electronics to assess corrosion and dielectric properties on thin films and coatings [24]–[26]. The electrical behavior of the insulation layer can be modeled as a capacitor, where the conductive solution and the electrodes underneath the coating act as parallel plates separated by a layer of dielectric material. The capacitance value is inversely proportional to the layer thickness and can be calculated from the imaginary part of the measured complex impedance spectrum. Deviation of the spectrum from purely capacitive behavior can be used as an indicator of a failed insulator.

The interface between the electrolyte and insulator along with the underlying electrodes can be represented as an electrical equivalent circuit. A model described by Bousse and Bergveld [27] for an insulator-electrolyte interface on a silicon substrate is used here as a starting point (Figure 1a).

An electrical double layer is formed on the insulator when immersed in aqueous solution.  $C_{DL}$  represents the double layer capacitance. The capacitance  $C_a$  is related to the adsorption of ions at the interface, and  $Z_w$  is the Warburg impedance.  $Z_s$  is the impedance of the silicon semiconductor, which in this case would be replaced by the metal conductor under the insulator.  $R_b$  is the bulk resistance of the electrolyte and  $Z_{ref}$  is the impedance of the reference electrode. An insulation layer that is expected to be free of voids and leakage is represented as an ideal capacitance  $C_i$ .

An intact MEA insulator can be modeled using this ideal capacitive behavior in a simplified form (Figure 1b). The solution resistance  $R_s$  and resistance of the metal conductor  $R_m$  are placed in series with the insulator capacitance. The electrical double layer can be modeled as a single capacitance in series with insulator capacitance. Capacitances of 10–40  $\mu\text{F}/\text{cm}^2$  for double layer have been reported for electrode-electrolyte

interfaces [28], [29]. For a 1-cm<sup>2</sup> electrode area with a 500-nm layer of SiN (dielectric constant 7 [30]), the calculated insulator capacitance is 12 nF with Eq 1. With capacitors connected in series, the smaller value of the insulation layer dominates the total capacitance. As the insulator capacitance is lower than that of the double layer by several orders of magnitude, the measured capacitance is a good representation of the insulation layer, and the double layer capacitance can be omitted.

The capacitance of the insulation layer can be calculated with equation 1, where  $\epsilon_0$  is the vacuum permittivity,  $\epsilon_r$  is the relative permittivity (dielectric constant) of the material, A is the area, and d is the distance between capacitor plates.

$$C = \epsilon_0 \epsilon_r \frac{A}{d} \quad (1)$$

Because the equivalent circuit for the insulator-electrolyte interface (Figure 1b) contains only resistive and capacitive components, the imaginary part of the complex impedance is solely affected by the capacitance. The impedance of a capacitor  $Z_c$  is given in Eq 2, where j is the imaginary unit and  $\omega$  is the angular frequency. Angular frequency  $\omega$  equals  $2\pi f$ , where f is frequency.

$$Z_c = -\frac{1}{j\omega C} \quad (2)$$

Based on Eqs 1 and 2, the insulator impedance is proportional to the thickness of the layer. Corrosion of the insulation layer is detectable with EIS as increased capacitance and lowered impedance. As the frequency at each EIS measurement point is known, the capacitance value can be calculated from the imaginary part of the complex impedance and thickness of an intact insulator using Eq 1, as the dielectric constant and area are known. The measured complex impedance contains information on the resistive and capacitive behavior of the system, which allows the detection of insulator leakage as resistive paths and loss of thickness as increased capacitance. The impedance spectrum of an intact insulator appears primarily as an ideal capacitance with a phase angle of  $-90^\circ$ , whereas local voids act as resistive paths in parallel and will change the shape of both impedance magnitude and phase angle curves.

A test slide (Figure 2) was designed to mimic the insulated areas of an MEA in a simplified form without the effect of the microelectrode openings. Although the actual microelectrodes were omitted from the structure, the model represents the behavior of an MEA insulator in a cell culture environment. Relatively large circular titanium electrodes were buried under an insulation layer to act similarly to the conducting tracks on an MEA. Electrode diameters were scaled up to allow evaluation of larger insulated areas and increase the measured capacitance. The higher capacitance level allows a lower detection limit for changes in thickness while reducing the effect of stray capacitances of the measurement system. The larger area also increases the likelihood of detecting possible failures or defects. The electrode diameters used were 1.5 mm and 2 mm, with two of each size on the slide. The effective areas were estimated by calculating the conductive area of each electrode and track located inside a 14-mm-diameter circle representing the medium well. The effective areas determined

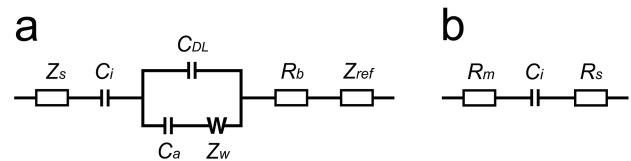


Fig. 1. (a) Equivalent circuit for the interface between a semiconductor, insulator and electrolyte. Redrawn from Bousse and Bergveld [26], [27]. (b) Simplified electrical model for the MEA insulator.

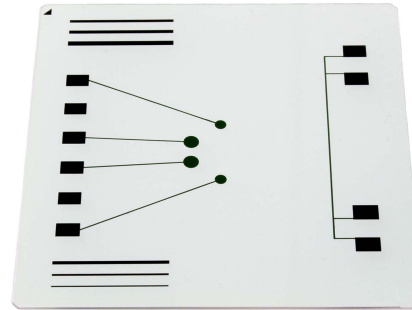


Fig. 2. Test slide with TiO<sub>2</sub> protected insulator.

were 2.43 mm<sup>2</sup> and 3.59 mm<sup>2</sup> for the 1.5 mm and 2 mm electrode diameters, respectively. The capacitance values for silicon nitride were calculated with Eq 1 using a film thickness of 500 nm and dielectric constant of 7. The calculated capacitance values were 301 pF and 445 pF for the 1.5 mm and 2 mm electrodes, respectively.

An impedance spectrum from 100 Hz to 1 MHz was obtained for each insulated electrode with 10 mV sinusoidal input using an IviumStat potentiostat (Ivium Technologies). Electrical connections between a test slide and the potentiostat were established through a commercial MEA head stage frame without amplifier electronics (MEA1060INV-CA from Multi Channel Systems MCS GmbH). A two-electrode configuration was used, with a platinum wire (ALS Co., Ltd) immersed in the medium as the counter electrode, while the titanium electrode under insulation acted as the working electrode. Ten frequencies per decade were measured with a total of 41 sample points for each spectrum. The spectral measurement was repeated three times for each electrode and averaged. For data acquisition, IviumSoft (Ivium Technologies) was used, and the resulting data were analyzed using MATLAB R2019a (MathWorks). The capacitance of the insulator was calculated from the imaginary part of the complex impedance by averaging over three repeated measurements. The frequency range of 200 Hz to 1 kHz was selected for the capacitance analysis.

Contact profilometry was used to physically validate the EIS thickness estimates and evaluate the rate of corrosion in four different cell culture media. A Bruker DektakXT profilometer (Bruker) was used to measure the thickness profile at the inner perimeter of the medium chamber. The transition from the medium exposed area to the surface covered by the PDMS ring was used to determine the possible wear. Thickness change was measured at six points around the perimeter, and the results were averaged over six points.

### B. Fabrication of Test Slides

During metal deposition in the electrode fabrication, metal stencils were used as a shadow mask allowing metal patterning

in a single step. Stencils containing the openings for electrodes, conductive tracks and contact pads were laser cut out of a 0.2-mm-thick stainless steel sheet. A conductor width of 200  $\mu\text{m}$  was selected based on the capabilities of the laser cutting process. Before metal deposition,  $49 \times 49 \times 0.7$  mm Borofloat 33 glass slides (Schott) were cleaned in an ultrasonic (US) bath with acetone and isopropanol, rinsed with deionized (DI) water and blow-dried with nitrogen. Titanium (400 nm) was deposited on glass slides with electron beam evaporation at 5  $\text{\AA}/\text{s}$  through metal stencil masks using an Orion BC-3000 box coater (System Control Technologies).

All PECVD layers were fabricated with Plasmalab 80 Plus (Oxford Instruments). Three different insulator variations were prepared for testing: SiN deposited at 300  $^{\circ}\text{C}$ , SiN deposited at 400  $^{\circ}\text{C}$  and a SiN-SiO<sub>2</sub>-SiN stack coated with ALD TiO<sub>2</sub> – the protected insulator. SiN with 300  $^{\circ}\text{C}$  deposition was previously found to suffer from substantial wear in BrainPhys medium [14]. An increased SiN deposition temperature of 400  $^{\circ}\text{C}$  was included to observe the possible changes in the corrosion properties. The ALD TiO<sub>2</sub> protected insulator was developed with the aim of providing chemical stability for the insulator throughout the test period.

SiN with a nominal thickness of 500 nm was deposited with gas flow rates of 1000 sccm for 2% SiH<sub>4</sub> diluted with N<sub>2</sub> and 30 sccm for NH<sub>3</sub>. The radio frequency (RF) power used was 20 W with chamber pressure 1000 mTorr and table temperatures of 300  $^{\circ}\text{C}$  and 400  $^{\circ}\text{C}$ . The actual measured thickness for the SiN films on glass was 500 nm  $\pm$  30 nm.

The protected insulator was formed using a PECVD deposited stack covered with an ALD TiO<sub>2</sub> layer. SiN (100 nm) was covered with 250 nm SiO<sub>2</sub>, which was again topped with 100 nm SiN at 400  $^{\circ}\text{C}$ . The deposition parameters for SiO<sub>2</sub> were 470 sccm SiH<sub>4</sub> flow and 710 sccm N<sub>2</sub>O flow with 20 W RF power and 1000 mTorr chamber pressure. SiN was deposited using the same parameters as previously described. The actual measured thickness of the three-layer stack on glass before ALD was 430 nm.

TiO<sub>2</sub> deposition was carried out using a Picosun Sunale ALD R200 Advanced reactor. Tetrakis(dimethylamido) titanium(IV) (Ti(N(CH<sub>3</sub>)<sub>2</sub>)<sub>4</sub>, TDMAT, 99%, Strem Chemicals Inc.), DI water, and Ar (99.9999%, Oy AGA Ab) were used as the Ti precursor, O precursor, and carrier/purge/venting gas, respectively. The film growth rate was calibrated by ellipsometry (Rudolph Auto EL III Ellipsometer, Rudolph Research Analytical). The substrate temperature was kept at 200  $^{\circ}\text{C}$  during deposition. The vapor pressure of TDMAT was increased to 3.6 mbar by heating the precursor bubbler to 76  $^{\circ}\text{C}$ , and the precursor gas delivery line was heated to 85  $^{\circ}\text{C}$  to prevent condensation. The water bubbler was sustained at 18  $^{\circ}\text{C}$  by a Peltier element for stability control. The substrate temperature was stabilized for 30 min before starting the deposition. A TiO<sub>2</sub> film thickness of 30 nm was achieved with 804 ALD cycles. The TiO<sub>2</sub> protective layer was air annealed at 450  $^{\circ}\text{C}$  with a ramp up of 30 minutes, followed by 60 minutes at the target temperature allowed to cool down with the oven. The thermal treatment resulted in a visual change from black to transparent.

Contact pad patterning was made with positive resist PR1-2000A1 (Futurrex Inc.), using the same resist layer

for insulator openings and the lift-off process of titanium nitride (TiN) coating. Then, 1.5 ml of resist was spin coated on the slides at 1500 rpm for 40 seconds. The insulator openings for contact pads on all slides were made with reactive ion etching (RIE) using Vision 320 RIE (Advanced Vacuum). SF<sub>6</sub> was used for etching all layers with a flow rate of 25 sccm and RF power of 100 W (at 13.56 MHz) at 20 mTorr pressure. TiN (400 nm) was deposited on the slides by using ion beam-assisted deposition (IBAD) [14] immediately following RIE etching to minimize the formation of a native oxide layer on the titanium surface. The surfaces to be coated were Ar etched for two minutes in the metallization chamber. TiN deposition [14] was performed in a N<sub>2</sub>/Ar atmosphere at 2  $\text{\AA}/\text{s}$  with an Orion BC-3000 box coater (System Control Technologies) enhanced with a SainTech ST55 ion source (Telemark). The resist remaining from the RIE step prevented adhesion of TiN to the surface of the slide outside the contact pad area and was removed in a lift-off process using a Microposit Remover 1165 (Specialty Electronic Materials) heated to 80  $^{\circ}\text{C}$  with 10-minute sonication.

The medium chambers were formed out of polydimethylsiloxane (PDMS), which was chosen for its elasticity, ease of fabrication and ability to attach to the materials used without additional adhesives. An 8-mm-thick PDMS slab was molded using a two-part silicone mixture Sylgard 184 (Dow Corning Inc.) and punched into cylindrical wells with an inner diameter of 14 mm and an outer diameter of 23 mm. The PDMS rings were manually positioned around the buried electrodes and lightly pressed onto the insulator surface. The rings were reversibly bonded, allowing easy removal after the experiment without damaging the remaining insulator.

Three commercial 60-electrode MEAs featuring PECVD SiN insulation with a nominal thickness of 500 nm were included in the experiment to provide a point of comparison for state-of-the-art device widely in use. Medium wells for the commercial samples were formed by punching four 8-mm-diameter holes on a 30  $\times$  30  $\times$  8 mm PDMS sheet. The formed structures were placed in the center of the MEA, exposing the insulated areas.

### C. Test Slide Preparation and Incubation

The insulator testing conditions were chosen to simulate a stem cell culture. The slides were kept in an incubator at 37  $^{\circ}\text{C}$ , 5% CO<sub>2</sub> concentration and high humidity. Four different solutions (phosphate buffer solution, neural maintenance medium, BrainPhys and embryoid body medium) were used for the endurance testing of the slides. Phosphate buffer solutions (PBS) are commonly used to characterize and pretreat MEA electrodes and to study their long-term stability, which may also affect the insulator surface depending on the duration of the exposure. Neural maintenance medium (NMM) and BrainPhys were selected to provide references for solutions used in neuronal studies. Embryoid body (EB) medium containing serum was included to provide insight into possible conditions during cardiac cell culture.

The PBS used was Dulbecco's PBS (DPBS) without Mg<sup>2+</sup> and Ca<sup>2+</sup> ions (Lonza). NMM is a medium used with neuronal cultures consisting of 1:1 Dulbecco's Modified Eagle Medium/Nutrient Mixture F-12 (DMEM/F-12) with Glutamax

and Neurobasal, 0.5% N2, 1% B27 with Retinoic Acid, 0.5 mM GlutaMAX, 0.5% NEEA, 50  $\mu\text{M}$  2-mercaptoethanol (all from Thermo Fisher Scientific), 2.5  $\mu\text{g/ml}$  insulin (Sigma-Aldrich) and 0.1% penicillin/streptomycin (Thermo Fisher Scientific). The third solution used was BrainPhys (STEMCELL Technologies) designed for neuronal cultures [31]. The fourth solution was EB medium consisting of knockout DMEM with 20% fetal bovine serum (Lonza), nonessential amino acids and GlutaMAX (Thermo Fisher Scientific). All solutions were supplemented with 1% penicillin-streptomycin (Thermo Fisher Scientific) to reduce the likelihood of microbial contamination.

The experimental setting for corrosion testing was divided into two parts. The first part focused on the EIS method for assessing film integrity and thickness changes of in-house SiN during incubation. The second part expanded the scope by studying commercial MEAs alongside the in-house fabricated films, with two additional media and pH monitoring. During the first 18-day experiment, the EIS curves were recorded while the insulators were exposed to DPBS and NMM, followed by endpoint profilometer measurements. In the second experiment, insulator behavior in DPBS, NMM, BrainPhys and EB media were further assessed by monitoring the medium pH and performing profilometry after 11 days to determine the corrosion rates.

In the first series, each slide was prepared with 1 ml of the test medium pipetted inside the PDMS chamber. The slides were allowed to be stabilized for 30 minutes before the first impedance measurement. Between measurement points, the slides were kept inside a petri dish in an incubator and allowed to acclimate to room temperature for a minimum of 15 minutes before subsequent measurements. Impedance measurements for the insulators were performed three times a week. After each measurement, the medium was changed to mimic the conditions of a cell culture study. The old medium was removed using a syringe and replaced with 1 ml of fresh medium before placing the slides back in the incubator.

The second test series followed the same protocol for medium changes, while the pH of each well was measured before every medium change using a Sentron SI600 (Welling). Each slide was removed from the incubator individually and measured immediately. The applied medium volumes for the in-house samples and commercial MEAs were 1 ml and 300  $\mu\text{l}$ , respectively, due to the differing chamber diameters.

After both experiments, all slides were cleaned with 1% Tergazyme detergent solution (Alconox). PDMS wells were removed before immersing the slides in detergent solution for one hour on an orbital shaker. Slides were then rinsed with DI water, acetone, isopropanol, and DI water and blow-dried with nitrogen. Profilometer measurements were performed on all slides after the cleaning procedure.

#### D. Cell Culture

Three additional slides were fabricated for cell testing. As described earlier, three tested insulator variations were deposited on bare 49 mm  $\times$  49 mm  $\times$  0.7 mm Borofloat 33 glass slides (Schott). A single 49 mm  $\times$  49 mm  $\times$  1 mm soda lime glass slide (Thermo Scientific) was used as a control

with no insulation. Each slide was covered with a 40 mm  $\times$  40 mm  $\times$  8 mm PDMS slab containing nine 6-mm-diameter holes to act as culture wells during incubation.

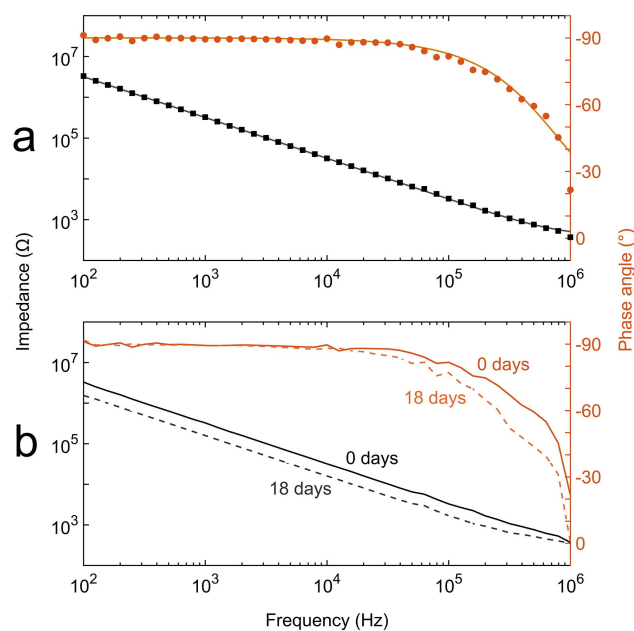
The cytocompatibility evaluation was carried out with human neuronal cells derived from the human embryonic stem cell (hESC) line Regea 08/023 [32]. The Faculty of Medicine and Health Technology, Tampere University, has a supportive statement from the regional ethics committee of Pirkanmaa Hospital District for the derivation, culture, and differentiation of hESCs (R05116).

The cortical neuron differentiation protocol is described in previous research [33]. The slides were coated with 0.05% polyethylenimine and incubated for one hour at room temperature, washed with sterile water three times and coated with 50  $\mu\text{g/ml}$  human recombinant laminin LN521 (Biolamina) at 4  $^{\circ}\text{C}$  overnight. The cells were plated on slides after a 32-day predifferentiation period [33] with a cell density of 80,000 cells per culture well. The cell culture media used was NMM supplemented with brain-derived neural factor (25  $\mu\text{g/ml}$ ; 248-BD, R&D Systems), glial cell line-derived neurotrophic factor (50  $\mu\text{g/ml}$ ; 212-GD, R&D Systems), dibutyryl cyclic-AMP sodium salt (100 mM; D0627, Sigma-Aldrich), and ascorbic acid (200 mM; A5960, Sigma-Aldrich) and was changed three times per week.

The neuronal cell populations were evaluated with immunocytochemical staining after 28 days of culture in slides [33]. The neurons were stained with primary antibodies against  $\beta$ -tubulin III (1:50; A01627, GenScript) and anti-microtubule-associated protein 2 (1:50; MAP-2 +  $\beta$ , AB5622, Millipore), and Alexa Fluor 488 (1:400; A21206, Thermo Fisher) was used as the secondary antibody. The nuclei were stained with 4',6-diamidino-2-phenylindole (1:5000; DAPI, D9542, Sigma-Aldrich) and Prolong gold (P36931, Thermo Fisher). Cultures were imaged using an Olympus IX51 inverted microscope equipped with an Olympus DP30BW camera (Olympus Corporation).

### III. RESULTS AND DISCUSSION

The simplified electrical model presented in Figure 1b and the calculated estimate for the insulator capacitance were validated by comparing a simulated impedance spectrum with a measured impedance spectrum. The simulated response of the system was calculated with Eq1 and Eq2 using estimated values for the circuit components as follows. The cell culture media are generally well conducting, with reported values of 14 mS/cm for DPBS [34], 10.69 mS/cm for Neurobasal [35] and 13.8-15.93 mS/cm for DMEM [35], [36]. Based on this, an estimate of 50  $\Omega$  was used for  $R_s$ . A capacitance value of 500 pF was calculated for  $C_i$  using the estimated electrode area.  $R_m$  was estimated at 350  $\Omega$ , which consists of the resistance of the titanium track as well as the contact resistance of the TiN pad. The superimposition of calculated spectrum on the measurement result (Figure 3a) shows an excellent fit and validates the match between electrical model and the capacitance estimation method successfully. The phase angle remains at  $-90^{\circ}$  at lower frequencies, implying no leakage and allowing thickness estimation from the data between 200 Hz and 1 kHz.



**Fig. 3.** (a) Measured impedance spectrum (data points) for 2 mm electrode with 300 °C SiN insulation in DPBS after 0 days. Solid lines show the calculated response using the model in Figure 1b with parameters  $C_i = 500$  pF,  $R_s = 50$   $\Omega$  and  $R_m = 350$   $\Omega$ . (b) EIS curves for the same electrode at 0 and 18 days.

Figure 3b shows the impedances for an electrode in DPBS at the beginning (Day 0) and end (Day 18) of the experiment. The EIS graphs show that the phase angle remains constant at frequencies below 10 kHz, although the impedance magnitude has substantially decreased due to corrosion. The corrosion process is assumed to occur only at the surface and therefore does not affect the dielectric constant of the insulator film. The thickness estimation during the medium exposure was performed based on the relative change, which was used to calculate the change in thickness. This allowed the comparison between different electrode sizes without determining the actual dielectric constant.

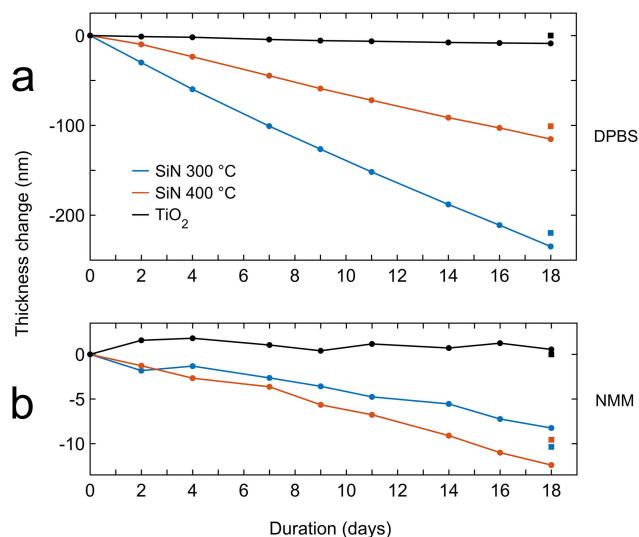
In the first experiment series, EIS analysis was performed for 18 test slides over the course of 18 days in DPBS and NMM. Six test slides with three insulator variations (SiN 300 °C, SiN 400 °C and ALD TiO<sub>2</sub> protected SiN) were included, with three slides for each group assessed in DPBS and three in NMM. Every slide contained four electrodes, making up a total of 72 electrodes, as listed in Table I. The impedance spectra on all slides exhibited predominantly capacitive behavior similar to the spectrum presented in Figure 3a which is typical for an intact insulator. No deviation from the capacitive spectrum suggested breakdown of the insulator was recorded during the experiment.

A total of seven electrodes were excluded from the final analysis as outliers due to inconsistencies in the thickness estimate, mostly due to a sudden increase in capacitance after the first measurement, which can be attributed to changes in the surface hydrophilicity. The impedance curves implied an intact film, but the data from the first measurement (Day 0) could no longer be used for accurate estimation of the layer thickness.

The results of the EIS-based thickness assessment in DPBS and NMM are presented in Figure 4 along with the

**TABLE I**  
DESCRIPTION OF THE ELECTRODES USED FOR EIS ANALYSIS IN EXPERIMENT 1

Insulator	Medium	Electrodes measured	Electrodes excluded
SiN 300 °C	DPBS	12	0
	NMM	12	0
SiN 400 °C	DPBS	12	0
	NMM	8	4
ALD TiO <sub>2</sub>	DPBS	10	2
	NMM	11	1



**Fig. 4.** EIS results for insulator thickness in (a) DPBS and (b) NMM. Squares denote the endpoint profilometer measurements.

profilometer measurements at the endpoint denoted as squares. DPBS resulted in substantial corrosion on the SiN surfaces. Corrosion was observed under a microscope on each slide, and the profilometer measurement confirmed the thickness change at  $-217.0$  nm with SiN 300 °C after 18 days, amounting to 43% of the initial nominal thickness. Increasing the deposition temperature to 400 °C lowered the wear to  $-101.0$  nm. No thickness change was observed with the ALD TiO<sub>2</sub> surface. The thickness change estimated from EIS was  $-234.8$  nm,  $-115.3$  nm and  $-8.8$  nm for SiN 300 °C, SiN 400 °C and TiO<sub>2</sub> protected SiN, respectively.

NMM caused only a slight corrosion for both 300 °C and 400 °C SiN. With both deposition temperatures, a visible edge indicates thickness loss and can be observed under a microscope, even for changes below 10 nm (as shown in Figure 5 with experiment 2). Only a minor difference in the corrosion rate was observed between the two deposition temperatures. Profilometry revealed a decrease of 10.4 nm for 300 °C SiN, while the EIS-based estimate of the thickness change was  $-8.2$  nm. The respective results for 400 °C SiN were  $-9.5$  nm with profilometry and  $-12.4$  nm with EIS. The ALD TiO<sub>2</sub> surfaces exhibited no wear with NMM. No thickness change was indicated under a microscope or through profilometry.

For the ALD TiO<sub>2</sub> surfaces, the EIS data suggest minor thickness changes of less than 10 nm for both media, although no actual change occurred. This can be a result of changes in

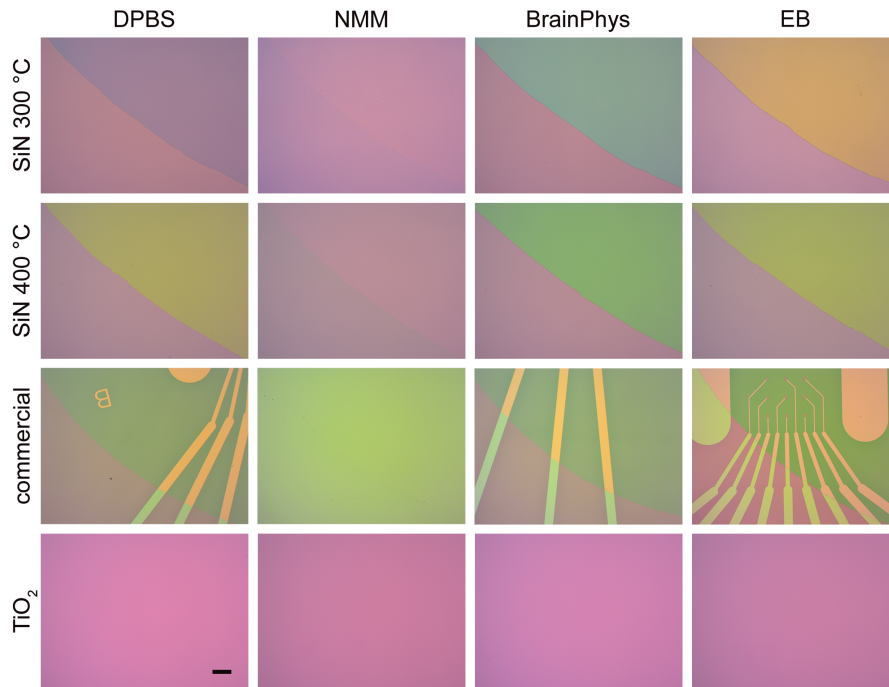


Fig. 5. The effects of media exposure on the insulator surfaces after 11 days imaged using Axio Imager.A1m (Zeiss) (scale bar 200  $\mu\text{m}$ ). The bottom left part in each image was covered by a PDMS ring during the experiment.

the surface hydrophilicity or reveal the shortcomings of EIS analysis for tiny capacitance changes. For ten  $\text{TiO}_2$  coated electrodes, the capacitance increased noticeably between the first and second measurements, and after the measurement period, the level settled for the remaining data points. This can be considered as insufficient stabilization at the insulator surface after adding the media and not actual wear of the film. For these electrodes, the second measurement point was used as the reference for thickness estimation. These results were included in our analysis, as no corrosion was detected with the profilometer and the capacitance values remained stable after the initial shift.

Compared with the profilometry measurements, the EIS analysis slightly overestimated the thickness reduction for most slides. The difference with the endpoint readings may also be explained by an error in the initial thickness estimate. Calculations using a nominal deposition value and the actual thickness on top of the titanium tracks were not experimentally confirmed. The initial electrode capacitances with SiN ranged from 326 pF to 415 pF for the 1.5 mm electrodes and from 470 pF to 580 pF for the 2 mm electrodes. These conditions provide higher thickness values than the calculated capacitance estimates, which may indicate a thinner initial layer than the expected nominal value. The variation in the initial capacitances is partly explained by the manual positioning of the PDMS rings, which affects the effective electrode area. The capacitances for  $\text{TiO}_2$ -coated surfaces varied from 265 pF to 287 pF for the 1.5 mm electrodes and from 380 pF to 410 pF for the 2 mm electrodes. This indicates that the ALD-coated stack provides a higher insulation impedance with smaller film thickness, meaning lower signal losses with cellular recordings [12].

Based on these results, EIS analysis acts as a powerful tool for detecting thickness changes for MEA insulator testing.

TABLE II  
RESULTS OF EIS AND PROFILOMETER ANALYSIS IN EXPERIMENT 1  
(18 DAYS)

Medium	Insulator	Thickness change (nm)			
		EIS	Profilometer	St Dev	Rate (nm/day)
DPBS	SiN 300 °C	-234.8	-217.0	10.2	-12.1
	SiN 400 °C	-115.3	-101.0	4.8	-5.6
	TiO <sub>2</sub>	-8.8	0	-	0
NMM	SiN 300 °C	-8.2	-10.4	1.3	-0.6
	SiN 400 °C	-12.4	-9.5	2.1	-0.5
	TiO <sub>2</sub>	0.6	0	-	0

The highly linear wear curves support the assumption that corrosion with SiN occurs at the surface, gradually dissolving the insulator in the medium without affecting the bulk structure of the film. Porosity or localized defects reaching through the insulator would show resistive behavior through the film, which was not observed in the experimental data.

The second series expanded the scope by including a commercial MEA, pH measurement and medium testing with BrainPhys and EB medium supplemented with FBS, both of which have previously been used with SiN insulated MEAs [14], [37]. Corrosion rates for all four media were estimated. Three test slides for each insulator variant and three commercial MEAs were exposed to each of the media and measured. The endpoint thickness changes, calculated corrosion rates and pH recordings are presented in Table II.

Figure 5 clearly shows the corroded edge on all samples except those featuring the  $\text{TiO}_2$  protection and NMM with the commercial MEA. On the commercial MEA, NMM showed

**TABLE III**  
PROFILOMETER AND pH MEASUREMENT RESULTS FOR EXPERIMENT 2  
(11 DAYS)

Medium	Insulator	Thickness change (nm)			pH
		Profilometer	StDev	Rate (nm/day)	
DPBS	SiN 300 °C	-133.7	7.6	-12.2	6.6
	SiN 400 °C	-59.2	4.8	-5.4	
	commercial	-45.9	2.8	-4.2	
	TiO <sub>2</sub>	0	-	0	
NMM	SiN 300 °C	-9.5	1.1	-0.8	7.4
	SiN 400 °C	-9.6	1.0	-0.9	
	commercial	-	-	-	
	TiO <sub>2</sub>	0	-	0	
BrainPhys	SiN 300 °C	-245.9	5.2	-22.4	7.5
	SiN 400 °C	-96.3	4.0	-8.8	
	commercial	-69.6	8.1	-6.3	
	TiO <sub>2</sub>	0	-	0	
EB	SiN 300 °C	-175.2	3.7	-15.9	7.4
	SiN 400 °C	-71.1	2.9	-6.5	
	commercial	-56.3	2.8	-5.1	
	TiO <sub>2</sub>	0	-	0	

faint marks on the edge of PDMS, but the thickness change could not be reliably detected with profilometry after the immersion period. The corrosion rates of in-house SiN with DPBS and NMM confirmed the rates found in the first test series. Commercial MEAs exhibited noticeable wear in DPBS, at 22% lower rate than the in-house 400 °C SiN, likely due to different process parameters and equipment. Herrera-Morales previously obtained a corrosion rate of 2.1 nm/day for 400 °C SiN in PBS [37], [38], although using different deposition parameters and equipment. The tests were performed on a hot plate, differing on incubator conditions regarding humidity and CO<sub>2</sub> levels, with medium changes only once a week.

Here, the introduction of BrainPhys and EB medium revealed more severe degradation on the in-house SiN surfaces when compared to degradation caused by DPBS or NMM. Exposure of SiN 300 °C to BrainPhys for 11 days resulted in a loss of 49% of the nominal insulator thickness. This is in line with previous findings [14], where BrainPhys was able to dissolve most of the insulation layer during a single cell culture cycle. The commercial MEAs exhibited substantial corrosion with both BrainPhys and EB medium. Compared to the in-house 400 °C SiN, the corrosion rates for the commercial MEAs were 28% lower for BrainPhys and 21% lower for EB medium, respectively.

No corrosion was detected with TiO<sub>2</sub> for any of the solutions, suggesting excellent corrosion protection for a wide range of cell culture media.

In each medium, the pH values remained constant throughout the experiment regardless of the possible wear, indicating no direct relation between the pH level and the corrosion rate. DPBS is not buffered to be used in a 5% CO<sub>2</sub> atmosphere, which explains the lower pH in the incubator conditions. NMM, BrainPhys and EB have approximately the same pH, although their corrosion profiles are distinctively different.

The differences in corrosion rates underline the need for systematic testing of MEA materials with different culture media. In the worst case presented here with 300 °C SiN and BrainPhys, the entire 500-nm insulation layer is estimated to be dissolved in under 23 days, which is shorter than the duration of many neuronal studies. With the EB medium used in cardiac studies, the corresponding estimate is 32 days. These estimates do not even take into account the case where signals may already be picked up through the thinned-out insulator once the impedance through the film is low enough.

Increasing the deposition temperature for SiN from 300 °C to 400 °C can reduce the corrosion up to 60%, although the wear is still substantial for several solutions. According to the results within this work, the base medium for NMM appears to be safe for use with SiN, but the effects of possible supplements are not assessed.

The commercial MEAs showed slightly improved stability over the in-house 400 °C SiN, for which however the other deposition parameters were not optimized. Nevertheless, the commercial MEAs were also subject to substantial corrosion with several cell culture media during a relatively short observation period. The results here suggest that the nominal insulation thickness of 500 nm is lost entirely after 80 days in contact with BrainPhys, which can be exceeded when reusing MEAs.

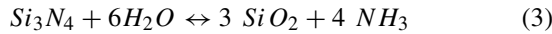
Previous accounts of SiN insulator corrosion [14] and cracking [13] have been reported, but the severity of the problem has not been properly addressed in the literature, although MEAs with SiN insulation have been in commercial and academic use for decades. Insulation loss can be critical when studying the electrical stimulation of cultured cells, where leakages of up to 30% of the stimulation current have been reported with a damaged SiN insulator [13]. The lack of insulation outside the electrode sites can lead to the loss of spatial information, as the signal paths for the stimuli or cell activity are no longer well known. A reduction in insulation impedance also causes severe attenuation of the measured signals at electrodes, as described by Gross [12]. With cardiac cells, the effect of insulator leakage may be emphasized due to the higher amplitude of electrical signals compared to neuronal cultures.

The effects of corrosion may be overlooked if the culture chambers are permanently bonded and not removed to observe the signs of possible thickness reduction. The phenomenon is also difficult to detect electrically from a used MEA, as impedance measurements of the opened electrodes do not directly reveal the state of the insulation layer. Although the reduced insulation should lower the measured impedance, the TiN electrodes increase in impedance [14] over time due to oxidation, potentially counteracting and masking the effect of insulation loss on the measurement.

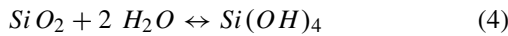
Dissolution of SiN in aqueous media has been reported with powder and PECVD thin films [39], [40], along with



studies related to implantable hip joint replacements [18], [41]. Dissolution of SiN has been found to be linear and occurs at the surface [42], which supports the results here. The dissolution process has been suggested to occur first by hydrolysis at the SiN surface through reaction



where exposure to water partly oxidizes the surface, forming SiO<sub>2</sub> and ammonia [39], [42]–[44]. The oxidized surface is further dissolved through reaction



producing silicic acid. It is worth noting that PECVD SiN films are nonstoichiometric, containing varying rates of hydrogen from the gas precursor, both of which may affect the dissolution properties. The film properties can vary significantly depending on the deposition parameters, including gas flow rates, temperature and RF power, as well as the equipment used. Here, only the effect of increased deposition temperature was demonstrated. Although the culture media is buffered for pH, the presence of the released reaction products may affect the cells locally at the corroding surface. Frequent changes of the culture medium also prevent the solution from saturating with reaction products, allowing corrosion to proceed for the duration of the culture. To ensure a consistent insulation performance throughout the cell culture period, a more chemically stable coating can be applied, as shown here. ALD TiO<sub>2</sub> layers have been applied for biological studies by Herrera-Morales [38] and more recently by Dollt *et al.* [45]. In the latter, the approach was to form an insulating coating over the electrodes, which we did not see as beneficial due to the increased electrode impedance levels. Here, the proposed method applies a protective layer covering only the insulator by eliminating the coating at the measurement sites. This method is compatible with existing MEA fabrication methods [14], [21], [46], where the TiO<sub>2</sub> layer would be etched over the electrodes together with the underlying dielectric layers before applying the final electrode surface.

TiO<sub>2</sub> resulting from the ALD process is initially black and moderately conductive, both of which are undesirable qualities when protecting the electrical insulator in cell studies. The opaqueness and conductance can, however, be counteracted by air annealing. The major benefits of annealed TiO<sub>2</sub> used here include vastly improved optical transparency, allowing the use of traditional optical microscopy methods needed in cell studies, reduced electrical conductivity of the protective layer and increased chemical stability of the surface. The effects of thermal annealing on the optical transparency and reduction in electrical conductivity of ALD TiO<sub>2</sub> have been described by Ali-Löyty *et al.* [47]. This is attributed to the transformation from the amorphous phase to more stable crystalline rutile that is achieved through oxidation at temperatures above 200 °C. Increased chemical stability in NaOH was also reported with air annealing at 300 °C or higher [47].

Although the annealed ALD TiO<sub>2</sub> alone does not provide sufficient electrical insulation, coating on thin film ceramics can be tailored to surpass the performance of a single SiN layer. Here, a layer of SiO<sub>2</sub> featuring a low dielectric constant

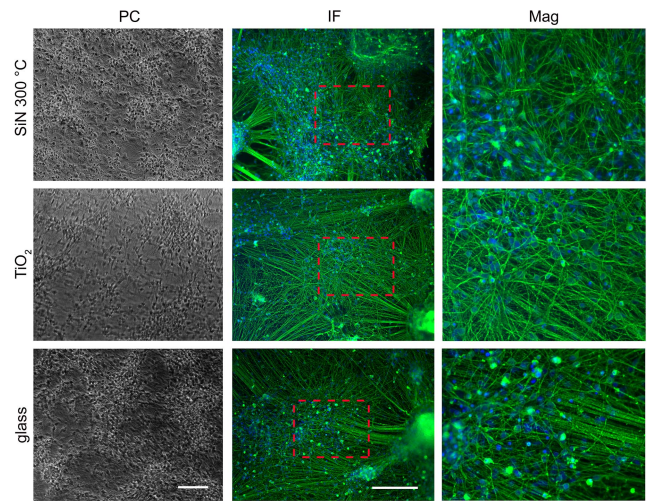


Fig. 6. Cultured in vitro hESC neurons for cytocompatibility evaluation. Phase contrast images (PCs) were taken 7 days after plating the cells. Magnification 10 $\times$  using Zeiss Axio Observer A1 (Zeiss). Immunofluorescence images (IFs) were taken after fixing the cells at 28 days. The labels used were DAPI in the blue channel for cell nuclei and MAP-2 +  $\beta$ -tubulin in the green channel for neuronal cells. Magnification 10 $\times$  using Olympus IX-51. Digital magnifications of immunofluorescence images (Mag) are from the dashed line areas. Scale bar represents 200  $\mu\text{m}$  in each image.

was sandwiched between two SiN layers to provide higher insulation impedance and to eliminate residual stress in the deposited layer. The ALD TiO<sub>2</sub> layer seals possible pinholes in the SiN surface, which may otherwise serve as starting points for corrosion and electrical short circuits. SiN underneath the protective ALD TiO<sub>2</sub> layer provides a diffusion barrier for ions, which SiO<sub>2</sub> alone is not capable of. This layered structure provides lower capacitance and losses with the same film thickness compared to SiN while remaining stable in various cellular media.

#### A. Cytocompatibility of ALD TiO<sub>2</sub>

The cytocompatibility of the ALD TiO<sub>2</sub> surface was evaluated using hESC-derived neurons. Cell growth and viability were visually inspected once per week during the experiment. All sample groups (300 °C SiN, ALD TiO<sub>2</sub>-coated SiN and glass substrates) exhibited similar growth rates and good viability throughout the culture period. At later timepoints, the cell growth aggregated to some extent in all samples, as shown in immunofluorescence images (IF) in Figure 6. Immunocytochemical staining after 28 days showed similar dense neuronal networks with each sample group regardless of the surface coating type. The developed ALD TiO<sub>2</sub> insulator appeared to promote neuronal cell growth with no signs of cytotoxicity. The cell visibility with ALD TiO<sub>2</sub> during microscopy was comparable to that of SiN-coated glass and a plain microscope slide. According to the results of the cytocompatibility evaluation, the ALD TiO<sub>2</sub> protected insulator suggested herein could be directly implemented in MEA measurements without adverse effects on neuronal cell viability or microscopy.

## IV. CONCLUSION

The possibility of degradation with SiN insulation should be considered as a potential source of error in MEA recordings. SiN degradation potentially affects the reliability and analysis

of MEA measurements in cellular electrophysiology studies due to loss of electrical insulation. The quality of the recordings can be compromised, especially when reusing MEAs for subsequent experiments, where the exposure time to the media is prolonged. Here, we addressed the severity of SiN corrosion with select cell culture media by using both in-house deposited coatings and commercial MEAs. The findings in this paper raise serious concerns about the effects of this phenomenon on the results of previously performed experiments with extended culture periods and reuse of MEAs.

EIS measurements were successfully introduced as the means of monitoring the insulator film thickness and integrity in the media during incubation, showing linear corrosion behavior on the in-house SiN surfaces. The measurement method presented here can be applied for the screening of insulator materials and their possible quantitative degradation in culture conditions. The alarming corrosion rates of in-house and commercial SiN surfaces with certain typically used media call for the re-evaluation of MEA insulators widely in use. All SiN films studied in this paper showed varying degrees of wear depending on the media used, with no direct relation to the pH. The corrosion reaction mechanisms between the surface and medium are not well understood due to the complex composition of both the cell culture media and PECVD SiN, warranting further research. The consequences of insulation loss should also be studied in more detail at the signal level.

The ALD TiO<sub>2</sub> protected insulator was developed to address the need for a more reliable MEA insulator for long-term measurements. The featured ALD coating was found to entirely prevent corrosion in this setting while showing good *in vitro* cytocompatibility with hESC-derived neurons. Both the chemical stability and electrical impedance of the developed corrosion-protected insulator were found to surpass the features of those with a single SiN layer. The proposed fabrication method can be used to directly replace the traditional SiN insulator, providing increased reliability for sensors used in electrophysiological studies while allowing the same optical imaging methods to be used. The proposed insulator structure provides improved overall performance without requiring any changes in culture or measurement protocols from the end user. The described protective layer may also provide benefits to other applications where chemical stability in biological fluids is needed.

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