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RESEARCH ARTICLE

Close-Loop Current Control With Current-Sensing Resistor for Electromagnet Driven by Full-Bridge PWM Inverter

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ABSTRACT The full-bridge pulse-width-modulation (PWM) inverter is wildly applied to drive the electromagnet and enables advanced technologies, such as the active magnetic bearing (AMB) and the permanentelectro magnetic suspension (PEMS). However, the characteristic relationship between the current through the electromagnet and the duty cycle of the PWM signal has strong nonlinearity around the zero current. Moreover, the electromagnet possesses the induction that results in the time constant and significantly hinders the change of the current. Hence, the open-loop control of the full-bridge PWM inverter cannot accurately or timely tune the current through the electromagnet, especially around the zero current. This work proposes a closed-loop current control approach by three steps: (1) inserting a current-sensing resistor into the middle of the electromagnet, (2) obtaining the current signal with an analog signal-processing circuit, and (3) generating the PWM signal with the bang-bang control circuit. The proposed approach innovatively arranges the current-sensing resistor to take advantage of the symmetry and to minimize the influence from the high-frequency switching of the inverter, so that the measured current signal is comparable to the hall-effect current sensor. The experimental results demonstrate the effectiveness and efficiency of the proposed closed-loop current control approach, though the weak charging capability of the full-bridge PWM inverter still hinders its performance.

INDEX TERMS Electromagnetics, DC-DC power converters, bang-bang control.

I. INTRODUCTION

The full-bridge pulse-width-modulation (PWM) inverter is wildly applied to drive the electromagnet and enables advanced technologies, such as the active magnetic bearing (AMB) [1] and the permanent-electro magnetic suspension (PEMS) [2]. AMB is famous for no contact and eliminates unwanted friction and lubrication so that AMB significantly promotes the high-speed rotating machinery [3]. Moreover, PEMS takes advantage of the Nd-Fe-B permanent magnet to reduce the power consumption of the electromagnet and

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enables the zero-power suspension [4]. Ren and *et al.* [5] achieved fast and stable levitation of an AMB rotor by compensating for system uncertainties via proper design of interval type-2 membership functions. Zhu and Liu [6] proposed a rotor displacement self-sensing modeling and achieved a six-pole radial hybrid magnetic bearing (HMB). Yalcin and *et al.* [7] applied the PEMS technology for the 3-DoF micro vibration isolation with the zero-power feature.

The current through the electromagnet plays an important role [8] in the robustness of AMB [9] and PEMS [4], [8]. However, the electromagnet possesses a large inductance that leads to the large time constant; besides, the tiny turn-off delay of the full-bridge PWM inverter results in the nonlinear current-duty cycle characteristic around the zero current [10]. Both the large time constant and the current-duty cycle nonlinearity challenge the timely and accurate control on the current through the electromagnet [11].

In order to achieve the timely and accurate control on the current through the electromagnet, various closed-loop current control approaches are proposed and mainly differ in current sensors / estimations:

- *Hall-effect current sensor* [7], [11]–[15]: prone to electromagnetic disturbance, especially for small current;
- *Resistive current sensor* [9], [16]: prone to fluctuation by the frequent PWM switching;
- *Polynomial estimation* [17]: based on the nonlinear relationship between the current and the PWM duty cycle; inaccurate due to temperature change of the electromagnet; untimely due to the inductance of the electromagnet;
- *State observer* [11]: prone to nonlinearity and highly relies on the dynamic model.

Hence, the hall-effect current sensor and the resistive current sensor can measure the current on the real-time basis, while the polynomial estimation and the state observer suffers from the nonlinear dynamics. Moreover, the resistive current sensor would prevail over the hall-effect current sensor in applications of PEMS and AMB under the small-current condition as a result of the excessive electromagnet disturbance.

Besides, closed-loop current control approaches also differ in control algorithms:

- *Digital one-cycle control (D-OCC)* [18]: it requires a sampling rate as high as the PWM frequency and a high-speed digital signal processing (DSP) controller;
- *Digital model predictive control (D-MPC)* [16]: it requires a high sampling rate and a powerful processor.
- *Bang-bang control (also known as switching control)* [19]: it reaches the target current with a minimized time according to the time constant of the electromagnet.

Hence, in order to optimize the close-loop current control, it generally requires a high sampling rate of the digital controller to capture the current dynamics. On the contrary, the bang-bang control is simple (*without control parameter*) and robust (*fast response*) to work with the electromagnet.

Taking the above literatures into consideration, the present work aims to propose a close-loop current control approach with a current-sensing resistor for the electromagnet driven by the full-bridge PWM inverter. The approach innovatively inserts a current-sensing resistor into the middle of the electromagnet and utilizes the symmetry to minimize the influence from the high-frequency switching of the inverter at the 10 kHz PWM frequency. Moreover, the analog signalprocessing circuit amplifies the current signal, whereas the differential comparator realizes the bang-bang control. Furthermore, the resistor-capacitor (RC) filter tunes the bandwidth of the control signal, and the PWM signal is generated based on the sawtooth carrier produced by a timer.

This work is organized as the following. Sec. II is devoted to formulate the theoretical basis for the proposed close-loop current control approach, including the full-bridge inverter, the signal-processing circuit, the bang-bang controller and the stability analysis. In Sec. III, five experiments are carefully demonstrated and discussed to evaluate the performance of the proposed approach. In particular, Exp. 1 illustrates the high-frequency ripples on the current signal due to the frequent switching of the inverter at the 10 kHz PWM frequency; Exp. 2 shows the current-duty cycle nonlinearity and the accuracy of the measured current signal; Exps. 3, 4, and 5 verify the proposed approach under various current setpoints. Last but not least, concluding remarks are addressed in Sec. IV.

II. THEORETICAL MODELLING

A. FULL-BRIDGE INVERTER

As reported in the previous work [10], Fig. 1 shows the circuit of the L298N full-bridge PWM inverter that is controlled by the PWM signal. In particular, the current-sensing resistor, R_{curr} , is serially connected into the middle of the electromagnet, which consists of two inductors and two resistors, i.e., L_{left} , L_{right} , R_{left} , and R_{right} . In order to minimize the influence from the inserted current-sensing resistor on the electromagnet, R_{curr} is much smaller than R_{left} and R_{right} . Denote the voltage across R_{curr} as u_{MN} and the current through R_{curr} as i_{MN} , so that we can write,

$$u_{MN} = U_M - U_N = R_{curr} i_{MN}, \qquad (1)$$

where U_M and U_N are two node voltages of R_{curr} .



FIGURE 1. Circuit of L298N full-bridge PWM inverter. The current-sensing resistor, R_{curr} , is serially connected into the middle of the electromagnet that consists of L_{left} , L_{right} , R_{left} , and R_{right} . U_M and U_N are two node voltages of R_{curr} . The NOT gate is used to reverse the PWM signal.

Moreover, the electromagnet and R_{curr} are driven by L298N. Denote the voltage output from L298N as *u*, and we can write the differential equation,

$$u = R_{total}i_{MN} + L_{total}di_{MN}/dt, \qquad (2)$$

where $R_{total} = R_{left} + R_{curr} + R_{right}$ and $L_{total} = L_{left} + L_{right}$. Equation (2) indicates a first-order self-balance process with the time constant of $\tau = L_{total} / R_{total}$.



FIGURE 2. The signal-processing circuit to convert the two node voltages, U_M and U_N , into the current signal, U_Q . U'_N , U_P , and U'_D are three intermediate voltages. All LM324 work at 24 V power supply.



FIGURE 3. The bang-bang control circuit to generate the PWM signal. U_Q is the current signal, U_S is the current setpoint, and U_W is the sawtooth carrier. U_{BB} and U'_{BB} are two intermediate voltages. The first differential comparator, IC_{10} , realizes the bang-bang control, whereas the second one, IC_{15} , generates the PWM signal. All LM339 and LM324 work at 5 V power supply.



FIGURE 4. The astable circuit of NE555 to generate the sawtooth carrier, U_W .

Furthermore, *u* is controlled by the PWM signal as,

$$u \approx \begin{cases} +U, \quad \text{PWM} = \text{HIGH} \\ -U, \quad \text{PWM} = \text{LOW}, \end{cases}$$
(3)

where U = 24 V denotes the power supply voltage.

Equation (3) indicates that U_M and U_N may potentially fluctuate between 0 V and U if $R_{left} = 0$ or $R_{right} = 0$ [10].



FIGURE 5. The block diagram of the proposed close-loop current control approach for the electromagnet with the full-bridge PWM inverter.

In order to minimize the voltage fluctuation of U_M and U_N , the electromagnet is separated into two equal halves, i.e., $R_{left} = R_{right} \gg R_{curr}$ and $L_{left} = L_{right}$, so that we can derive U_M and U_N by symmetry,

$$\begin{cases} U_M = \left(U + R_{curr}i_{MN}\right)/2\\ U_N = \left(U - R_{curr}i_{MN}\right)/2, \end{cases}$$
(4)

which minimizes the influence from the high-frequency switching of the inverter as a result of small R_{curr} .



FIGURE 6. Experiment setup of the close-loop current control for the electromagnet driven by the L298N full-bridge PWM inverter, (a) graphic diagram, and (b) photograph. Two equivalent electromagnets are face-to-face stacked, and the current-sensing resistor as well as the ACS712 hall-effect current sensor are serially connected into the two electromagnets. U_M and U_N are connected to the signal-processing circuit, whereas the PWM signal is generated by the bang-bang control circuit or the wave generator. The oscilloscope is used to capture the raw data.

B. SIGNAL-PROCESSING CIRCUIT

Fig. 2 shows the signal-processing circuit to convert the two node voltages, U_M and U_N , into the current signal, U_Q . IC_i (i = 1, 2, ..., 9) are nine LM324 operational amplifiers and work at 24 V power supply. In particular, IC_1 , IC_2 , IC_4 , IC_6 , IC_7 , and IC_9 serve as the voltage followers, whereas IC_3 , IC_5 , and IC_8 aim to process signals as detailed below.

 IC_3 reverses U_N as,

$$U_N' = U - U_N, \tag{5}$$

where $R_{rev,1} = R_{rev,2}$.

 IC_5 subtracts U_N from U_M as,

$$\frac{U_M - U/2}{R_{base,1a}} + \frac{U_N' - U/2}{R_{base,1b}} = \frac{U/2 - U_P}{R_{amp,1}},\tag{6}$$

where $R_{base,1a} = R_{base,1}$. Solving (6) with (5) gives,

$$U_P = U/2 - \frac{R_{amp,1}}{R_{base,1a}} u_{MN}.$$
(7)

The 1st RC filter consisting of $R_{fil,1}$ and $C_{fil,1}$ removes high-frequency ripples [20] on U_P with the cutoff frequency at $f_{fil,1} = 1/2\pi R_{fil,1}C_{fil,1}$ and outputs $U'_P (\approx U_P)$, which is to be further illustrated in Sec. III(B).

 IC_8 further shifts and amplifies U'_P as,

$$\frac{U'_P - U_{offset}}{R_{base,2}} = \frac{U_{offset} - U_Q}{R_{amp,2}},\tag{8}$$

where U_{offset} is tuned by the potentiometer R_{offset} . Solving (8) with (1) and (7) gives,

$$U_{Q} = \left(1 + \frac{R_{amp,2}}{R_{base,2}}\right) U_{offset} - \frac{R_{amp,2}}{R_{base,2}} U'_{P}$$

$$\approx \frac{R_{amp,1}}{R_{base,1a}} \frac{R_{amp,2}}{R_{base,2}} R_{curr} i_{MN} + \left(1 + \frac{R_{amp,2}}{R_{base,2}}\right) U_{offset}$$

$$- \frac{R_{amp,2}}{2R_{base,2}} U. \tag{9}$$

TABLE 1. Physical properties.

Symbol	Quantity	Value	Unit
R _{total}	Total resistance	27.0	Ω
R_{curr}	Current-sensing resistance	0.1	Ω
L_{total}	Total inductance	334.1	mH
τ	time constant	12.4	ms
U	Power supply voltage	24	V
C_W, C_{GND}	Waveform capacitances	10	nF
$R_{W,1}$	Waveform resistance	630	Ω
$R_{W,2}$	Waveform resistance	6.88	kΩ
fpwm	PWM frequency	10	kHz
D	PWM duty cycle	0.0 to 1.0	-
$R_{rev,1},R_{rev,2},R_{rev,3},R_{rev,4}$	Resistances to reverse signals	100	kΩ
$R_{base,1a}, R_{base,1b}$	Resistances as the base	220	kΩ
$R_{amp,1}$	Resistance to amplify signal	1	MΩ
$R_{base,2}$	Resistance as the base	20	kΩ
$R_{amp,2}$	Resistance to amplify signal	41.2	kΩ
U_{offset}	Offset voltage	8.62	V
$R_{fil,1}$	Resistance of 1st RC filter	160	kΩ
$R_{fil,2}$	Resistance of 2 nd RC filter	480	kΩ
$C_{fil,1}, C_{fil,2}$	Capacitances of RC filters	1	nF
$f_{fil,1}$	Cutoff frequency of 1st RC filter	1.0	kHz
$f_{fil,2}$	Cutoff frequency of 2nd RC filter	0.33	kHz
$R_{pull,1}, R_{pull,2}$	Pull-up resistances	5	kΩ
U_P	Raw signal	≈ 12.5	V
U_Q	Current signal	0.9 to 2.5	V
U_{BB}^{\prime}	Signal from bang-bang control circuit	1.4 to 3.6	V

Hence, when $i_{MN} = 0$ (i.e., no current through the electromagnet), $U_Q = \left(1 + \frac{R_{amp,2}}{R_{base,2}}\right) U_{offset} - \frac{R_{amp,2}}{2R_{base,2}}U$. Besides, the gain between i_{MN} and U_Q is $\frac{R_{amp,1}}{R_{base,1a}} \frac{R_{amp,2}}{R_{curr}}R_{curr}$.

C. BANG-BANG CONTROLLER

Fig. 3 shows the bang-bang control circuit to generate the PWM signal. IC_{10} and IC_{15} are two LM339 differential comparators and IC_i (i = 11, 12, 13, 14) are four LM324



FIGURE 7. Ripples on the raw signal with two duty cycles, D = 0.5 and 0.65, (a & c) U_P , and (b & d) fast Fourier transformation (FFT) of U_P . The cyan curves in (a & c) are the raw data out of 20 consecutive cycles, whereas the black curves are the corresponding averaged values. The sampling rate is 25 MHz.



FIGURE 8. (a) Nonlinear relationship between the current signal and the duty cycle, with three asymptotic lines drawn to highlight the nonlinearity, (b) linear relationship between the current signal and the hall-effect signal, with one asymptotic line.

operational amplifiers. All LM339 and LM324 work at 5 V power supply. In particular, IC_{11} , IC_{13} , and IC_{14} serve as the voltage followers, IC_{10} realizes the bang-bang control, IC_{12} reverses the control signal, and IC_{15} generates the PWM signal, as detailed below.

Since $R_{rev,3} = R_{rev,4}$, the bang-bang control signal, U_{BB} , can be expressed as,

$$U_{BB} = \begin{cases} \text{HIGH}, & U_Q > U_S \\ \text{LOW}, & U_O < U_S, \end{cases}$$
(10)

where U_S is the current setpoint.

Moreover, the 2nd RC filter consisting of $R_{fil,2}$ and $C_{fil,2}$ removes high-frequency noises from U_{BB} with the cutoff frequency at $f_{fil,2} = 1/2\pi R_{fil,2}C_{fil,2}$ and outputs U'_{BB} ($\approx U_{BB}$). Technically, in order to maintain a sufficient bandwidth of the bang-bang control circuit, $f_{fil,1}$ should be larger than $f_{fil,2}$ but smaller than the PWM frequency, f_{PWM} , i.e., $f_{fil,2} < f_{fil,1} < f_{PWM}$.

Furthermore, U_W is the sawtooth carrier generated by the NE555 timer under the astable operation in Fig. 4 and fluctuates between 1.65 V and 3.3 V. By referring to the datasheet of NE555, the frequency of the astable waveform is,

$$f_{\rm PWM} = 1.44 / (R_{W,1} + 2R_{W,2}) C_W.$$
 (11)

Nevertheless, IC_{15} generates the PWM signal by comparing U'_{BB} and U_W as,

$$PWM = \begin{cases} HIGH, & U_W > U'_{BB} \\ LOW, & U_W < U'_{BB}. \end{cases}$$
(12)

which indicates that the duty cycle, D, increases as the decrease of U'_{BB} .

D. STABILITY ANALYSIS

Fig. 5 shows the block diagram of the proposed close-loop current control approach for electromagnet with L298N.



FIGURE 9. Close-loop current control for two current setpoints, $U_S = 1.64$ V and 1.81 V, (a & d) dynamic responses of U'_{BB} , (b & e) dynamic responses of U_Q , and (c & f) FFT of U'_{BB} and U_Q . The cyan curves in (a & d) are the raw data out of 25 consecutive cycles, whereas the black curves are the corresponding averaged values. The green curves in (b & e) are the raw data out of 25 consecutive cycles, whereas the red curves are the corresponding averaged values. The sampling rate is 100 kHz.

In order to analyze its stability, we consider the following two scenarios:

- When the current signal is larger than the associated setpoint, i.e., $U_Q > U_S$, U'_{BB} will increase by (10), D will decrease by (12), i_{MN} will decrease by (3), which results in the decrease of U_Q by (9).
- When the current signal is lower than the associated setpoint, i.e., $U_Q < U_S$, U'_{BB} will decrease by (10), D will increase by (12), i_{MN} will increase by (3), which results in the increase of U_Q by (9).

Therefore, the proposed approach can stabilize the current signal at the associated setpoint, i.e., $U_Q \approx U_S$.

In addition, this work considers a low-voltage scenario (24 V), whereas a high-voltage counterpart ($200 \sim 600$ V) might differ in the selection of the full-bridge PWM inverter in Fig. 1 as well as those operational amplifiers in Fig. 2 to meet the high-voltage requirement.

III. RESULTS AND DISCUSSION

A. EXPERIMENT SETUP

In order to verify the proposed close-loop current control approach, Fig. 6(a) shows the graphic experiment setup consisting of two face-to-face-stacked electromagnets, the current-sensing resistor, the L298N full-bridge PWM inverter, and the ACS712 hall-effect current sensor (with the optimized accuracy range from -5 A to +5 A, the sensitivity around 185 mV/A and the bandwidth at 50 kHz). Besides, Fig. 6(b) shows the photograph which further shows the 5 V power bank, the 24 V power supply, the wave generator, the oscilloscope, the signal-processing circuit, and the bang-bang control circuit. The current-sensing resistor is serially connected into the two electromagnets, U_M and U_N are connected to the signal-processing circuit, and its measured current signal is to be compared with that measured by the ACS712 hall-effect current sensor. The PWM signal is generated by the bang-bang control circuit or the wave generator. The oscilloscope is used to capture the raw data. Also, physical properties regarding the experiment setup are listed in Table 1.

In particular, $R_{base,1a}$, $R_{amp,1}$, $R_{base,2}$, $R_{amp,2}$, and U_{offset} are carefully tuned to meet the following three conditions,

$$\begin{cases} U_Q(D=0.5) = \left(1 + \frac{R_{amp,2}}{R_{base,2}}\right) U_{offset} = \frac{R_{amp,2}}{2R_{base,2}} U = 1.65 \text{ V} \\ U_Q(D=1) - U_Q(D=0) = 2 \frac{R_{amp,1}}{R_{base,1a}} \frac{R_{amp,2}}{R_{base,2}} R_{curr} |i_{MN}|_{max} = 1.65 \text{ V}, (13) \\ \Delta U_Q/\Delta i_{MN} = \frac{R_{amp,1}}{R_{base,1a}} \frac{R_{amp,2}}{R_{base,2}} R_{curr} = 0.93 \text{ V/A} \end{cases}$$

where $i_{MN} (D = 0.5) = 0$ A and $|i_{MN}|_{max} = U / R_{total}$.

Moreover, $R_{W,2}$ is tuned to realize $f_{PWM} = 10$ kHz according to (11).

B. EXP. 1: RIPPLES ON UP

Since $f_{PWM} = 10$ kHz, the high-frequency switching of L298N results in ripples on U_P [10, 20]. Fig. 7 shows two sets of data with D = 0.5 and 0.65. We observe that ripples tightly correspond to both switch-on and -off of L298N and have frequencies at 10 kHz together with higher harmonic frequencies. Besides, the amplitude of the ripples is less than 1 V and is less than 5% of the 24 V power supply voltage, which agrees well with (4) to minimize the influence from the high-frequency switching of the inverter.

Moreover, in order to remove the high-frequency ripples on U_P , the 1st RC filter is applied in the signal-processing circuit in Fig. 2. From Table I, $R_{fil,1} = 160 \text{ k}\Omega$ and $C_{fil,1} =$ 1 nF lead to the cutoff frequency at $f_{fil,1} = 1.0$ kHz that is sufficient.

C. EXP. 2: CURRENT SIGNAL U₀

By applying the PWM signal with various duty cycles to the full-bridge PWM inverter, the signal-processing circuit outputs the current signal (U_Q) that is to be compared with the hall-effect signal (U_{hall}) measured by the ACS712 halleffect current sensor.

Fig. 8(a) shows the nonlinear relationship between the current signal and the duty cycle [10]. In particular, three asymptotic lines with the slopes of around 1.72, 0.40, and 1.70 from left to right are drawn to highlight the nonlinearity. In other words, the sensitivity of U_Q with respect to D significantly drops for more than 75% when approaching to D = 0.5 where the corresponding current through the electromagnet is zero. Therefore, the nonlinearity makes it inaccurate to control the small current through the electromagnet by an open-loop control on the duty cycle.

Fig. 8(b) shows the linear relationship between the current signal and the hall-effect signal, which can be well represented by the asymptotic line, $U_Q = 3.16U_{hall} - 6.15$, with a high coefficient of determination, $R^2 = 0.9932$. Therefore, the proposed signal-processing circuit demonstrates a suffi-



FIGURE 10. Close-loop current control for step-change U_S under three scenarios, (a) U'_{BB} and (b) U_Q . The curves are the averaged values for 11 consecutive cycles, whereas the sampling rate is 625 Hz. The step-change frequency is 1 Hz, whereas the step changes occur at t = 0.0 and 0.5 s, t = 0.1 and 0.6 s, and t = 0.2 and 0.7 s for red solid, blue dashed, and black dotted curves, respectively. The step changes are 3.3 V, 1.0 V, and 0.5 V for red solid, blue dashed, and black dotted curves, respectively, whereas the mean value of step-change U_S is 1.64 V. The two horizontal black dotted lines in (a) correspond to the upper and lower saturations of U'_{BB} . The four horizontal green dashed lines in (b) correspond to 1.14 V, 1.39 V, 1.89 V, and 2.14 V, respectively, whereas the vertical black dotted lines correspond to the saturations of U'_{BB} in (a).

cient accuracy that is comparable to the ACS712 hall-effect current sensor.

D. EXP. 3: CONSTANT U_S

Fig. 9 shows the close-loop current control for two constant setpoints, $U_S = 1.64$ V and 1.81 V. In Figs. 9(a & d), we observe that the control signal (U'_{BB}) has rigorous high-frequency noise due to the bang-bang control in (10). In order to remove the high-frequency noise on U_{BB} , the 2^{nd} RC filter is applied in the bang-bang control circuit in Fig. 3. From Table I, $R_{fil,2} = 480$ k Ω and $C_{fil,2} = 1$ nF lead to the cutoff frequency at $f_{fil,2} = 0.33$ kHz that is sufficient.

Moreover, in Figs. 9(c & f), we observe that noises on the current signal (U_Q) are significantly smaller (around 20 dB) than those on the control signal (U'_{BB}) . In particular, the 50 Hz fluctuations in Figs. 9(c & f) result from the 220 V AC power supply for the 24 V DC power supply. Hence, 20 ms is determined as the time span of Figs. 9(a, b, d & e).

Furthermore, though slight noises are observed on U_Q in Figs. 9(b & e), the averaged U_Q are much more stable than the averaged U'_{BB} in Figs. 9(a & d). Therefore, the proposed close-loop current control approach can stabilize U_Q at the constant setpoint and compensate the external noise by adjusting U'_{BB} .



FIGURE 11. Close-loop current control for sinusoidal U_S under two amplitudes and three frequencies, (a, c & e) U'_{BB} and (b, d &f) U_Q . The curves are the averaged values for 11 consecutive cycles, whereas the sampling rates are 156.25 Hz, 312.5 Hz, and 625 Hz for 0.5 Hz, 1.0 Hz, and 2.0 Hz sinusoidal frequencies, respectively. The sinusoidal amplitudes are 0.25 V and 0.5 V for red solid and blue dotted curves, respectively, whereas the green dashed curves illustrate sinusoidal U_S . The mean value of sinusoidal U_S is 1.64 V. The two horizontal black dotted lines in (a, c e) corresponds to the upper and lower saturations of U'_{BB} , whereas the vertical black dotted lines correspond to the saturations of U'_{BR} in (a, c e).

E. EXP. 4: STEP-CHANGE US

Fig. 10 shows the close-loop current control for step-change U_S under three scenarios. In Fig. 10(a), we observe that the step change in U_S results in the saturation of U'_{BB} according to the bang-bang control. Moreover, the saturation duration of U'_{BB} increases as the increase of the step change. Furthermore, U_Q is already close to respective U_S in Fig. 10(a). Therefore, the proposed close-loop current control approach can make full use of the bang-bang control without sacrificing the accuracy.

Besides, since $U_Q = 1.64$ V corresponds to $D \approx 0.5$ ($i_{MN} \approx 0$) in Fig. 2, Fig. 10(b) also indicates that the discharging capability of the full-bridge PWM inverter, i.e., $|i_{MN}|$ decreases, is much stronger than its charging capability, i.e., $|i_{MN}|$ increases. In other words, the weak charging capability significantly hinders the proposed close-loop current control approach.

F. EXP. 5: SINUSOIDAL US

Fig. 11 shows the close-loop current control for sinusoidal U_S under two amplitudes and three frequencies. We observe that U_Q can faithfully follow U_S for both A = 0.25 V and A = 0.5 V in Fig. 11(b), while U'_{BB} does not saturate in Fig. 11(a). However, U_Q for A = 0.5 V deviates from U_S in Fig. 11(d) while U'_{BB} for A = 0.5 V saturates in Fig. 11(c). Similarly, the deviations of U_Q from U_S in Fig. 11(f) and

the saturations of U'_{BB} in Fig. 11(e) occur simultaneously. Moreover, the saturations of U'_{BB} in Fig. 11(c & e) take place mainly during the charging of the electromagnet as shown in Fig. 10(b). Therefore, the proposed close-loop current control approach tries to entirely utilize the charging and discharging capabilities to follow the varying current setpoint but is significantly restricted by the weak charging capability of the full-bridge PWM inverter.

IV. CONCLUSION

This work proposes a close-loop control approach to accurately and timely tune the current through the electromagnet driven by the full-bridge PWM inverter. The proposed approach innovatively inserts a current-sensing resistor into the middle of the electromagnet to minimize the voltage fluctuation and obtain the current signal by an analog signal-processing circuit that is comparable to the hall-effect current sensor. Moreover, the bang-bang controller is realized by the differential comparator to entirely utilize the charging and discharging capabilities of the full-bridge PWM inverter. The experimental results demonstrate that the proposed approach can effectively overcome the current-duty cycle nonlinearity of the full-bridge PWM inverter and faithfully follow the current setpoint. However, the approach is significantly restricted by the weak charging capability of the full-bridge PWM inverter with the electromagnet, which may be addressed by increasing the power supply voltage.

CONFLICTS OF INTEREST

The authors declare no conflict of interest.

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