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COMMENTS AND CORRECTIONS

Corrections to "Design and Implementation of Seventeen Level Inverter With Reduced Components"

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In the above article [1], reference [2] was missing.

The third sentence in Section II should read: "The THD of the structure in [2] with multicarrier modulation technique can be reduced by incorporating a novel staircase modulation technique. Also, the bidirectional switch S_a is designed with only two diodes which reduces the overall components count of the system. Despite the proposed topology being developed for high power ratings, the TSV, losses, and THD are reduced which increases the overall efficiency of the system."

The last sentence in Section II-A, and *Mode 0* should read: "The expected nine level output voltage waveform is represented in FIGURE 3(a). The staircase modulation technique is represented in FIGURE 3(b). The control architecture of the proposed topology is shown in FIGURE 3(c)."

REFERENCES

[1] C. Dhanamjayulu, D. Prasad, S. Padmanaban, P. K. Maroti, J. B. Holm-Nielsen, and F. Blaabjerg, "Design and implementation of seventeen level inverter with reduced components," *IEEE Access*, vol. 9, pp. 16746–16760, 2021.

[2] S. P. Gautam, L. Kumar, and S. Gupta, "Hybrid topology of symmetrical multilevel inverter using less number of devices," *IET Power Electron.*, vol. 8, no. 11, pp. 2125–2135, Nov. 2015.

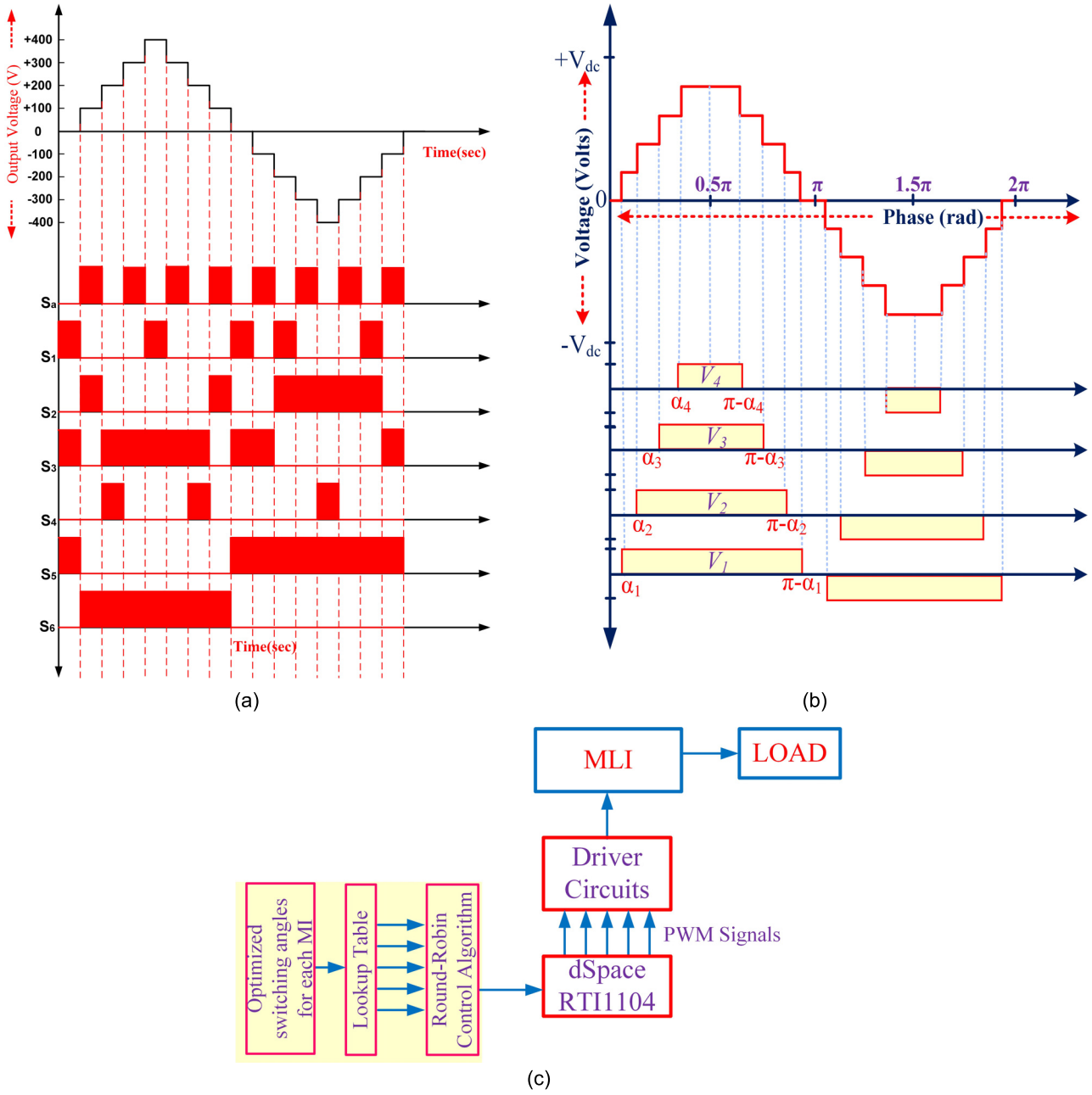

FIGURE 3. (a) Expected 9 level waveform of the proposed topology. (b) Staircase modulation. (c) Control block diagram.