# Improved Correlation Power Analysis on Bitslice Block Ciphers 

JAESEUNG HAN ${ }^{\text {® }}$, YEON-JAE KIM ${ }^{\text {® }}$, SOO-JIN KIM ${ }^{\text {© }}$, BO-YEON SIM ${ }^{\bullet}$ 2, AND DONG-GUK HAN ${ }^{(1,3}$<br>${ }^{1}$ Department of Financial Information Security, Kookmin University, Seoul 02707, Republic of Korea<br>${ }^{2}$ Department of Intelligent Convergence Research Laboratory, Electronics and Telecommunications Research Institute (ETRI), Daejeon 34129, Republic of Korea<br>${ }^{3}$ Department of Mathematics, Kookmin University, Seoul 02707, Republic of Korea<br>Corresponding author: Dong-Guk Han (christa@kookmin.ac.kr)

This work was supported by the Institute for Information and Communications Technology Promotion (IITP) grant funded by the Korean Government (MSIT), Development of SCR-Friendly Symmetric Key Cryptosystem and Its Application Modes, under Grant 2017-0-00520.


#### Abstract

Bitslice block ciphers have the advantage of allowing parallel computation using bitwise logical operations, and Boolean masking can be applied efficiently. Thus, various bitslice block ciphers, such as Robin, Fantomas, RECTANGLE, RoadRunneR, PRIDE, and CRAFT, have been proposed previously. Additionally, a bitslice implementation for AES, National Institute of Standards and Technology (NIST) standard block cipher, has been proposed. These ciphers construct an S-Box using only bitwise logical operators. They perform operations by storing the $i$-th bits of each S-Box input/output value in one register, i.e., they have a feature that each bit of an S-Box output is stored in a different register. Because of this feature, in correlation power analysis (CPA) for bitslice block ciphers, a single-bit of the S-Box output should be selected as an intermediate value. Moreover, depending on which bit is selected as the intermediate value, there are differences in analysis performance. Consequently, we propose an algorithm that predicts the CPA performance of each single-bit and we describe the theoretical basis of this algorithm. The effectiveness of the proposed algorithm is verified experimentally by comparing actual CPA results and predicted results on various bitslice block ciphers.


INDEX TERMS Bitslice block cipher, correlation power analysis, side-channel analysis, AES, Robin, Fantomas, RECTANGLE, RoadRunneR, PRIDE, CRAFT.

## I. INTRODUCTION

Side-channel analysis (SCA) is a technique that analyzes secret information (e.g., a secret key) using side-channel information, such as power consumptions, electromagnetics, and sounds. Paul Kocher [1] first described SCA and he demonstrated that it could be applied against mathematically secure cryptographic algorithms. Analyzing power consumption is a representative example of SCA that includes simple power analysis [1], differential power analysis [2], and correlation power analysis (CPA) [3]. Among these, CPA is a technique that calculates Pearson's correlation coefficient between power consumption traces and intermediate values of cryptographic algorithm to analyze secret key [3].

A block cipher is an algorithm that encrypts data in a given block unit and comprises linear and nonlinear functions.

The associate editor coordinating the review of this manuscript and approving it for publication was Yassine Maleh ${ }^{\text {(D) }}$.

Generally, S-Box operation is used as nonlinear function that affects all output bits when even 1-bit of input changes. Because of these characteristics, the S-Box output is typically used as the intermediate value when performing CPA. For block ciphers implemented without SCA countermeasures, secret information can be extracted using CPA. Among countermeasures against CPA, the most commonly used technique is Boolean masking, which is operated by dividing intermediate values into several shares.

The bitslice technique implements algorithms using bitwise logical operators. Bitslice block ciphers are designed to optimize a bitslice implementation. Therefore, bitslice block cipher algorithms mostly comprise bitwise logical operators. Particularly, the S-Box operations comprise logical operators that can perform operations in parallel. Because of these features, Boolean masking can be applied efficiently to bitslice block ciphers, resulting in the proposition of various bitslice block ciphers, such as Robin \& Fantomas [4], RoadRunneR [5], and CRAFT [6].

In these implementations, each bit of the S-Box input/output values is stored in a different register. Considering this, Balasch et al [7] performed CPA using a single-bit of S-Box output as an intermediate value of the bitslice implementation of AES [8]. However, there is no study about which single-bit is more effective for CPA. In this paper, we propose an algorithm that predicts CPA performance by each single-bit of the S-Box output.

## A. CONTRIBUTIONS

First, we propose a method that calculates CPA predictive performance determined hypothetical power consumption. We also explain the mathematical basis for this method. Using the proposed method, we propose an algorithm to calculate the CPA predictive performance for each single-bit intermediate value. Finally, we experimentally verify the proposed algorithm by comparing the actual single-bit CPA results of various bitslice block ciphers and the predictive results of the proposed algorithm.

## B. ORGANIZATION

The remainder of the paper is organized as follows. In Section II, we describe CPA, bitslice implementation, CPA on bitslice block ciphers, and our challenge. In Section III, we describe the proposed method and algorithm. In Section IV, we compare the CPA results for the Robin cipher and the predictive values obtained using the proposed algorithm. In Section V, we compare the CPA results and predictive values for bitslice implementation of AES, Fantomas, RECTANGLE [9], RoadRunneR, PRIDE [10], and CRAFT. In Section VI, we conclude this paper.

## II. PRELIMINARIES

## A. SYMBOLS AND NOTATIONS

Table 1 lists the notations used in this paper.

## B. CORRELATION POWER ANALYSIS

CPA is an SCA method that uses Pearson's correlation coefficient (PCC) between the hypothetical power consumption values $\mathbb{H}_{K}$ and the actual power consumption values $\mathbb{A}[3]$. The hypothetical power consumption values $\mathbb{H}_{K}$ are determined by key-dependent intermediate values and a power consumption model. It generally takes the output of a nonlinear function, such as S-Box, as the intermediate value. Example of power consumption models are Hamming weight (HW) model and Hamming distance model. For instance, if the intermediate value is the S-Box output and the power consumption model is the HW model, then the hypothetical power consumption $\mathbb{H}_{K}$ is the HW of the S-Box output when the key is $K$. If the key $K$ and power consumption model are correct, there is a linear relationship between hypothetical and actual power consumption values. The formula for the PCC between $\mathbb{A}$ and $\mathbb{H}_{K}$ is

$$
-1 \leq r_{\mathbb{A}, \mathbb{H}_{K}}=\frac{\operatorname{Cov}\left(\mathbb{A}, \mathbb{H}_{K}\right)}{\sigma_{\mathbb{A}} \sigma_{\mathbb{H}_{K}}} \leq 1
$$

TABLE 1. Definition.

| Symbol | Definition |
| :---: | :---: |
| $\oplus$ | eXclusive OR operation |
| $\wedge$ | AND operation |
| V | OR operation |
| $n$ | Input, output bit size of S-Box |
| $d$ | eXclusive OR difference between S-Box inputs |
| $\operatorname{Cov}(X, Y)$ | Covariance between $X$ and $Y$ |
| $\sigma_{X}$ | Standard deviation of $X$ |
| $r_{X, Y}$ | Pearson's correlation coefficient between $X$ and $Y$ |
| $\|x\|$ | Absolute value of $x$ |
| $r k$ (Right key) | Right key, $r k \in\{0,1\}^{n}$ |
| $g k$ (Guessed key) | Guessed key, $g k \in\left\{0,1,2, \cdots, 2^{n-1}\right\}$ |
| sk (Second key) | $g k$ having the highest absolute value of Pearson's correlation coefficient except $r k$ |
| A | A sequence of actual power consumption values |
| $\mathbb{H}_{K}$ | Hypothetical power consumption values when the key is set to $K$ |
| Ratio | $\left\|r_{\mathbb{A}, \mathbb{H}_{r k}}\right\| /\left\|r_{\mathbb{A}, \mathbb{H}_{s k}}\right\|$ |
| $S(x)$ | S-Box output for input $x$ |
| $H W(x)$ | Hamming weight value of $x$ |
| $s_{i}(x)$ | $i$-th bit of S-Box output for input $x(0 \leq i<n)$ |

In CPA, we compute the absolute value of $\mathrm{PCC}\left|r_{\mathbb{A}, \mathbb{H}_{g k}}\right|$ for every Guessed key gk. We expect that the absolute value of PCC $\left|r_{\mathbb{A}, \mathbb{H}_{r k}}\right|$ of the right key $r k$ is highest. The CPA process is as follows:

- Determine the intermediate value
- Determine the power consumption model
- Calculate the absolute value of PCC $\left|r_{\mathbb{A}, \mathbb{H}_{g k}}\right|$ of every Guessed key gk
We expect the Guessed key having the highest PCC to be the Right key. From this perspective, Ratio is used as a CPA performance indicator; it denotes a value that divides $\left|r_{\mathbb{A}, \mathbb{H}_{r k}}\right|$ by $\left|r_{\mathbb{A}, \mathbb{H}_{s k}}\right|$, which is the absolute values of PCC about the Right key rk and Second key sk, respectively. There is another indicator known as guessing entropy, which is the average rank position of the Right key. However, guessing entropy cannot distinguish between cases where the Right key is the first rank key. Conversely, the Ratio indicates how well the Right key can be analyzed as the first rank key. Therefore, we choose Ratio as the CPA performance indicator in this paper.


FIGURE 1. Structure of lookup table implementation.


FIGURE 2. Structure of bitslice implementation.

## C. BITSLICE IMPLEMENTATION OF CIPHERS

The basic idea behind bitslicing is expressing a function using only bitwise logical operators, e.g., the AND, XOR, OR, and NOT operators, to perform multiple instances of the function in parallel. Therefore, S-Box operations are parallelly operated in bitslice implementation. Conversely, implementation techniques such as the lookup table perform serial operations in units of S-Box input/output. Figure 1 and Figure 2 depict the register usage structure of the lookup table implementations and bitslice implementations, respectively. In the lookup table implementation, an $n$-bit input/output value of the S-Box is stored in a single register, whereas each bit of the $n$-bit input/output value of the S-Box is stored in a different register in bitslice implementation.

An example of bitslice block ciphers is Robin, which uses an 8 -bit S-Box. Figure 3 is the S-Box structure of Robin, where $x_{7}$ is the most significant bit (MSB), and $x_{0}$ is the least significant bit (LSB). Algorithm 1 shows the pseudocode for the Class-13 function of Robin's S-Box. In this S-Box structure, $n \times m$ bits S-Box outputs can be calculated in parallel because it only comprises the bitwise operations.

## D. CPA ON BITSLICE BLOCK CIPHER

Before CPA, we should determine the hypothetical power consumption using the intermediate value and power consumption model. Generally, to improve CPA performance, the output of a nonlinear function, such as S-Box, is preferred as an intermediate value, and the HW power consumption


FIGURE 3. Structure of Robin's S-box.

```
Algorithm 1 Robin's S-Box: Class-13 Algorithm
Input: \(x_{4}, x_{5}, x_{6}, x_{7}\) ( \(x_{7}\) is MSB)
Output: \(x_{4}^{\prime}, x_{5}^{\prime}, x_{6}^{\prime}, x_{7}^{\prime}\)
    \(x_{4}^{\prime}=\left(x_{4} \wedge x_{5}\right) \oplus x_{6}\)
    \(x_{6}^{\prime}=\left(x_{5} \vee x_{6}\right) \oplus x_{7}\)
    \(x_{7}^{\prime}=\left(x_{4}^{\prime} \wedge x_{7}\right) \oplus x_{4}\)
    \(x_{5}^{\prime}=\left(x_{6}^{\prime} \wedge x_{4}\right) \oplus x_{5}\)
    Return \(x_{4}^{\prime}, x_{5}^{\prime}, x_{6}^{\prime}, x_{7}^{\prime}\)
```

model is mainly employed in a software implementation. However, in the bitslice implementation (Figure 2), it is necessary to guess the $n \times m$ bits key to calculate the hypothetical power consumption of the register's value after the S-Box operation.

Because of the key guessing complexity, Adomnicai et al. [11] performed CPA for a bitslice block cipher PRIDE using the key XOR output as the intermediate value. In this case, the hypothetical power consumption of the register can be calculated by guessing the $m$-bit key. However, because of the feature of key XOR, which is a linear operation, the difference in the PCC between the Right key and other Guessed keys is not large, so the CPA performance decreases, and even specific Guessed key that has the same PCC to the Right key exists.

Conversely, Balasch et al. [7] performed CPA using the single-bit of S-Box output as the intermediate value for the bitslice implementation of AES. They used only the single-bit hypothetical power consumption of the register value; however, because the nonlinear function's output was used as the intermediate value, the CPA performance was relatively guaranteed, and the hypothetical power consumption can be calculated by guessing the $n$-bit key. Thus, we focus on a single-bit of the S-Box output as the intermediate values in CPA.

## E. CHALLENGE

Related work shows that effective CPA can be performed using a single-bit of the S-Box output as an intermediate value
for bitslice block ciphers. However, there is no study about the difference in the CPA performance for each single-bit of the S-Box output and the selection of a single-bit that is suitable for CPA as an intermediate value. Thus, we intend to propose an algorithm that receives an S-Box structure as an input and returns each single-bit CPA predictive performance as an output.

## III. EACH SINGLE-BIT CPA PERFORMANCE PREDICTION ALGORITHM

In this section, we propose a method that predicts CPA performance and describe a mathematical basis of this method. Further, through the method, we propose an algorithm that predicts the CPA performance of each single-bit of a target cipher's S-Box.

## A. CPA PERFORMANCE PREDICTION METHOD

As mentioned in Section II, we choose Ratio as the indicator of CPA performance. In other words, we predict the CPA performance by predicting the Ratio value. For simplicity, let $\mathbb{B}=\mathbb{H}_{r k}, \mathbb{C}=\mathbb{H}_{s k}$, and $\mathbb{C}_{d}=\mathbb{H}_{r k \oplus d}$. The CPA process calculates $\left|r_{\mathbb{A}, \mathbb{C}_{d}}\right|$ values about every $d,\left(0 \leq d \leq 2^{n}-1\right)$, and Ratio value is defined as $\left|r_{\mathbb{A}, \mathbb{B}}\right| /\left|r_{\mathbb{A}, \mathbb{C}}\right|$. Consequently, we propose a method that predicts $\left|r_{\mathbb{A}, \mathbb{B}}\right| /\left|r_{\mathbb{A}, \mathbb{C}}\right|$ without calculating $\left|r_{\mathbb{A}, \mathbb{B}}\right|,\left|r_{\mathbb{A}, \mathbb{C}}\right|$.

Proposition 1 suggests that it is possible to predict Ratio using $r_{\mathbb{B}, \mathbb{C}}$ without the CPA results $r_{\mathbb{A}, \mathbb{B}}$ and $r_{\mathbb{A}, \mathbb{C}}$. To prove Proposition 1, we assume the following.

1) The power consumption model is linear [12], [13], which allows $\mathbb{A}=\{\epsilon b+\delta: b \in \mathbb{B}\}$, where $\epsilon$ is a constant and $\delta$ is noise random variable.
2) Random variable $\delta$ is independent of $\mathbb{B}$ and $\mathbb{C}$ because the noise is unrelated to the process of predicting hypothetical power consumptions $\mathbb{B}$ and $\mathbb{C}$.
Since $\delta$ is independent of $\mathbb{B}$ and $\mathbb{C}, \operatorname{Cov}(\delta, \mathbb{B})=$ $\operatorname{Cov}(\delta, \mathbb{C})=0$. The relational formula between $r_{\mathbb{A}, \mathbb{B}}, r_{\mathbb{B}, \mathbb{C}}$ and $r_{\mathbb{A}, \mathbb{C}}$ is as expressed as follows.

Proposition 1: Let $\mathbb{A}, \mathbb{B}$ and $\mathbb{C}$ be random variables and $\mathbb{A}=\epsilon \mathbb{B}+\delta$ (where $\epsilon$ is a constant and $\delta$ is a random variable of noise). Let $\delta$ be independent of $\mathbb{B}$ and $\mathbb{C}$. Then $\left|\frac{r_{\mathbb{A}}, \mathbb{B}}{r_{\mathbb{A}}, \mathbb{C}}\right|=$ $\left|\frac{1}{r_{\mathbb{B}, \mathbb{C}}}\right|$.

Proof of Proposition 1:
$\mathbb{A}, \mathbb{B}, \mathbb{C}, \delta, X, Y, Z:$ Random variables
$a, b, c, \epsilon:$ Constants
Theorem 1) $\operatorname{Cov}(a X+b Y, c X)=a c\left(\sigma_{X}\right)^{2}+b c \operatorname{Cov}(X, Y)$
Theorem 2) $\operatorname{Cov}(a X+b Y, c Z)=a c \operatorname{Cov}(X, Z)+$ $b c \operatorname{Cov}(Y, Z)$
$\left|\frac{r_{\mathbb{A}}, \mathbb{B}}{r_{\mathbb{A}, \mathbb{C}}}\right|=\left|\frac{\frac{\operatorname{Cov}(\mathbb{A}, \mathbb{B})}{\sigma_{\mathbb{A}} \sigma_{\mathbb{B}}}}{\frac{\operatorname{Cov}(\mathbb{A}, \mathbb{C})}{\sigma_{\mathbb{A}} \sigma_{\mathbb{C}}}}\right|=\left|\frac{\sigma_{\mathbb{C}} \operatorname{Cov}(\mathbb{A}, \mathbb{B})}{\sigma_{\mathbb{B}} \operatorname{Cov}(\mathbb{A}, \mathbb{C})}\right|$

$$
\begin{aligned}
& =\left|\frac{\sigma_{\mathbb{C}} \operatorname{Cov}(\epsilon \mathbb{B}+\delta, \mathbb{B})}{\sigma_{\mathbb{B}} \operatorname{Cov}(\epsilon \mathbb{B}+\delta, \mathbb{C})}\right|(\because \mathbb{A}=\epsilon \mathbb{B}+\delta) \\
& =\left|\frac{\sigma_{\mathbb{C}}\left(\epsilon\left(\sigma_{\mathbb{B}}\right)^{2}+\operatorname{Cov}(\delta, \mathbb{B})\right)}{\sigma_{\mathbb{B}} \operatorname{Cov}(\epsilon \mathbb{B}+\delta, \mathbb{C})}\right|(\text { by Theorem 1) } \\
& =\left|\frac{\sigma_{\mathbb{C}}\left(\epsilon\left(\sigma_{\mathbb{B}}\right)^{2}+\operatorname{Cov}(\delta, \mathbb{B})\right)}{\sigma_{\mathbb{B}}(\epsilon \operatorname{Cov}(\mathbb{B}, \mathbb{C})+\operatorname{Cov}(\delta, \mathbb{C}))}\right|(\text { by Theorem 2) } \\
& =\left|\frac{\sigma_{\mathbb{C}} \epsilon\left(\sigma_{\mathbb{B}}\right)^{2}}{\sigma_{\mathbb{B}} \epsilon \operatorname{Cov}(\mathbb{B}, \mathbb{C})}\right|(\because \operatorname{Cov}(\delta, \mathbb{B})=\operatorname{Cov}(\delta, \mathbb{C})=0) \\
& =\left|\frac{\sigma_{\mathbb{C}} \sigma_{\mathbb{B}}}{\operatorname{Cov}(\mathbb{B}, \mathbb{C})}\right|=\left|\frac{1}{r_{\mathbb{B}}, \mathbb{C}}\right|
\end{aligned}
$$

Proposition 1 shows that the Ratio value $\left|r_{\mathbb{A}, \mathbb{B}} / r_{\mathbb{A}, \mathbb{C}}\right|$ can be calculated as $\left|1 / r_{\mathbb{B}, \mathbb{C}}\right|$ by some assumptions. Using Proposition 1, we can predict the Ratio value using the hypothetical power consumptions $\mathbb{B}, \mathbb{C}$ without the actual power consumptions $\mathbb{A}$. However, there is a limitation that we cannot know which of $\mathbb{C}_{d}$ is $\mathbb{B}$ or $\mathbb{C}$ without actual CPA, and we cannot calculate $\left|r_{\mathbb{B}, \mathbb{C}}\right|$ without the actual plaintexts. Therefore, we transform $\left|r_{\mathbb{B}, \mathbb{C}}\right|$ into simulation value $\left|r_{\mathbb{B}^{\prime}, \mathbb{C}^{\prime}}\right|$.

First, we assume that actual plaintexts are determined by the uniform distribution. Then we can define alternative sequences $\hat{\mathbb{B}}$ and $\widehat{\mathbb{C}}_{d}$ of hypothetical power consumption sequences $\mathbb{B}$ and $\mathbb{C}$ about the actual plaintexts. For example, if the intermediate value is the $n$-bit $S$-Box output, and the power consumption model is HW , then since possible plaintext is one of 0 to $2^{n}-1$, the alternative sequences $\hat{\mathbb{B}}$ and $\hat{\mathbb{C}}_{d}$ are defined as follows:

$$
\begin{aligned}
\hat{\mathbb{B}}:= & \{H W(S(0 \oplus r k)), H W(S(1 \oplus r k)), \ldots, \\
& \left.H W\left(S\left(2^{n}-1 \oplus r k\right)\right)\right\} \\
\hat{\mathbb{C}}_{d}:= & \{H W(S(0 \oplus r k \oplus d)), H W(S(1 \oplus r k \oplus d)), \ldots, \\
& \left.H W\left(S\left(2^{n}-1 \oplus r k \oplus d\right)\right)\right\}
\end{aligned}
$$

Since $\hat{\mathbb{B}}, \widehat{\mathbb{C}}_{d}$ have elements of each $\mathbb{B}, \mathbb{C}_{d}$ by the uniform distribution, we expect that $r_{\mathbb{B}, \mathbb{C}_{d}} \approx r_{\hat{\mathbb{B}}, \hat{\mathbb{C}}_{d}}$. Additionally, by rearranging sequences $\hat{\mathbb{B}}$ and $\hat{\mathbb{C}}_{d}$ based on $r k$ can be simplified as follows:

$$
\begin{aligned}
\mathbb{B}^{\prime}:= & \left\{H W(S(0)), H W(S(1)), \ldots, H W\left(S\left(2^{n}-1\right)\right)\right\} \\
\mathbb{C}_{d}^{\prime}:= & \{H W(S(0 \oplus d)), H W(S(1 \oplus d)), \ldots, \\
& \left.H W\left(S\left(2^{n}-1 \oplus d\right)\right)\right\}
\end{aligned}
$$

Since both $\hat{\mathbb{B}}$ and $\hat{\mathbb{C}}_{d}$ are rearranged on the same basis, we have $r_{\hat{\mathbb{B}}, \hat{\mathbb{C}}_{d}}=r_{\mathbb{B}^{\prime}, \mathbb{C}_{d}^{\prime}}$. We can calculate $r_{\mathbb{B}^{\prime}, \mathbb{C}_{d}^{\prime}}$ about every $d\left(1 \leq d \stackrel{\mathbb{B}}{\leq} 2^{h}-1\right)$ using only the target cipher's structure (in the case of the example, the S-Box). Next, we define that $\mathbb{C}^{\prime}$ is the $\mathbb{C}_{d}$ that has the highest value of $\left|r_{\mathbb{B}^{\prime}, \mathbb{C}_{d}^{\prime}}\right|$. Consequently, $r_{\mathbb{B}^{\prime}, \mathbb{C}^{\prime}}$ has the following relationship with the Ratio value.

$$
\text { Ratio }=\left|\frac{r_{\mathbb{A}, \mathbb{B}}}{r_{\mathbb{A}, \mathbb{C}}}\right|=\left|\frac{1}{r_{\mathbb{B}, \mathbb{C}}}\right| \approx\left|\frac{1}{r_{\hat{\mathbb{B}}, \hat{\mathbb{C}}}}\right|=\left|\frac{1}{r_{\mathbb{B}^{\prime}, \mathbb{C}^{\prime}}}\right|
$$

By calculating $\left|1 / r_{\mathbb{B}^{\prime}}, \mathbb{C}^{\prime}\right|$, we can predict the Ratio value without hypothetical power consumption $\mathbb{B}$ and $\mathbb{C}$ about actual plaintexts and actual power consumption $\mathbb{A}$.

## B. EACH SINGLE-BIT CPA PERFORMANCE PREDICTION ALGORITHM

In this section, we propose an algorithm that predicts each single-bit CPA performance on an $n$-bit S-Box using the proposed CPA performance prediction method. First, we define $\mathbb{B}^{\prime}$ and $\mathbb{C}_{d}^{\prime}$ for a single-bit of S-Box output and get the $r_{\mathbb{B}^{\prime}, \mathbb{C}^{\prime}}$ value by calculating every $r_{\mathbb{B}^{\prime}}, \mathbb{C}_{d}^{\prime}$. Then, the performance of this single-bit is predicted by $\left|1 / r_{\mathbb{B}^{\prime}, \mathbb{C}^{\prime}}\right|$. This process is repeated to predict the CPA performance for every single-bit. Assuming that the hypothetical power consumption is the $i$ th bit of the $n$-bit $S$-Box output, $\mathbb{B}^{\prime}$ and $\mathbb{C}_{d}^{\prime}$ of the $i$-th bit are defined as $\mathbb{S}_{i}, \mathbb{S}_{i}(d)$, respectively, as follows:

$$
\begin{aligned}
\mathbb{S}_{i}:= & \left\{s_{i}(0), s_{i}(1), \ldots, s_{i}\left(2^{n}-1\right)\right\} \\
\mathbb{S}_{i}(d):= & \left\{s_{i}(0 \oplus d), s_{i}(1 \oplus d), \ldots, s_{i}\left(\left(2^{n}-1\right) \oplus d\right)\right\} \\
& \left(0<d<2^{n}\right)
\end{aligned}
$$

Algorithm 2 is an algorithm that receives an $n$-bit S-Box as an input and returns each single-bit CPA performance as an output. In Algorithm 2, $c_{i}^{d}$ in line 3 means $r_{\mathbb{B}^{\prime}, \mathbb{C}_{d}^{\prime}}$ of the $i$-th bit, tmp in line 5 means $r_{\mathbb{B}^{\prime}, \mathbb{C}^{\prime}}$, and $R_{i}$ in line 6 means $\left|1 / r_{\mathbb{B}^{\prime}, \mathbb{C}^{\prime}}\right|$ the CPA performance prediction value of the $i$ - $t h$ bit. Algorithm 2 repeats this process for all $i$ and returns the CPA performance predictive values of each $i$-th bit.

```
Algorithm 2 Each Single-bit CPA Performance Prediction
Algorithm on \(n\)-bit S-Box
Input: An \(n\)-bit S-Box
Output: Each single-bit CPA performance \(R_{i}\)
    for \(i=0\) to \(n-1\) do
        for \(d=1\) to \(2^{n}-1\) do
            \(c_{i}^{d} \leftarrow\left|r_{\mathbb{S}_{i}, \mathbb{S}_{i}(d)}\right|\)
        end for
        tтp \(\leftarrow \max \left(c_{i}^{1}, c_{i}^{2}, \ldots, c_{i}^{2^{n}-1}\right)\)
        \(R_{i} \leftarrow 1 /\) tmp
    end for
    Return \(\left(R_{0}, R_{1}, \ldots, R_{n-1}\right)\)
```


## IV. EXPERIMENTAL RESULTS FOR BITSLICE BLOCK CIPHER ROBIN

In this section, we discuss the difference in CPA performance caused by a single-bit intermediate value through experiments on Robin and verify the effectiveness of the proposed algorithm.

Table 2 describes the experimental environment. As the target chip, we chose the Atmel XMEGA128, an 8-bit low power microcontroller unit (MCU). Additionally, using the ChipWhisperer-Lite board, we collected power traces and sampled the S-Box operation part of the first round while Robin encryption was performed on a total of 2,000 random plaintexts.

TABLE 2. Experimental environment.

| Target board | ChipWhisperer-Lite [14] |
| :---: | :---: |
| Target Chip | Atmel XMEGA128 <br> (8-bit MCU) |
| Number of traces | 2,000 |
| Sampling rate | $29,538 \mathrm{MS} / \mathrm{s}$ |
| Target cipher | Robin |
| Analysis point | 1 round S-Box |
| Power model | Single-bit |



FIGURE 4. Ratio of Robin CPA by each single-bit.

## A. COMPARING BETWEEN PREDICTION OF CPA PERFORMANCE AND ACTUAL CPA PERFORMANCE ABOUT EACH SINGLE-BIT INTERMEDIATE VALUE

We performed a 16 byte CPA by selecting each single-bit of the S-Box output as the intermediate value. Figure 4 shows that the Ratio of Robin CPA result differed depending on which single-bit is selected. We subsequently applied Algorithm 2 to Robin's S-Box and compared the results shown in Figure 4 and the predictive performance. First, we calculated $R_{i}$ of Robin using Algorithm 2, and then we compared $R_{i}$ with the results of actual CPA.

Algorithm 2 returns predictive CPA performance $R_{i}(0 \leq$ $i \leq 7$ ) shown in Table 3. Assuming that the 8 -bit S-Box output of Robin is represented by $Y=\left(y_{7} y_{6} y_{5} y_{4} y_{3} y_{2} y_{1} y_{0}\right)_{2}\left(y_{0}\right.$ is the LSB), $R_{i}$ means the predictive Ratio value of CPA that uses the $i$-th bit of S-Box output $y_{i}$ as the intermediate value. Robin CPA results can be divided into the following three sets depending on the size of $R_{i}$.

$$
Y_{1}=\left\{y_{4}, y_{6}\right\}, \quad Y_{2}=\left\{y_{0}, y_{5}, y_{7}\right\}, Y_{3}=\left\{y_{1}, y_{2}, y_{3}\right\}
$$

## 1) SELECT INTERMEDIATE VALUE FROM $Y_{1}=\left\{y_{4}, y_{6}\right\}$

Table 3 shows that $R_{4}$ is 1.0 . Furthermore, in Algorithm 2 about Robin's S-Box, $c_{4}^{d}$ has the maximum value when the XOR difference $d$ is $0 \times 04,0 \times 08$, or $0 \times 0 \mathrm{C}$. This suggests that if $y_{4}$ is selected as the intermediate value, the Ratio value will be $R_{4}=1.0$ and the XOR differences between the

TABLE 3. $\boldsymbol{R}_{\boldsymbol{i}}$ of robin.

| $i$ | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $R_{i}$ | 2.0 | 3.57 | 4.0 | 4.0 | 1.0 | 2.0 | 1.0 | 2.0 |



FIGURE 5. Max correlation coefficient by Guessed key (intermediate value: $\boldsymbol{y}_{4}$ ).

Second keys and the Right key will be $0 \times 04,0 \times 08$, and $0 \times 0 \mathrm{C}$. In other words, if $y_{4}$ is selected as the intermediate value, we can predict that the three Second keys will have the same absolute value of PCC as the Right key; also, the Second keys will have XOR differences of $0 \times 04,0 \times 08$, and $0 \times 0 \mathrm{C}$ from the Right key.

Figure 5 shows the result of performing the first-byte CPA with $y_{4}$ as the intermediate value. In Figure 5, the max correlation coefficient means the maximum value of the corresponding Guessed key's absolute value of PCC about each sample point. Thus, the Ratio value is calculated as the Right key's max correlation coefficient divided by the Second key's max correlation coefficient. Figure 5 shows the max correlation coefficients of each Guessed key. In practice, Figure 5 shows that keys $0 \times 2 \mathrm{~F}(=0 \times 2 \mathrm{~B} \oplus 0 \times 04), 0 \times 23(=0 \times 2 \mathrm{~B}$ $\oplus 0 \times 08)$, and $0 \times 27(=0 \times 2 \mathrm{~B} \oplus 0 \times 0 \mathrm{C})$ have the same absolute value of PCC to the Right key $0 \times 2 \mathrm{~B}$.

Similarly, $R_{6}=1.0$ and in Algorithm 2 about Robin's S-Box, $c_{6}^{d}$ has the maximum value when XOR difference $d$ is $0 \times 01,0 \times 08$, or $0 \times 09$. Thus, if $y_{6}$ is selected as the intermediate value, we can predict that $0 \times 2 \mathrm{~A}(=0 \times 2 \mathrm{~B}$ $\oplus 0 \times 01), 0 \times 23(=0 \times 2 \mathrm{~B} \oplus 0 \times 08)$, and $0 \times 22(=$ $0 \times 2 \mathrm{~B} \oplus 0 \times 09)$ will be the Second keys with the same absolute value of PCC to the Right key $0 \times 2 \mathrm{~B}$.

Figure 6 shows the result of performing the first-byte CPA with $y_{6}$ as the intermediate value. As we predicted, Figure 6 shows that keys $0 \times 22,0 \times 23$, and $0 \times 2 \mathrm{~A}$ have the same absolute value of PCC to the Right key $0 \times 2 \mathrm{~B}$.

## 2) SELECT INTERMEDIATE VALUE FROM $Y_{2}=\left\{y_{0}, y_{5}, y_{7}\right\}$

Table 3 shows $R_{0}=2.0$. Furthermore, in Algorithm 2 about Robin's S-Box, $c_{0}^{d}$ has the maximum value when $d$ is $0 \times 08$. Thus, we can predict that the Ratio value is close to 2.0 and


FIGURE 6. Max correlation coefficient by Guessed key (intermediate value: $y_{6}$ ).


FIGURE 7. Max correlation coefficient by Guessed key (intermediate value: $\boldsymbol{y}_{\mathbf{0}}$ ).
the XOR difference between the Second key and the Right key will be $0 \times 08$.

Figure 7 shows the result of performing CPA with $y_{0}$ as the intermediate value. The absolute value of PCC 0.272 for the Right key $0 \times 2 \mathrm{~B}$ is approximately 1.51 times greater than 0.18 for the Second key $0 \times 23(=0 \times 2 B \oplus 0 \times 08)$. Because of the effect of practical noise, the Ratio value 1.51 is less than the predictive value $R_{0}=2.0$.

Similarly, $R_{5}$ is 2.0 and $c_{5}^{d}$ has the maximum value when $d$ is $0 \times 02,0 \times 04,0 \times 06,0 \times 0 \mathrm{~A}, 0 \times 0 \mathrm{C}$, or $0 \times 0 \mathrm{E}$. As shown in Figure 8, the XOR difference between Second key $0 \times 29$ and Right key $0 \times 2 \mathrm{~B}$ is $0 \times 02$. Furthermore, the Ratio value is approximately 1.75 , which is close to $R_{5}=2$.

We observed similar results for $y_{7} . c_{7}^{d}$ has the maximum value when $d$ is $0 \times 01,0 \times 02,0 \times 03,0 \times 05,0 \times 06$ and $0 \times 07$. Figure 9 shows that the XOR difference between the Second key $0 \times 29$ and Right key $0 \times 2 \mathrm{~B}$ is $0 \times 02$. In Figure 9 , the Ratio value is approximately 1.84 , which is close to $R_{7}=2$.


FIGURE 8. Max correlation coefficient by Guessed key (intermediate value: $y_{5}$ ).


FIGURE 9. Max correlation coefficient by Guessed key (intermediate value: $y_{7}$ ).


FIGURE 10. Max correlation coefficient by Guessed key (intermediate value: $\boldsymbol{y}_{1}$ ).

## 3) SELECT INTERMEDIATE VALUE FROM $Y_{3}=\left\{y_{1}, y_{2}, y_{3}\right\}$

For $Y_{3}$, Table 3 shows that $R_{1}$ is 3.57 , and $R_{2}$ and $R_{3}$ are both 4.0 .

Figure 10 shows that when $y_{1}$ is selected as the intermediate value, the Ratio value is approximately 2.80 . Figure 11 shows that when $y_{2}$ is selected, the Ratio value is approximately 2.15 . Additionally, Figure 12 shows that when $y_{3}$ is selected, the Ratio is approximately 2.73 .

In the case of $Y_{3}$, because the Guessed keys, except for the Right key, have absolute value of PCC of a similar level, we do not predict which key is the Second key.


FIGURE 11. Max correlation coefficient by Guessed key (intermediate value: $\boldsymbol{y}_{2}$ ).


FIGURE 12. Max correlation coefficient by Guessed key (intermediate value: $y_{3}$ ).


FIGURE 13. CPA results for Robin $\left(Y_{1}, Y_{2}, Y_{3}\right)$.

Consequently, as shown in Figure 10, Figure 11, and Figure $12, Y_{3}$ has the larger Ratio value than $Y_{1}$ and $Y_{2}$. Therefore, selecting $y_{1}, y_{2}, y_{3}\left(\in Y_{3}\right)$ as the intermediate values improves CPA performance over other single-bits.

## B. RESULT OF TOTAL BYTES

Figure 13 shows the 16 byte CPA results for Robin when the bits are grouped according to $Y_{1}, Y_{2}$, and $Y_{3}$; blue, green, and orange graphs are for the $Y_{1}, Y_{2}$, and $Y_{3}$ groups, respectively. Thus, the CPA results for Robin show that CPA performance improves when $Y_{3}$ with relatively high $R_{i}$ values is selected as the intermediate value.


FIGURE 14. Ratio by bit according to the number of traces, the CPA results for Robin.

TABLE 4. Ratio of robin.

| $i$ | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Ratio of $y_{i}$ | 1.83 | 2.41 | 2.49 | 2.69 | 1.0 | 1.71 | 1.0 | 1.79 |

TABLE 5. $\boldsymbol{R}_{\boldsymbol{i}}$ of other ciphers.

| Cipher | $i$ | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| AES | $R_{i}$ | 8.0 | 8.0 | 8.0 | 8.0 | 8.0 | 8.0 | 8.0 | 8.0 |
| Fantomas | $R_{i}$ | 2.0 | 2.0 | 1.0 | 1.0 | 1.0 | 1.0 | 1.0 | 1.0 |
| RECTANGLE | $R_{i}$ | 1.0 | 1.0 | 2.0 | 2.0 | - | - | - | - |
| RoadRunneR | $R_{i}$ | 1.0 | 2.0 | 1.0 | 2.0 | - | - | - | - |
| PRIDE | $R_{i}$ | 2.0 | 2.0 | 1.0 | 1.0 | - | - | - | - |
| CRAFT | $R_{i}$ | 2.0 | 1.0 | 2.0 | 2.0 | - | - | - | - |

Figure 14 shows a graph of the average Ratio for each intermediate value $y_{i}$, the $i$-th bit of S-Box output, based on the number of traces when CPA is performed for Robin. In this paper, the average Ratio means the average value of Ratio on each key byte (or nibble if S-Box size is 4-bit) CPA of the first round key. The black line in Figure 14 indicates that the Ratio value is 1.6 . At 2,000 traces, $Y_{1}$ does not satisfy the Ratio value of 1.6 or greater. By contrast, when selecting the bit from $Y_{3}$, the minimum number of attack traces satisfying the Ratio value of 1.6 or greater is approximately 360 , and it is 510 when selecting the bit from $Y_{2}$. Consequently, when selecting bits from $Y_{3}$, the analysis performance is 1.41 times better than when selecting bits from $Y_{2}$.

Table 4 summarizes the Ratio results of each single-bit CPA using 2,000 traces for Robin.

## V. EXPERIMENTAL RESULTS FOR OTHER CIPHERS

In this section, we present experimental CPA results for other ciphers implemented by bitslice structures.

AES is the most representative block cipher that uses an 8-bit S-Box. We chose the target by the bitslice implementation of AES. Fantomas is a Robin family bitslice block cipher that uses an 8-bit S-Box. RECTANGLE, RoadRunneR, PRIDE, and CRAFT are block ciphers that use a 4-bit S-Box. We performed the CPA for the above block ciphers.

Table 5 shows the $R_{i}$ values of each block cipher's S-Box. Figure 15 shows the average Ratio based on the number of traces for each block cipher. The experimental environment is the same as in Section IV.

The predictive value for AES in Table 5 shows that the $R_{i}$ values are all equally high values of 8.0 because the AES's S-Box is designed as a mathematical structure to perfectly satisfy nonlinearity. As shown in Figure 15 (a), on AES CPA, every single-bit analyzed has an almost equal Ratio. This result is the same as predicted in Table 5, and AES has a higher Ratio than other ciphers because of higher $R_{i}$ values.

Table 5 shows that the $R_{i}$ values for Fantomas, RECTANGLE, RoadRunneR, PRIDE, and CRAFT are 2.0 or 1.0. According to the prediction result of Fantomas, $R_{i}$ has


FIGURE 15. Ratio by bit according to the number of traces, the CPA results against bitslice implementation ciphers.

TABLE 6. Ratio of other ciphers.

| Cipher | $i$ | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| AES | Ratio of $y_{i}$ | 3.62 | 3.57 | 3.53 | 3.48 | 3.59 | 3.62 | 3.65 | 3.62 |
| Fantomas | Ratio of $y_{i}$ | 1.74 | 1.71 | 1.0 | 1.0 | 1.0 | 1.0 | 1.0 | 1.0 |
| RECTANGLE | Ratio of $y_{i}$ | 0.99 | 1.0 | 1.57 | 1.60 | - | - | - | - |
| RoadRunneR | Ratio of $y_{i}$ | 1.0 | 1.41 | 1.0 | 1.46 | - | - | - | - |
| PRIDE | Ratio of $y_{i}$ | 1.39 | 1.46 | 1.0 | 1.0 | - | - | - | - |
| CRAFT | Ratio of $y_{i}$ | 1.49 | 1.0 | 1.53 | 1.47 | - | - | - | - |

the maximum value of 2.0 when $i$ is 0 or 1 ; thus, these bits are expected to have the best performance in CPA. Figure 15 (b) shows that at 2,000 traces, $y_{0}$ and $y_{1}$ have a Ratio value of approximately 1.6 , whereas other bits have a Ratio value of 1.0. Similarly, for RECTANGLE, $R_{i}$ has the maximum value of 2.0 when $i$ is 2 or 3 . In the actual CPA results (Figure 15 (c)) at 2,000 traces, $y_{2}$ and $y_{3}$ have a Ratio value of approximately 1.5 , and the other bits have a Ratio value of 1.0. For RoadRunneR, PRIDE, and CRAFT, the prediction results in Table 5 match the actual CPA results in Figure 15. Notably, in RoadRunneR, $y_{1}$ and $y_{3}$ have higher Ratio values than other bits. In PRIDE, $y_{0}$ and $y_{1}$ have higher Ratio values than other bits. Finally, in CRAFT, $y_{0}, y_{2}$, and $y_{3}$ have higher Ratio values than $y_{1}$.

Table 6 summarizes the Ratio results of each single-bit CPA using 2,000 traces for block ciphers. These results
demonstrated that the proposed Algorithm 2 correctly predicted the CPA results for each block cipher, i.e., AES, Fantomas, RECTANGLE, RoadRunneR, PRIDE, and CRAFT.

## VI. CONCLUSION

In this paper, we propose a method to predict the CPA performance. Additionally, using the method, we propose an algorithm to predict the CPA performance of each single-bit using a target bitslice block cipher's S-Box. Applying the proposed algorithm to Robin, Fantomas, AES, RECTANGLE, RoadRunneR, PRIDE, and CRAFT provided experimental confirmation of its efficacy. The proposed algorithm can assist systematic and improved CPA for bitslice block ciphers, and through this, it also can be utilized in the CPA security verification model for bitslice block ciphers.

Additionally, our findings can be extended to higher-order CPA for bitslice block ciphers.

## REFERENCES

[1] P. C. Kocher, "Timing attacks on implementations of Diffie-Hellman, RSA, DSS, and other systems," in Advances in Cryptology. Santa Barbara, CA, USA, Aug. 1996, pp. 104-113.
[2] P. C. Kocher, J. Jaffe, and B. Jun, "Differential power analysis," in Advances in Cryptology (Lecture Notes in Computer Science), vol. 1666, M. J. Wiener, Ed. Santa Barbara, CA, USA: Springer, 1999, pp. 388-397.
[3] E. Brier, C. Clavier, and F. Olivier, "Correlation power analysis with a leakage model," in Cryptographic Hardware and Embedded Systems. Cambridge, MA, USA, Aug. 2004, pp. 16-29.
[4] V. Grosso, G. Leurent, F. Standaert, and K. Varici, "LS-designs: Bitslice encryption for efficient masked software implementations," in Fast Software Encryption (Lecture Notes in Computer Science), vol. 8540, C. Cid and C. Rechberger, Eds. London, U.K.: Springer, Mar. 2014, pp. 18-37.
[5] A. Baysal and S. Sahin, "Roadrunner: A small and fast bitslice block cipher for low cost 8-bit processors," in Lightweight Cryptography for Security and Privacy (Lecture Notes in Computer Science), vol. 9542, T. Güneysu, G. Leander, and A. Moradi, Eds. Bochum, Germany: Springer, Sep. 2015, pp. 58-76.
[6] C. Beierle, G. Leander, A. Moradi, and S. Rasoolzadeh, "CRAFT: Lightweight tweakable block cipher with efficient protection against DFA Attacks," IACR Trans. Symmetric Cryptol., vol. 2019, no. 1, pp. 5-45, 2019.
[7] J. Balasch, B. Gierlichs, O. Reparaz, and I. Verbauwhede, "DPA, bitslicing and masking at 1 GHz ," in Cryptographic Hardware and Embedded Systems (Lecture Notes in Computer Science), vol. 9293, T. Güneysu and H. Handschuh, Eds. Saint-Malo, France: Springer, Sep. 2015, pp. 599-619. 2015.
[8] Advanced Encryption Standard, NIST FIPS PUB 197, NIST, Gaithersburg, MD, USA, 2001.
[9] W. T. Zhang, Z. Z. Bao, D. D. Lin, V. Rijmen, B. Yang, and I. Verbauwhede, "RECTANGLE: A bit-slice lightweight block cipher suitable for multiple platforms," Sci. China Inf. Sci., vol. 58, no. 12, pp. 1-15, Dec. 2015.
[10] M. R. Albrecht, B. Driessen, E. B. Kavun, G. Leander, C. Paar, and T. Yalçin, "Block ciphers-Focus on the linear layer (feat. PRIDE)," in Advances in Cryptology (Lecture Notes in Computer Science), vol. 8616, J. A. Garay and R. Gennaro, Eds. Santa Barbara, CA, USA: Springer, Aug. 2014, pp. 57-76.
[11] A. Adomnicai, B. Lac, A. Canteaut, J. Fournier, L. Masson, R. Sirdey, and A. Tria, "On the importance of considering physical attacks when implementing lightweight cryptography," Tech. Rep., 2016.
[12] M. Akkar, R. Bevan, P. Dischamp, and D. Moyart, "Power analysis, what is now possible," in Advances in Cryptology (Lecture Notes in Computer Science), vol. 1976, T. Okamoto, Ed. Kyoto, Japan: Springer, Dec. 2000, pp. 489-502.
[13] J. Doget, E. Prouff, M. Rivain, and F. Standaert, "Univariate side channel attacks and leakage modeling," IACR Cryptol. ePrint Arch., vol. 2011, p. 302, 2011.
[14] NewAE Techonology. ChipWhisperer-Lite (CW1173) Two-Part Version. [Online]. Available: https://rtfm.newae.com/Capture/ChipWhisperer-Lite/


YEON-JAE KIM received the B.S. degree in information security, cryptology, and mathematics from Kookmin University, Seoul, Republic of Korea, in 2021, where she is currently pursuing the master's degree in financial information security. Her research interests include side-channel attacks and public key cryptography.


SOO-JIN KIM received the B.S. degree in information security, cryptology, and mathematics from Kookmin University, Seoul, Republic of Korea, in 2021, where she is currently pursuing the master's degree in financial information security. Her research interests include side-channel attacks, symmetric key cryptography, and post quantum cryptography.


BO-YEON SIM received the Ph.D. degree in information security from Kookmin University, Seoul, Republic of Korea, in 2020. She worked as a Research Professor at Kookmin University, in 2020. She is currently working as a Researcher at the Electronics and Telecommunications Research Institute (ETRI). Her research interests include side-channel attacks, cryptography, reverse engineering, and implementation of information protection technology for embedded systems.


DONG-GUK HAN received the B.S. and M.S. degrees in mathematics from Korea University, Seoul, Republic of Korea, in 1999 and 2002, respectively, and the Ph.D. degree in engineering (information security) from Korea University, in 2005. From April 2004 to March 2005, he was an Exchange Student with the Department of Computer Science and Communication Engineering, Kyushu University, Japan. From 2006 to 2009, he was a Senior Researcher at the Electronics and Telecommunications Research Institute, Daejeon, Republic of Korea. He was also a Postdoctoral Researcher at Future University Hakodate, Hokkaido, Japan. He is currently working as a Professor with the Department of Information Security, Cryptology, Mathematics, Kookmin University, Seoul, Republic of Korea. He is a member of KIISC, IEEK, and IACR.

