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State-of-the-Art Review on Soft-Switching Technologies for Non-Isolated DC-DC Converters

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ABSTRACT The soft-switching technology is widely used in DC-DC converters, DC-AC inverters, and AC-DC rectifiers. Soft-switching non-isolated DC/DC converters (Buck, Boost, and Buck-Boost) are the most active research area of the soft-switching technology, and fruitful results are achieved. However, there is currently no comprehensive review for the soft-switching technology of non-isolated DC/DC converters. This paper introduces soft-switching topologies for non-isolated DC/DC converters, and presents classification and characteristic of all kinds of soft-switching technologies. Some soft-switching technologies of derivative converters (non-inverting Buck-Boost converters and multiphase converters) are also presented. Finally future trends and generalized constitution methods of the soft-switching topology for non-isolated DC/DC converters are concluded. The soft-switching theory and constitution methods summarized in this article are expected to be able to guide the proposing of new soft-switching technologies for DC-DC converters.

INDEX TERMS Review, state-of-the-art, non-isolated DC-DC converter, ZVS, ZVT, soft-switching.

I. INTRODUCTION

Benefit from the simple structure, non-isolated DC-DC converters are widely used in renewable energy applications, which need not an electrical isolation, such as the hybrid energy storage system (HESS), electric vehicles (EV) and the photovoltaic power generation. Non-isolated DC-DC converters mainly include 6 types: Buck converter, Boost converter, Buck-Boost converter, Cuk converter, Sepic converter, and Zeta converter [1], [2]. All these DC-DC converters will become current reversible bidirectional DC-DC converters by replacing the power diode with a power transistor. Unidirectional non-isolated converters can only transfer the power from the DC power supply to the load with the output voltage step-up or step-down functions. And

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bidirectional DC-DC converters have bidirectional power flow transmission capability also with step-up or step-down functions of the voltage at both sides.

In renewable energy applications, the switching frequency is required to be high to reduce the volume and mass of the non-isolated DC-DC converter prototype, meanwhile the voltage and current will be very large in these applications. These factors will cause severe switching losses in switching devices (power transistors and power diodes). The switching loss generally is the main loss in high-voltage, high-current, and high-frequency non-isolated DC-DC converters. In order to obtain an efficient and practical non-isolated DC-DC converter, the switching loss must be eliminated or reduced. We take the MOSFET as an example transistor to explain the mechanism of the switching loss. During the turn-on process of a MOSFET, the drain-source voltage V_S is decreasing, and the drain-source current (In this paper, it indicate the

total current of the switching unit) i_S is increasing. During the turn-off process of a MOSFET, the drain-source voltage *V^S* is increasing, and the drain-source current is decreasing. The overlapping portions of voltage and current will appear and produce the switching loss, which will be converted to heat. Therefore, the switching loss will not only affect the efficiency, but also increase the heat dissipation burden. A simple estimated method of the switching loss will be [3]:

$$
P_{SL} = \frac{1}{2} V_B I_L f(t_{ON} + t_{OFF})
$$
\n(1)

where: V_B is the bus voltage; I_L is the load current; f is the switching frequency; t_{ON} is the turn-on time; t_{OFF} is the turnoff time.

Based on [\(1\)](#page-1-0), we can know that the switching loss is proportional to V_B , I_L , f , and the switching time (the turnon time and the turn-off time). The use of fast switching devices (such as SiC devices) can reduce the switching loss by reducing the switching time. However, this method cannot eliminate the switching loss. With the increase of the power and the switching frequency, the switching loss will still become very serious. Reducing or even eliminating the overlapping area of *V^S* and *i^S* during the switching process is another method to reduce the switching loss, which is called the soft-switching technology. There are four soft-switching forms: zero voltage switching on (ZVS-on), ZVS-off, zero current switching on (ZCS-on), and ZCS-off. For a practice MOSFET, snubber capacitors are usually used to connect with drain-source poles. Then these snubber capacitors can transfer most of the turn-off loss to the turn-on process [3]. Therefore, this turn-off loss can be reduced by a turn-on soft-switching method. Soft-switching conditions of turnon process become more important. Thanks to the similar structure, most of soft-switching methods and important conclusions proposed for Buck, Boost, and Buck-Boost converters can also be used in Cuk, Sepic, Zeta converters and other derivative non-isolated DC-DC converters.

In recent decades, soft-switching technologies have achieved rapid development. Many modern DC-DC converters have adopted soft-switching technologies to improve efficiency and reduce heat generation. However there is no literature on the state-of-the-art review of soft switching technologies for non-isolated DC-DC converters. Therefore this paper summarizes and classifies main soft-switching technologies for non-isolated DC-DC converters, and gives the introduction of the latest soft-switching technologies. Then several soft-switching topology derived methods are summarized and presented. The contribution of this paper is:

[\(1\)](#page-1-0) A comprehensive state-of-the-art review of softswitching topologies for non-isolated DC-DC converters is presented.

[\(2\)](#page-2-0) Based on the state-of-the-art review, several generalized soft-switching topology derived methods are summarized and presented for non-isolated DC/DC converters.

[\(3\)](#page-2-1) Based on the soft-switching theory and constitution methods summarized in this article, considering the industrial

practice, future development trends and areas of softswitching technologies is discussed.

(4) The comprehensive summary of soft-switching technologies and converters in this paper will provide theory basics for the exploration and discovery of new soft-switching technologies in the future, and promote further applications of soft-switching technologies in various industry areas.

This paper concerns soft-switching technologies used for Buck, Boost, Buck-Boost and their derived converters. And most of these soft-switching technologies can also be applied in Cuk, Sepic, and Zeta converters. The structure of this paper is organized as: Section II gives the basic soft-switching principle and the classification of softswitching topologies for non-isolated DC-DC converters. Base on this classification, section III give an introduction of control-only soft-switching topologies. Section IV presents the non-auxiliary-switch soft-switching technologies, and section V introduces auxiliary-switch soft-switching technologies. Finally section VI and VII discuss features, topology constitution methods, and future trends of softswitching technologies for non-isolated DC/DC converters, and summarizes all the work of this paper.

II. BASIC PRINCIPLES OF SOFT-SWITCHING TECHNOLOGIES

A. SOFT-SWITCHING WAVEFORMS AND SAVED LOSSES

At the first, we defines the switching unit in this section as a MOSFET (or other transistors) with necessary auxiliary structures such as the body diode (or the anti-parallel diode), the snubber inductor, or the snubber capacitor. In this section, we will present the soft-switching principle of non-isolated DC-DC converters at the view of switching waveforms $(V_G,$ V_S , i_S) and switch structures. The target of soft-switching technology is to reduce or even eliminate the overlapping area of *V^S* and *i^S* during the switching period. Therefore four soft-switching forms can be obtained as ZVS-on, ZCS-on, ZVS-off, and ZCS-off, which are shown in FIGURE 1. It should be noted that waveforms in FIGURE 1 is used to indicate the interrelation of V_G , V_S , i_S , therefore the shape of V_G will not be a standard rectangle, and the shape of V_S can be rectangular, sinusoidal or others. Then we will presents detail waveform characteristics and corresponding descriptions of each soft-switching form in the following.

1) ZVS-OFF CONDITION

The usual method of the ZVS-off condition is to connect a snubber capacitor to the MOSFET (*S*) in parallel. According to [3], the switching loss saved by this ZVS-off condition will be transfer to the turn-on process, so a ZVS-on condition is required with this condition.

2) ZCS-ON CONDITION

The switching unit in this condition is composed of a MOSFET (*S* with the body diode) and a small snubber inductor (*L*). This snubber inductor is connected with the

MOSFET in series, and is used to limit the rise rate of the turn-on current. Due to the existence of *L*, the increase rate of *i^S* is reduced, and the ZCS-on conditions is obtained. All the typical ZCS method uses a snubber inductor, which will be the main inductor, an auxiliary inductor, or a mutual inductor generated by other inductors in the actual converter, to cascade with the MOSFET or other transistors. One notable aspect is that the energy of the snubber inductor should be released before the next turn-on process.

FIGURE 1. Several main soft-switching waveforms.

3) ZVS-ON CONDITION

This condition requires that the switching unit is composed of a MOSFET (including the body diode) and a snubber capacitor. A reverse current is generated to discharge the capacitor before the MOSFET is turned on. Then the voltage of the snubber capacitor is discharged to 0. And the body diode will conduct, the drain-source voltage (V_S) is clamped to be 0. The MOSFET will turn on under ZVS-on condition. The realization key of the ZVS-on condition is the reverse current generated before the switching unit is turned on.

4) ZCS-OFF CONDITION

The structure of the switching unit in this condition is same to the ZCS-on condition without the snubber inductor. Before starting of the turn-off process, the switching unit is conductive, and the drain-source current i_S reduced when closing to the switching time point. Before the MOSFET is turned off, *i^S* decreases to be a small negative value, then the switching unit can turn off under ZCS-off condition.

Abovementioned soft-switching forms also need other auxiliary circuits or control strategies in addition to the switching unit structure. For the ZVS-off condition, a softswitching turn-on condition is required; otherwise the turn-off saved loss will occur in the turn-on process. For the ZCS-on condition, appropriate auxiliary circuit or control strategies are required to achieve 0-reset by releasing the energy of the snubber inductor before the next turn-on process. For ZVS-on condition and ZCS-off condition, auxiliary circuit or control strategies are required to generate the reverse current across the switching unit.

B. SAVED LOSSES BY SOFT-SWITCHING TECHNOLOGY

The soft-switching technology can save switching loss. This section will give saved loss by different soft-switching conditions. Based on [\(1\)](#page-1-0), the saved loss of the ZVS-off condition will be:

$$
P_{SL} = \frac{1}{2} I_L f(V_{BH} t_{OFFH} - V_{BS} t_{OFFS})
$$
 (2)

VBH is the bus voltage under hard-switching condition, and *VBS* is the equivalent bus voltage under ZVS-off condition; *tOFFH* is the turn-off time under hard-switching condition, and *tOFFS* is the turn-off time under ZVS-off condition. From this equation, we can know that V_{BS} will be smaller than V_{BH} when the increase rate of V_S is reduced. t_{OFFS} will be also different from *tOFFH* . But the turn-off loss will be reduced in summary.

Similarly the saved loss of the ZCS-on condition will be:

$$
P_{SL} = \frac{1}{2} V_B f (I_{LH} t_{ONH} - I_{LS} t_{ONS})
$$
 (3)

ILH is the load current under hard-switching condition, and *ILS* is the equivalent load current under ZCS-on condition; *tONH* is the turn-on time under hard-switching condition, and *tONS* is the turn-on time under ZCS-on condition. From this equation, we can know that I_{LS} will be smaller than I_{LH} when the increase rate of *i^S* is reduced. *tONS* will be different from *tONH* . But the turn-on loss will be reduced in summary.

In summary, ZVS-on and ZCS-off conditions can almost completely eliminate the switching loss, but ZVS-off and ZCS-on conditions can only reduce the switching loss. Saved losses for ZVS-on and ZCS-off conditions will be all the switching loss, therefore ZVS-on and ZCS-off conditions are most meaningful methods. The saved loss of the ZVS-on and ZCS-off condition will respectively be:

$$
P_{SL} = \frac{1}{2} V_B I_L f t_{ON}
$$
 (4)

$$
P_{SL} = \frac{1}{2} V_B I_L f t_{OFF}
$$
 (5)

C. SOFT-SWITCHING TECHNOLOGY CLASSIFICATIONS

At present, the research of soft-switching non-isolated DC-DC converters has been very rich. The classification of soft-switching non-isolated DC-DC converter is very complicated and overlapping each other. To simplify this process, based on the auxiliary circuit structure (with or without auxiliary circuits; with or without active devices), these soft-switching technologies can be classified into 3 main categories as: control-only technologies (COT), non-auxiliary-switch technologies (NAST), auxiliary-switch

technologies (AST), which are shown in FIGURE 2. Detailed descriptions of these soft-switching technologies are:

1) COT

The control-only technology (COT) provides soft-switching conditions for the non-isolated DC-DC converter without using any auxiliary circuit. By appropriate parameter design and switching timing control modulation method, all nonisolated DC-DC converters can operate under ZCS-on or ZVS-on conditions. The key of this technology is the control strategy, therefore this method is the simplest method in topological structure. The control-only technology needs complex control strategies, and has a large peak-to-peak current across the main inductor. Thus the efficiency is not very high with fixed frequency control method.

FIGURE 2. Soft-switching technology classifications.

2) NAST

In order to further improve the efficiency of non-isolated DC-DC converters, the auxiliary circuit should be added in the converter. If the auxiliary circuit does not contain a switch that can be actively controlled such as MOSFET and IGBT, this is the non-auxiliary-switch technology (NAST). Therefore the NAST uses auxiliary circuit without active devices. Based on the implementation principle, the NAST includes quasi-resonant converters (QRC), passive snubber converters (PSC), and auxiliary commutation converters (ALCC). QRC usually uses small inductors and capacitors connected with the switching unit in series or parallel. Then ZCS or ZVS conditions can be obtained. PSC uses a snubber net composed of inductors, diodes, and capacitors to achieve ZVS or ZCS conditions. ALCC uses auxiliary circuits to generate an additional auxiliary current between two semiconductor devices to composite current waveforms needed by ZCS or ZVS conditions.

3) AST

If the auxiliary circuit contains switches that can be actively controlled, the auxiliary-switch technology (AST) can be

TABLE 1. Soft-switching form and losses.

obtained. This technology includes zero voltage transition converters (ZVTCs), zero current transition converters (ZCTCs), active resonant converters (active RC) and active snubber converters (active SC). The ZVTC achieves ZVSon conditions (FIGURE 1) with the help of the auxiliary switches. The ZCTC achieve ZCS-off conditions (FIGURE 1) with the help of the auxiliary switches. The active RC adds active switches to the resonant converters to obtain a fixed frequency control characteristics. And the active SC is the PSC with an active switches added in the auxiliary circuit.

In our papers, the main classification is clear and unambiguous, because it is based on the structure of with/without auxiliary circuits or with/without active devices. However the sub classification has overlapping phenomena. Different softswitching technologies may use the same theory as shown in FIGURE 2. For example, the PSC and the active SC both use the snubber technology, and the QRC and the active RC both use the resonant principle. Soft-switching form and losses for each soft-switching technology is shown in TABLE 1. In following sections, we will give detailed introductions of abovementioned several soft-switching technologies.

III. CONTROL-ONLY TECHNOLOGIES

As mentioned above, the control-only soft-switching DC-DC converter has no auxiliary circuits but the control modulation strategy for the switching unit. There are three basic soft-switching modulation strategies: discontinuous conduction mode (DCM), critical conduction mode (CRM), and triangular current mode (TCM) [4]–[6]. The waveform of current across the main inductor is triangular, and the whole waveform will be above the zero-current axis in continuous conduction mode (CCM). If the inductance of the main inductor is reduced, or the load resistance increased, this current waveform will move down. When the minimum value of the waveform is equal to 0, we obtain the CRM waveform. By changing the switching frequency based on the load condition, the CRM DC-DC converter can obtain ZCS-on condition at wide load range. Continue to move

down this current waveform, the DCM waveform will be obtained if the DC-DC converter is unidirectional, and the TCM waveform will be obtained if the DC-DC converter is bidirectional. The DCM DC-DC converter can also obtain ZCS-on condition, and the TCM DC-DC converter can achieve ZVS-on condition.

The TCM DC-DC converter can eliminate almost all turnon losses and most turn-off losses. Constant frequency control TCM non-isolated DC-DC converters have low light-load efficiency. In order to improve the light load efficiency of the Boost converter, Alberto Rodriguez *et al.* [5] proposes a quasi-square wave mode with ZVS (QSW-ZVS), which is the TCM with fixed minimum value of current waveform and variable frequency. This method can also be used on other non-isolated DC-DC converters. Besides, the TCM can also be used in the non-inverting Buck-Boost converter, which is shown in FIGURE 3. The non-inverting Buck-Boost converter can be regarded as a cascaded DC-DC converter of a Buck converter and a Boost converter without the lowside filter capacitor. Based on the switching characteristics of the non-inverting Buck-Boost converter, 6 modulation strategies are proposed [7]. And the waveform of a softswitching modulation strategy is shown in FIGURE 3. Based on these modulation strategies, some practical soft-switching control modulation strategies for the non-inverting Buck-Boost converter are proposed by [8]–[10]. These control modulation strategies obtained a high efficiency up to 98% at the power of 4 kW.

FIGURE 3. The non-inverting Buck-Boost converter.

IV. NON-AUXILIARY-SWITCH TECHNOLOGIES

The non-auxiliary-switch soft-switching technology includes the QRC, the PSC, and the ALCC. This section will present soft-switching non-isolated DC-DC converters using these technologies. We mainly concerned with the latest technological developments, therefore new converters proposed in recent years will be given descriptions in detail.

A. QUASI-RESONANT CONVERTERS

The QRC firstly proposed by F.C.Y. Lee and his partners use the resonant switching unit, which is composed of transistors, inductors, and capacitors, to replace the general switch of non-isolated DC-DC converters [1], [11]–[13]. QRCs have two types: ZCS-QRCs and ZVS-QRCs, whose switching units are shown in FIGURE 4 [11]–[13]. In FIGURE 4, *L^r* is the resonant inductor, and *C^r* is the resonate capacitor. *T* is the unidirectional switch, and can be configured to halfwave mode and full-wave mode. In ZCS-QRCs, the resonant inductor is cascaded with the unidirectional switch to reduce the current rising rate, and then the ZCS condition is obtained. The resonant capacitor is used to release the energy stored in the resonant inductor before the next turn-on process. Then the ZCS conditions will occur in every period. In ZVS-QRC, The resonant capacitor is connected in parallel to the unidirectional switch. The resonant tank between the resonant capacitor and the resonant inductor is used to generate ZVS conditions. By applying these switching units of FIGURE 4 to non-isolated DC-DC converters, a large number of softswitching QRC topologies can be obtained. The resonant frequency of the QRC is usually higher than the switching frequency. In order to obtain detailed characteristics, Be-Tao Lin *et al.* [14] proposed a unified modeling, synthesizing, and analyzing method for QRCs.

FIGURE 4. Switching units of QRCs.

Traditional QRCs has disadvantages such as variable switching frequency and increased current or voltage stress. Based on the similar principle, a lot of new improved QRCs are proposed as multi-resonant converters (MRCs) [15]–[17], constant frequency bidirectional resonant converters (CF-BRCs) [16]–[18], new resonant DC-DC converters with LC circuit [19], [20], and new single switch quasi-resonant converters (SS-QRCs) [21]–[24]. MRCs usually employ a multi-element resonant network, and the ZVS-MCR is suitable for high switching frequency converters. CF-BRCs solved the variable frequency control problems of traditional QRCs.

The SS-QRC is an ancient but very useful soft switching method, until now, new topologies based on the SS-QRC theory are still being proposed. In recent years, the SS-QRC is proposed to obtain characteristic of reduce inductor size, inherent power factor correction (PFC) features, and etc. The SS-QRC employs small inductors and capacitors to reduce the weight and volume. And full soft-switching capability with ZVS/ZCS conditions is obtained. So the efficiency and power density is improved.

B. PASSIVE SNUBBER CONVERTERS

Passive snubber converters use a passive snubber net (PSN), which consist of diodes, inductors and capacitors, to obtain ZCS-on and ZVS-off conditions. The reverse recovery loss of diodes in the snubber net will also be reduced by reasonable design. As shown in FIGURE 5, the transistor will usually be cascaded to a small inductor (or a virtual inductor), which will be implemented by an actual inductor, the mutual inductance or other method. Besides, the position of transistor and inductor can be interchanged. The inductor is used to reduce the current ascent rate, and the capacitor is used to reduce the voltage ascent rate, then ZCS-on and ZVS-off conditions can be obtained. Meanwhile the PSN must have voltage and current reset function for inductors and capacitors in one period to prepare for the soft-switching of the next period. Based on the using of coupled inductor, there are two type of PSN: PSN type I that has no coupled inductor; PSN type II that uses coupled inductors.

FIGURE 5. Passive snubber structure.

FIGURE 6. Typical passive snubber converters. (a) A PSN boost converter [31]. (b) A PSN buck converter with coupled inductors [38]. (c) A simplified structure PSN boost converters with coupled inductors [42].

The typical PSN DC-DC converter has a complex structure, which employed several inductor, capacitor, and diodes [25]–[33]. A PSN boost converter is shown in FIGURE 6 (a) [31]. The L_r will reduce the current rising rate when *S* is turned on; C_{r1} and C_{r2} will reduce the voltage rising rate when *S* is turned off; D_2 and D_3 perform auxiliary circuit reset function. In order to improve the performance or simplify the structure of typical PSN DC-DC converters, the coupled inductor is introduced. The coupled inductor PSN

converters use the snubber inductor coupled with the main inductor [34]–[37], or other inductors [38]–[41], one example is shown in FIGURE 6 (b).

The PSC is very commonly used soft-switching method, but almost all books will not list it as the soft-switching technology. In this paper, we give the detailed introduction. In recent years the new PSC also keep appearing, Mohammad Reza Amini *et al.* [42] gave a very simple PSN using a coupled inductor as shown in FIGURE 6 (c), this PSN has a very simple structure with employing a coupled inductor and a diode. The ZCS condition will be obtained, and the efficiency and power density is improved. Bo H. Choi *et al.* [43], [44] used a tapped inductor instead of the coupled inductor to reduce the weight and volume. These new PSN DC-DC converters further optimize the performance of the PSN.

FIGURE 7. Auxiliary commutation structure and waveforms.

C. AUXILIARY COMMUTATION CONVERTERS

Auxiliary commutation converters (ALCs) employ auxiliary circuits to change waveforms across power transistors. As shown in FIGURE 7, assume that the main inductor is large enough to be considered as a current source at CCM. If an auxiliary current (i_a) generated by the auxiliary circuit has following characteristics: *i^a* will make the composited current $(i_a + i_b)$ to be negative when S_1 is turning on, and be positive when S_2 is turning on, the current across S_1 and S_2 will clamp drain-source voltages of S_1 and S_2 to be 0. The ZVS turn-on condition will be obtained. Based on the classification of auxiliary circuits, ALC has two types: capacitor inductor ALC (CL-ALC), and coupled inductor and diode ALC (CLD-ALC). CL-ALCs employ the auxiliary circuit composed of capacitors and inductors shown in FIGURE 8 (a) and (b) [45]–[47], and have the same ZVS turn-on waveforms to FIGURE 7. Besides, CL-ALCs can also be configured to be unidirectional operation [48], [49], bidirectional operation [45], [46], [50] or ZCS operation [51]. The CL-ALC in [50] can also obtain the filter characteristic. And CL-ALCs in [47], [52] use the magnetic coupling effect to improve the converter performance.

CL-ALCs always have a larger ripple even if they are configured to be unidirectional operation, where the

positive ripple of *i^a* is not necessary. Then the CLD-ALC is proposed to eliminate the positive section of *i^a* at unidirectional applications. CLD-ALCs are relatively new soft-switching method proposed from the year of 2003, and can provide ZCS conditions (ZCS CLD-ALCs in [56], [57], can also be considered as PSN DC-DC converters) and ZVS conditions [53]–[65], two examples of which are shown in FIGURE 8 (c) and (d) [53]–[56]. Both ZCS CLD-ALCs and ZVS CLD-ALCs employ a coupled inductor and a power diode as the auxiliary circuit. CLD-ALCs can also be extended to the non-inverting buck-boost converter (NIBBC) [59], [60] or replace the auxiliary power diode to be a transistor [61], [62]. Some bidirectional buck/boost converters based on CLD-ALC are also proposed by [63]–[65].

FIGURE 8. Auxiliary commutation converters. (a) and (b) CL-ALCs [45]–[47]. (c) and (d) CLD-ALCs [53]–[56].

The CL-ALC is a method that was proposed very early, but the CLD-ALC is firstly proposed by Y. Zhang, and P. C. Sen in 2003 [53]. In recent years, Lei Jiang, H. Do, Y. Berkovich, G. Chen, Xu-Feng Cheng, *et al.* did a lot of research on the CLD-ALC, and proposed numerous number of new CLD-ZLC topologies. This research promotes the development and application of the CLD-ALC and enriches related theories. In the future more new topologies will continue to be proposed.

V. AUXILIARY-SWITCH TECHNOLOGIES

The NAST cannot freely control the generation and elimination of the auxiliary current, which will introduce extra auxiliary losses. Besides, some soft-switching converters need auxiliary switches to obtain the constantfrequency control characteristic. Therefore this section will give the detailed review of ASTs. ASTs include ZVTCs, ZCTCs, active resonant converters (RCs) and active snubber converters (SCs).

A. ZVT CONVERTERS

The ZVTC uses auxiliary power transistors to create ZVS conditions for main power transistors. Auxiliary power transistors will be turned on before main power transistors, then the auxiliary current across the snubber capacitor and the resonant inductor will clamps the voltage of main transistors to 0, which obtains ZVS conditions. By appropriate design, auxiliary power transistors will also obtain ZVS or ZCS conditions. By auxiliary power transistor, the generation of auxiliary currents is limited to a short time, and then the auxiliary loss is greatly reduced. However auxiliary power transistors need extra control and driving circuits, which will increase the cost and control complexity. Typical ZVTC auxiliary circuits usually consists of one resonant inductor, two power transistors (or one transistor plus one diode), which are shown in FIGURE 9. A resonant tank will occur between the resonant inductor and the snubber capacitor (or the parasitic capacitance C_{DS}) connected with main transistors. One transistor is used to control the generation of the resonant tank, the other is used to release the energy in the resonant inductor. Then the ZVTC will obtain a ZVS conditions at the turn-on process.

FIGURE 9. ZVTC auxiliary circuits.

The ZVTC is also a traditional soft-switching technology accompanied by a lot of existing topologies. The research direction of these years is to obtain bidirectional softswitching operation and improve the performance using the magnetic coupling method and new auxiliary circuit structure. Apply ZVTC auxiliary circuits to the specific converter, lots of ZVT non-isolated DC-DC converters are proposed by [6], [66]–[80]. Four reprehensive ZVTCs are shown in FIGURE 10 [66]–[71]. In 2016, M. R. Mohammadi proposed a family of new ZVT converters as shown in FIGURE 10 (d) [71]. These ZVT converters are brand new ZVT topologies, and have high efficiency and high power density. Guipeng Chen *et al.* present the topological deduction and unified analysis of new ZVT converters shown in FIGURE 10 (d) [77], [78].All these ZVTC auxiliary circuits can be used in unidirectional and bidirectional DC-DC converters like Buck, Boost, Buck-Boost, Cuk, Zeta and Sepic. By adding extra diodes, inductors and capacitors,

some improved ZVTCs based on ZVTC auxiliary circuits are proposed by [81]–[87].

Also the ZVTC can be combined with the ZCS passive snubber circuits in FIGURE 6 to be new ZVTCs as shown in FIGURE 11 (a) [88]–[91]. These ZVTCs use the diode to release the energy of the ZCS inductor in the auxiliary circuit. Although these converters employ many auxiliary devices, the voltage and current stress is reduced. Meanwhile Das, P. *et al.* [92], [93] propose ZVT active clamped converters with only one auxiliary switch at bidirectional operation as shown in FIGURE 11 (b). These ZVTCs use one auxiliary switch to give soft-switching conditions for the bidirectional Buck/Boost converter with a very complicated control strategy.

FIGURE 10. ZVTCs with different configurations. (a) Traditional bidirectional ZVTC [66], [67]. (b) Traditional bidirectional ZVTC with coupled inductors [68], [69] (c) ZVTCs with type III [70]. (d) ZVTCs with type IV [71].

FIGURE 11. Other ZVT converters. (a) ZVTCs with passive snubber circuit. (b) single-switch ZVT BDC.

B. ZCT CONVERTERS

ZVTC auxiliary circuits are composed of power transistors with an inductor in series, but a ZCTC auxiliary circuit is composed of power transistors with a capacitor in series. ZCTCs will usually provide a ZCS turn-off condition [6], [94]–[106] (FIGURE 12) with a ZCS turnon condition [6], [95]–[100] (FIGURE 12 (a)) or a ZVS turn-on condition [101] for main transistors. Combine ZVTC and ZCTC technologies, some ZVT-ZCT-PWM DC-DC converter have been proposed in [102]–[105] (FIGURE 12(b)). These converters can obtain ZVS turn-on and ZCS turn-off conditions, and then the efficiency will be greatly improved. Besides, M. Amin *et al.* [106] proposed a bidirectional buck/boost converter with a ZCT buck mode and a ZVT boost mode.

FIGURE 12. ZCTCs. (a) A ZVTC with ZCS turn-on and turn-off [97]. (b) A ZVT-ZCT-PWM DC-DC converter [102].

C. ACTIVE RC/SC

Traditional soft-switching QRCs have some disadvantage such as need pulse frequency modulation (PFM) and increased current and voltage stress. In order to solve these problems, several active resonant converters (RCs) are proposed [107]–[109], [111], [112], one of which [109] is shown in FIGURE 13 (a). These converters also use extra auxiliary transistors to control the resonant process. Similarly, the snubber converter (SC) can also be configured to be active SC by using active switches [113]–[120]. In active SCs, an extra transistor is introduced to the snubber net to control the soft-switching occurring process, and the auxiliary loss can be reduced. One typical active SC is shown in FIGURE 13(b). In most active SCs, auxiliary transistors have different control timing comparing to the main transistors. This point will increase the control complexity and consume more computing resources. To improve this problem, some active SCs have the synchronous turn-on and turn-off behavior between auxiliary and main transistors [117], [120].

FIGURE 13. Active QRC/PSCs. (a) Buck-N [109]. (b) Active snubber buck-boost converter [120].

VI. COMPARISON AND PROSPECT

A. COMBINED CONVERTERS

In order to obtain various functions, or increase the rated power, single DC-DC converters (buck, boost, buck-boost converters, and etc.) can be connected in series or in parallel respectively to be the cascaded DC-DC converter or the multiphase DC-DC converter. The multiphase converter is the parallel connected converters by two or more single DC-DC converters. By the interleaved control method, multiphase DC-DC converters can obtain reduced output current ripple and greater rated power. For interleaved multiphase DC-DC converters, abovementioned soft-switching technologies are also suitable, several soft-switching multiphase DC-DC converters are also proposed as control-only softswitching interleaved converters [121], [122], ZVS/ZVT interleaved converters [123]–[135], and ZCS/ZCT interleaved converters [136]–[141]. Article [121] proposed a high-power density design of a soft-switching three-phase bidirectional DC–DC converter without any auxiliary circuit. This converter has the power of 100kW. Article [122] proposed a ZVS interleaved DC-DC converter by establishing coupling effects between main inductors with variable coupling coefficient. In 2017 a ZVT interleaved boost converter is proposed using an auxiliary coupled inductor as shown in FIGURE 14. (a) [128]. Its maximum efficiency exceeds 97%. In 2018, Yie-Tone Chen [129] proposed a interleaved boost converter with coupled-inductor, and this converter only use one auxiliary circuit to generate soft-switching conditions for all transistors. The interleaved multiphase DC-DC converter can increase output power and reduce current ripple. Therefore the soft-switching technology for multiphase DC-DC converter can be widely used in industrial electronics equipment, transportation and information equipment. Until now, new soft-switching interleaved DC-DC converters are emerging.

FIGURE 14. Two combined converters s. (a) ZVT interleaved boost converter with coupled inductor [128]. (b) ZVT NIBBC [143].

The main cascaded DC-DC converter is the non-inverting buck-boost converter (NIBBC), which also can be called cascaded buck+boost converter, and four/two switches buck-boost converter. It is reversely connected by a buck converter and a boost converter without the low side DC-link capacitor. For the NIBBC, above-mentioned soft-switching methods can be used to be control-only soft-switching NIBBCs [7], [9], ALC ZVS NIBBCs [59], [60], [142], and ZVT NIBBCs [143]–[145]. The NIBBC has several softswitching modulation methods listed by [7]. Based on these methods, S. Waffler and J. W. Kolar proposed a low-loss modulation strategy for high-power NIBBC with a peak efficiency of 98.3% [9]. J. Xue, X. F. Cheng, Y. Zhang *et al.* applied ALC technology to the NIBBC, and obtained a lot of new soft-switching NIBBCs [59], [60], [142]. In 2019 L. Cong *et al.* proposed a ZVT NIBBC with only one ZVT auxiliary circuit. This new converter has a peak buck-boost efficiency of 92.5% at the switching frequency of 1MHz [143]. The NIBBC has the superiority of arbitrary voltage conversion function, but its efficiency is low because of the using of four transistors. The soft-switching technology will improve the efficiency of the NIBBC to obtain more extensive applications. Currently, the soft-switching technology of the NIBBC is insufficient. With the addition of more researchers, more new soft-switching topologies of the NIBBC will be constantly created.

B. SOFT-SWITCHING OF SIC AND GAN

Wide-bandgap devices such as SiC and GaN have different characteristics comparing to traditional silicon-based devices. SiC devices have the higher withstand voltage and switching frequency with strict drive requirements. GaN devices can operate at a very high frequency up to several MHz. Therefore Existing soft-switching technologies should be fully assessed before applying to the widebandgap-device-based DC-DC converter.

P. Ranstad *et al.* [146] presents the experimental evaluation of SiC switches for the soft-switching converter comparing to the Si IGBT with the loss reduction up to 65%. A. Jafari *et al.* [147] compare the soft-switching loss between GaN and SiC devices at the high switching frequency. M. R. Ahmed *et al.* [148] proposed a circuit-level analytical model to predict the switching Behavior of SiC MOSFET under different conditions such as hard-switching, soft-switching, and false turn-on. Abovementioned studies promote the understanding of the switching characteristics of SiC devices and the development of soft-switching technologies of SiC converters. Articles [5], [149], [150] study the control and Modulation strategy for the SiC non-isolated DC-DC converters with significant efficiency improvements.

For GaN devices, R. Li *et al.* [151] gives the analysis and verification for the dynamic on-resistance at both hardswitching and soft-switching conditions. Articles study GaNbased soft-switching DC–DC converters to evaluate the characteristic of GaN devices and improve the efficiency and power density of DC-DC converters. Overall, GaN-based soft-switching DC–DC converters are suitable for low voltage UHF applications such as totem pole rectifiers, high-power mobile phone chargers. SiC-based converters are suitable for high-voltage and high-power applications to replace IGBT such as electric vehicle power conversion, chargers, and motor drive systems.

C. CONSTITUTIONS AND FEATURES

TABLE 2 presents features of different soft-switching technologies in this paper. We give power rating, peak efficiency, operating range, pros, cons and applications of the COT, the QRC, the PSC, the ALC, the ZVT/ZCT, and the

active RC/SC. These characteristics indicate that the single performance of some DC-DC converters reached this range. Because some features of DC-DC converters are mutually exclusive: a DC-DC converter is impossible to have a high efficiency and a high frequency at the same time. The COT is suitable for the photovoltaic, wind energy and electric vehicles. The QRC and the PSC are suitable for UHF DC-DC converter with a low power. The ZVT/ZCT DC-DC converter cannot be used in a very high frequency application. Because its auxiliary circuit increases the design difficulty during high-frequency operations. In practice applications, we can choose soft-switching DC-DC converters based on these features.

Now soft-switching DC-DC converters are already very rich, and new soft-switching converters are constantly emerging. However, the most basic soft-switching technology has appeared more than ten years ago. In recent years, although new soft-switching topologies have been continuously proposed, no new soft-switching technology has been found. In order to provide a solid foundation for the emergence of new soft-switching technology, promote the proposal of a unified construction method for soft-switching DC-DC converters, and developing the computer-aided soft-switching DC-DC automatic construction technology, we summarized several basic soft-switching DC-DC converter constitution methods based on existing soft-switching converters as:

[\(1\)](#page-1-0) Theoretical constitution method. In FIGURE 1, we give several main soft-switching forms, soft-switching structure foundations, and operating principles of the switching unit. All methods (include control strategies and auxiliary circuits) that can generate these waveforms will obtain soft-switching conditions. Therefore the basic theoretical constitution method is to directly design control methods or auxiliary circuits to implement these waveforms;

[\(2\)](#page-2-0) Promote to other converters. Most soft-switching technologies are universal, and can be promoting to other non-isolated DC-DC converters, isolated DC-DC converters, even inverters;

[\(3\)](#page-2-1) Family expansion. By changing connecting locations of the auxiliary circuits, a family of this type soft-switching converter can be obtained to satisfy different configuration requirement in practice applications;

(4) Performance improvement. By adding or deleting components, and introducing magnetic coupling, the original soft-switching converters will be improved at efficiency, current ripples or other aspect.

D. FUTURE TRENDS

In industrial applications, soft-switching technologies have been widely used. Until now, basic theory and topology of soft-switching technologies are already relatively complete. And soft-switching technologies are gaining more and more widespread application such as electric vehicles, power systems, consumer electronics, etc. In recent years, basic softswitching technologies have stalled, and there are no new soft-switching technologies appeared. From the analysis of the basic realization principle of soft-switching, the basic theory and method of the soft-switching technology are relatively complete at present. In our opinion, future work is mainly to discover more soft-switching converters based on existing technologies, and to promote the further application of soft-switching technologies in various converters. Meanwhile we hope that the comprehensive summary of soft-switching technologies in this paper will provide theory basics for the exploration and discovery of new soft-switching technologies in the future.

More concretely, in the future, soft-switching technologies will continue developing in the following areas:

[\(1\)](#page-1-0) Exploration and discovery of new soft-switching technologies. Although the soft-switching technology is relatively complete, it is still expected that the new softswitching technology will be discovered. Since the current

soft-switching technology cannot solve the switching loss of some converters under extreme working conditions (such as low-voltage and high-current converters).

[\(2\)](#page-2-0) Soft-switching topology construction methods based on general principle of soft-switching technologies. Guipeng Chen et al [55], [78] already do some works on the same type of soft-switching topology deduction to get all possible similar topologies. Furthermore he proposed an equivalent converter identification method with computer aided to eliminate equivalent topologies and repeat researches [152]. More general soft-switching topology deduction methods are looking forward in the future. In addition, the deductive algorithm of new soft-switching DC-DC converters based on existing soft-switching technologies and the computer-aided technology is also a key technology for the development of the future soft-switching technology. Computer-aided deduction algorithms can not only eliminate the repeated research of equivalent converters, but also discover a large number of new soft-switching converters in a short time through automated performance and verification methods, which greatly reduces the workload of soft-switching technology researchers;

[\(3\)](#page-2-1) Soft-switching technology applications on special DC-DC converters and actual industrial applications. Although existing soft-switching converters are already very rich, Soft-switching technologies for special DC-DC converters (such as multiphase DC-DC converters, cascade DC-DC converters, multilevel DC-DC converters, and so on) and special applications (such as photovoltaic power generation systems, fuel cells, electric vehicles, and so on) lack enough attention and research. At present, most academic researches on the soft-switching technology are based on low-power converters (usually less than 1kW). The research results and conclusions cannot be directly applied to industrial practice. Therefore, further research on the industrial application of the soft-switching technology is needed;

(4) Advanced control modulation technologies for softswitching DC-DC converters. Although most of researchers will give the basic control method for the new soft-switching DC-DC converters when it is proposed, the performance of this control method will be not the best. The performance of the proposed soft-switching DC-DC converter cannot be fully utilized. In addition, for different application scenarios, corresponding control strategies need to be designed. With the development of the computer technology, the occupancy of computing resources by the complex modulation behavior is no longer limiting condition. Therefore advanced control modulation technologies for not only control-only softswitching DC-DC converters but also all other soft-switching DC-DC converters should be deeply researched based on switching behavior modulations and modern control theory methods.

VII. CONCLUSION AND DISCUSS

So far there is not a paper given an overview review of all softswitching technologies for non-isolated DC-DC converters.

Therefore this paper gives this overview review to organize the entire soft-switching technologies and their development context. Though introducing each soft-switching technology with typical converter topologies in detail, features of every soft-switching technology, soft-switching topology constitution method and future trends are also presented. In addition, through the summary of all soft-switching technologies and new soft-switching DC-DC converters, this article attempts to provide a theoretical basis for the exploration and discovery of new soft-switching technologies, provide research ideas for soft-switching technology researchers, and provide guidance on the discovery and application of softswitching converters, thereby promoting the development and further industrial applications of soft-switching technologies. We believe that soft-switching technologies will obtain a more widely application in the future.

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