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Cause-Consequence Diagram Reliability Analysis Using Formal Techniques With Application to Electrical Power Networks

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ABSTRACT Cause-consequence Diagram (CCD) is widely used as a deductive safety analysis technique for decision-making at the critical-system design stage. This approach models the causes of subsystem failures in a highly-critical system and their potential consequences using Fault Tree (FT) and Event Tree (ET) methods, which are well-known dependability modeling techniques. Paper-and-pencil-based approaches and simulation tools, such as the Monte-Carlo approach, are commonly used to carry out CCD analysis, but lack the ability to rigorously verify essential system reliability properties. In this paper, we propose to use formal techniques based on theorem proving for the formal modeling and step-analysis of CCDs to overcome the inaccuracies of the simulation-based analysis and the error-proneness of informal reasoning by mathematical proofs. In particular, we use the HOL4 theorem prover, which is a computer-based mathematical reasoning tool. To this end, we developed a formalization of CCDs in Higher-Order Logic (HOL), based on the algebraic approach, using HOL4. We demonstrate the practical effectiveness of the proposed CCD formalization by performing the formal reliability analysis of the IEEE 39-bus electrical power network. Also, we formally determine the Forced Outage Rate (FOR) of the power generation units and the network reliability index, i.e., System Average Interruption Duration Index ($SAIDI$). To assess the accuracy of our proposed approach, we compare our results with those obtained with MATLAB Monte-Carlo Simulation (MCS) as well as other state-of-the-art approaches for subsystem-level reliability analysis.

INDEX TERMS Cause-consequence diagram, event tree, fault tree, reliability analysis, safety, formal methods, theorem proving, HOL4, Monte-Carlo, FMECA, electrical power network, FOR, SAIDI.

I. INTRODUCTION

NOWADAYS, in many safety-critical systems, which are prevalent, e.g. in smart grids [1] and automotive industry [2], a catastrophic accident may happen due to coincidence of sudden events and/or failures of specific subsystem components. These undesirable accidents may result in loss of profits and sometimes severe fatalities. Therefore, the central inquiry, in many critical-systems, where safety is of the utmost importance, is to identify the possible consequences given that one or more components could fail at a subsystem level on the entire system. For that purpose, the main discipline for safety design engineers is to perform a detailed Cause-Consequence Diagram (CCD) [3] reliability analysis for identifying the subsystem events that prevent the entire system from functioning as desired. This approach models

the causes of component failures and their consequences on the entire system using Fault Tree (FT) [4] and Event Tree (ET) [5] dependability modeling techniques.

FTs mainly provide a graphical model for analyzing the factors causing a system failure upon their occurrences. FTs are generally classified into two categories Static Fault Trees (SFT) and Dynamic Fault Trees (DFT) [6]. SFTs and DFTs allow safety-analysts to capture the static/dynamic failure characteristics of systems in a very effective manner using *logic-gates*, such as OR, AND, NOT, Priority-AND (PAND) and SPare (SP) [4]. However, the FT technique is incapable of identifying the possible consequences resulting from an undesirable failure on the entire system. ETs provide risk analysis with all possible system-level operating states that can occur in the system, i.e., success and failure, so that one of these possible scenarios can occur [5]. However, both of these modeling techniques are limited to analyzing either a critical-system failure or

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cascading dependencies of system-level components only, respectively.

There exist some techniques that have been developed for subsystem-level reliability analysis of safety-critical systems. For instance, Papadopoulos *et al.* in [7] have developed a software tool called *HiP-HOPS* (Hierarchically Performed Hazard Origin & Propagation Studies) [8] for subsystem-level failure analysis to overcome classical manual failure analysis of complex systems and prevent human errors. HiP-HOPS can automatically generate the subsystem-level FT and perform Failure Modes, Effects, and Critically Analyses (FEMCA) from a given system model, where each system component is associated with its failure rate or failure probability [7]. Currently, HiP-HOPS lacks the modeling of *multi-state* system components and also cannot provide generic mathematical expressions that can be used to predict the reliability of a critical-system based on any probabilistic distribution [9]. Similarly, Jahanian in [10] has proposed a new technique called Failure Mode Reasoning (FMR) for identifying and quantifying the failure modes for safety-critical systems at the subsystem level. However, according to Jahanian *et al.* [11], the soundness of the FMR approach needs to be proven mathematically.

On the other hand, CCD analysis typically uses FTs to analyze failures at the subsystem or component level combined with an ET diagram to integrate their cascading failure dependencies at the system level. CCDs are categorized into two general methods for the ET linking process with the FTs [12]: (1) Small ET diagram and large subsystem-level FT; (2) Large ET diagram and small subsystem-level FT. The former one with small ET and large subsystem-level FT is the most commonly used for the probabilistic safety assessment of industrial applications (e.g., in [13]). There are *four* main steps involved in the CCD analysis [14]: (1) *Component failure events*: identify the causes of each component failure associated with their different modes of operations; (2) *Construction of a complete CCD*: construct a CCD model using its basic blocks, i.e., *Decision box*, *Consequence path* and *Consequence box*; (3) *Reduction*: removal of unnecessary decision boxes based on the system functional behavior to obtain a minimal CCD; and lastly (4) *Probabilistic analysis*: evaluating the probabilities of CCD paths describing the occurrence of a sequence of events.

Traditionally, CCD subsystem-level reliability analysis is carried out by using paper-and-pencil-based approaches to analyze safety-critical systems, such as high-integrity protection systems (HIPS) [14] and nuclear power plants [15], or using computer simulation tools based on Monte-Carlo approach, as in [16]. A major limitation in both of the above approaches is the possibility of introducing inaccuracies in the CCD analysis either due to human infallibility or the approximation errors due to numerical methods and pseudo-random numbers in the simulation tools. Moreover, simulation tools do not provide the mathematical expressions that can be used to predict the reliability of a given system based on any probabilistic distributions and failure rates.

A more safe way is to substitute the error-prone informal reasoning of CCD analysis by formal generic mathematical proofs as per recommendations of safety standards, such as IEC 61850 [17], EN 50128 [18] and ISO 26262 [19]. In this paper, we propose to use formal techniques based on theorem proving for the formal reliability CCD analysis-based of safety-critical systems, which provides us the ability to obtain a *verified* subsystem-level failure/operating consequence expression. Theorem proving is a formal verification technique [20], which is used for conducting the proof of mathematical theorems based on a computerized proof tool. In particular, we use HOL4 [21], which is an interactive theorem prover with the ability of verifying a wide range of mathematical expressions constructed in higher-order logic (HOL). For this purpose, we endeavor to formalize the above-mentioned *four* steps of CCD analysis using HOL4 proof assistant. To demonstrate the practical effectiveness of the proposed CCD formalization, we conduct the formal CCD analysis of an IEEE 39-bus electrical power network system. Subsequently, we formally determine a commonly used metric, namely Forced Outage Rate (*FOR*), which determines the capacity outage or unavailability of the power generation units [22]. Also, we evaluate the System Average Interruption Duration Index (*SAIDI*), which describes the average duration of interruptions for each customer in a power network [22].

A. NOVEL CONTRIBUTIONS OF THE PAPER

The main contributions of the paper using the HOL4 theorem prover can be summarized as follows:

- Formalization of the CCD basic constructors, such as *Decision box*, *Consequence path* and *Consequence box*, that can be used to build an arbitrary level of CCDs
- Enabling the formal reduction of CCDs that can remove unnecessary decision boxes from a given CCD model, a feature not available in other existing approaches
- Provide reasoning support for formal probabilistic analysis of scalable \mathcal{N} -level CCD consequence paths with new proposed mathematical formulations
- Application on a real-world IEEE 39-bus electrical power network system and verification of its reliability indexes *FOR* and *SAIDI*
- Comparison between our formal CCD reliability assessment with the corresponding results obtained from MATLAB MCS and other notorious approaches

B. ORGANIZATION OF THE PAPER

The rest of the paper is organized as follows: In Section II, we present the related literature review. In Section III, we describe the preliminaries to facilitate the understanding of the rest of the paper. Section IV presents the proposed formalization of CCD and its formal probabilistic properties. In Section V, we describe the formal CCD analysis of an electrical network system and the evaluation of its reliability indices *FOR* and *SAIDI*. Lastly, Section VI concludes the paper.

II. RELATED WORK

Only a few work have previously considered using formal techniques [20] to model and analyze CCDs. For instance, Ortmeier *et al.* in [23] developed a framework for Deductive Cause-Consequence Analysis (DCCA) using the SMV model checker [24] to verify the CCD proof obligations. However, according to the authors [23], there is a problem of showing the completeness of DCCA due to the exponential growth of the number of proof obligations with complex systems that need cumbersome proof efforts. To overcome above-mentioned limitations, a more practical way is to verify *generic* mathematical formulations that can perform \mathcal{N} -level CCD reliability analysis for real-world systems within a sound environment. Higher-Order-Logic (HOL) [25] is a good candidate formalism for achieving this goal.

Prior to our work, there were two notable projects for building frameworks to formally analyze dependability models using HOL4 theorem proving [21]. For instance, HOL4 has been previously used by Ahmad *et al.* in [26] to formalize SFTs. The SFT formalization includes a new datatype consisting of AND, OR and NOT FT gates [4] to analyze the factors causing a static system failure. Furthermore, Elderhalli *et al.* in [27] had formalized DFTs in the HOL4 theorem prover, which can be used to conduct formal dynamic failure analysis. Similarly, we have defined in [28] a new `EVENT_TREE` datatype to model and analyze all possible system-level success and failure relationships. All these formalizations are basically required to formally analyze either a system static/dynamic failure or cascading dependencies of system-level components only, respectively. On the other hand, CCDs have the superior capability to use SFTs/DFTs for analyzing the static/dynamic failures at the subsystem level and analyze their cascading dependencies at the system-level using ETs. For that purpose, in this paper, we provide new formulations that can model mathematically the graphical diagrams of CCDs and perform the subsystem-level reliability analysis of highly-critical systems. Moreover, our proposed new mathematics provides the modeling of *multi-state* system components and is based on any given probabilistic distribution and failure rates, which makes our proposed work the first of its kind. In order to check the correctness of the proposed equations, we verified them within the sound environment of the HOL4 theorem prover.

III. PRELIMINARIES

In this section, we briefly summarize the fundamentals of the HOL4 theorem proving approach and existing FT and ET formalizations in HOL4 to facilitate the reader's understanding of the rest of the paper.

A. HOL4 THEOREM PROVING

Theorem proving [20] is one of the formal verification techniques that use a computerized proof system for conducting the proof of mathematical theorems. HOL4 [21] is an interactive theorem prover, which is capable of verifying a wide range of safety-critical systems as well as mathematical

TABLE 1. HOL4 symbols and functions.

HOL4 Symbol	Standard	Meaning
$\{x \mid P(x)\}$	$\{\lambda x. P(x)\}$	Set of all x such that $P(x)$
$h :: L$	<i>cons</i>	Add an element h to a list L
$\text{MAP } (\lambda x. f(x)) X$	$x \in X \rightarrow (\lambda x. f)$	Function that maps each element x in the list X to $f(x)$
$L_1 ++ L_2$	<i>append</i>	Joins lists L_1 and L_2 together

expressions constructed in HOL. In general, given a safety-critical system to be formally analyzed, we first model its structure mathematically, then using the HOL4 theorem prover, several properties of the system can be verified based on this mathematical model. The main characteristic of the HOL4 theorem prover is that its core consists only of four axioms and eight inference rules. Any further proof or theorem should be formally verified based on these axioms and rules or based on previously proven theorems. This ensured the soundness of the system model analysis, i.e., no wrong proof goal can be proved. Moreover, since the system properties are proven mathematically within HOL4, no approximation is involved in the analysis results. These features make HOL4 suitable for carrying out the CCD-based reliability analysis of safety-critical systems, such as electrical power networks, that require *sound verification* results. Table 1 provides the HOL4 symbols and functions that we will use in this paper.

B. PROBABILITY THEORY IN HOL4

Measure space is defined mathematically as $(\Omega, \Sigma, \text{and } \mu)$, where Ω represents the sample space, Σ represents a σ -algebra of subsets of Ω , and μ represents a measure with the domain Σ . A probability space is a measure space $(\Omega, \Sigma, \text{and } \text{Pr})$, where Ω is the complete sample space, Σ is the corresponding event space containing all the events of interest, and Pr is the probability measure of the sample space as 1. The HOL4 theorem prover has a rich library of probabilities, including the functions `p_space`, `events`, and `prob`. Given a probability space p , these functions return the corresponding Ω , Σ , and Pr , respectively. The Cumulative Distribution Function (CDF) is defined as the probability of the event where a random variable X has a value less or equal to a value t , i.e., $\mathcal{P}(X \leq t)$. This definition can be formalized in HOL4 as:

$$\vdash \text{CDF } p \ X \ t = \text{distribution } p \ X \ \{y \mid y \leq t\}$$

where the function `distribution` takes three inputs: (i) probability space p ; (ii) random variable X ; and (iii) set of real numbers, then returns the probability of the variable X acquiring all the values of the given set in probability space p .

C. FT FORMALIZATION

Fault Tree (FT) analysis [4] is one of the commonly used reliability assessment techniques for critical-systems. It mainly provides a schematic diagram for analyzing undesired *top events*, which can cause complete system failure upon their

occurrence. An FT model is represented by *logic-gates*, like OR, AND and NOT, where an OR gate models the failure of the output if any of the input failure events occurs alone, while an AND gate models the failure of the output if all of the input failure events occur at the same time, and lastly a NOT gate models the complement of the input failure event. Ahmad *et al.* [26] presented the FT formalization by defining a new datatype gate, in HOL4 as:

```
Hol_datatype gate =
  AND of (gate list) | OR of (gate list) |
  NOT of (gate) | atomic of (event)
```

The FT constructors AND and OR are recursive functions on gate-typed lists, while the FT constructor NOT operates on a gate-type variable. A semantic function is then defined over the gate datatype that can yield an FT diagram as:

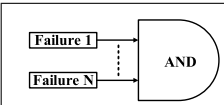
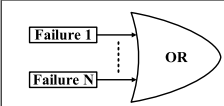
Definition 1:

```
⊢ FTree p (atomic X) = X ∧
  FTree p (OR (h::t)) =
  FTree p h ∪ FTree p (OR t) ∧
  FTree p (AND (h::t)) =
  FTree p h ∩ FTree p (AND t) ∧
  FTree p (NOT X) = p_space p DIFF FTree p X
```

The function FTree takes an event X, identified by a type constructor atomic, and returns the given event X. If the function FTree takes a list of type gate, identified by a type constructor OR, then it returns the union of all elements after applying the function FTree on each element of the given list. Similarly, if the function FTree takes a list of type gate, identified by a type constructor AND, then it performs the intersection of all elements after applying the function FTree on each element of the given list. For the NOT type constructor, the function FTree returns the complement of the failure event obtained from the function FTree.

The formal verification in HOL4 for the failure probabilistic expressions of the above-mentioned FT gates is presented in Table 2 [26]. These expressions are verified under the following constrains: (a) $F_{\mathcal{N}} \in \text{events } p$ ensures that all associated failure events in the given list $F_{\mathcal{N}}$ are drawn from the events space p ; (b) $\text{prob_space } p$ ensures that p is a valid probability space; and lastly (c) $\text{MUTUAL_INDEP } p F_{\mathcal{N}}$ ensures the independence of the failure events in the given list $F_{\mathcal{N}}$. The function \prod takes

TABLE 2. FT HOL4 probabilistic theorems.

FT Gate	Probabilistic Theorem
	$\text{prob } p \text{ (FTree } p \text{ (AND } F_{\mathcal{N}})) = \prod (\text{PROB_LIST } p F_{\mathcal{N}})$
	$\text{prob } p \text{ (FTree } p \text{ (OR } F_{\mathcal{N}})) = \frac{1}{\prod (\text{PROB_LIST } p (\text{COMPL_LIST } p F_{\mathcal{N}}))}$

a list and returns the product of the list elements while the function PROB_LIST returns a list of probabilities associated with the elements of the list. The function COMPL_LIST returns the complement of the given list elements.

D. ET FORMALIZATION

Event Tree (ET) [5] analysis is a widely used technique to enumerate all possible combinations of system-level components failure and success states and external events in the form of a tree structure. An ET diagram starts by an initiating event called *Node* and then all possible scenarios of an event that can occur in the system are drawn as *Branches*. ETs were formally modeled by using a new recursive datatype EVENT_TREE, in HOL4 as [28]:

```
Hol_datatype EVENT_TREE =
  ATOMIC of (event) |
  NODE of (EVENT_TREE list) |
  BRANCH of (event) (EVENT_TREE list)
```

The type constructors NODE and BRANCH are recursive functions on EVENT_TREE-typed lists. A semantic function is then defined over the EVENT_TREE datatype that can yield a corresponding ET diagram as:

Definition 2:

```
⊢ ETREE (ATOMIC X) = X ∧
  ETREE (NODE (h::L)) =
  ETREE h ∪ (ETREE (NODE L)) ∧
  ETREE (BRANCH X (h::L)) =
  X ∩ (ETREE h ∪ ETREE (BRANCH X L))
```

The function ETREE takes an event X, identified by a type constructor ATOMIC and returns the event X. If the function ETREE takes a list of type EVENT_TREE, identified by a type constructor NODE, then it returns the union of all elements after applying the function ETREE on each element of the list. Similarly, if the function ETREE takes an event X and a list of type EVENT_TREE, identified by a type constructor BRANCH, then it performs the intersection of the event X with the union of the head of the list after applying the function ETREE and the recursive call of the BRANCH constructor. For the formal probabilistic assessment of each path occurrence in the ET diagram, HOL4 probabilistic properties for NODE and BRANCH ET constructors are presented in Table 3 [28]. These expressions are formally verified under the same FT constrains, i.e., $\mathcal{X}_{\mathcal{N}} \in \text{events } p, \text{prob_space } p$ and $\text{MUTUAL_INDEP } p \mathcal{X}_{\mathcal{N}}$. The function \sum_p is defined to sum the probabilities of events for a list.

IV. CAUSE-CONSEQUENCE DIAGRAMS

Cause–Consequence Diagram [15] (CCD) has been developed to analyze the causes of an undesired subsystem failure events, using FT analysis, and from these events obtain all possible consequences on the entire system, using ET analysis [29]. The description of the CCD basic constructors are illustrated in Table 4 [14]. CCD analysis is mainly divided into two categories [30]: (1) *Type A* that combines SFT and ET, as shown in Fig. 1 and Table 5 [12]; and (2) *Type B* that combines DFT and ET without shared events in different

TABLE 3. ET HOL4 probabilistic theorems.

ET Constructor	Probabilistic Theorem
	$\text{prob } p \text{ (ETREE (NODE } \mathcal{X}_N)) = \sum_{\mathcal{P}} p \mathcal{X}_N$
	$\text{prob } p \text{ (ETREE (BRANCH } Y \mathcal{Z}_N)) = (\text{prob } p Y) \times \sum_{\mathcal{P}} p \mathcal{Z}_N$

TABLE 4. CCD symbols and functions.

CCD Symbol	Function
	<p>Decision Box: represents the functionality of a component.</p> <p>(1) NO Box: describes the component or subsystem failure behavior. A FT of the component is connected to this box that can be used to determine the failure probability (\mathcal{P}_F)</p> <p>(2) YES Box: represents the correct functioning of the component or reliability, which can be calculated by simply taking the complement of the failure probability determined in the NO Box, i.e., $1 - \mathcal{P}_F$</p>
	Consequence Path: models the next possible consequence scenarios due to a particular event
	Consequence Box: models the outcome event due to a particular sequence of events

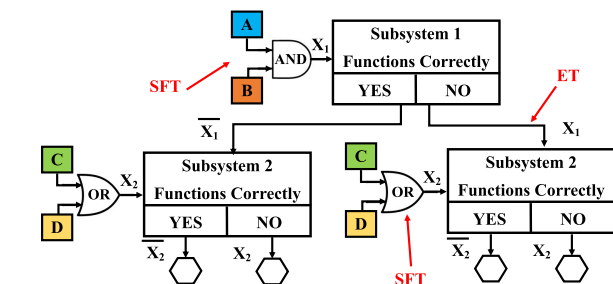


FIGURE 1. CCD analysis type A.

subsystems, as shown in Fig. 2 and Table 6 [12]. In this analysis, we focus on the CCD-based reliability analysis at the subsystem level of *Type A*.

Fig. 3 depicts the overview of the *four* steps of CCD analysis [3]: (1) *Components failure events*: identify the causes of the undesired failure events for each subsystem/component in the safety-critical system; (2) *Construction of a complete CCD*: draw a complete system CCD model using its basic constructors considering that the order of components should follow the temporal action of the system; (3) *CCD model reduction*: remove the unnecessary decision boxes in the system to obtain its minimal CCD representing the actual functional behavior of the system; and (4) *CCD probabilistic*

TABLE 5. SFT symbols and functions.

SFT Symbol	Function
	AND Gate: models the failure of the output if all of the input failure events, i.e., A and B, occur at the same time (simultaneously)
	OR Gate: models the failure of the output if any of the input failure events, i.e., C or D, occurs alone

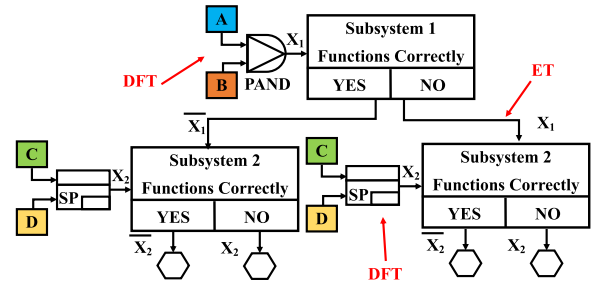


FIGURE 2. CCD analysis type B.

TABLE 6. DFT symbols and functions.

DFT Symbol	Function
	Priority-AND (PAND) Gate: models the dynamic behavior of failing the output when all input events occur in a sequence, i.e., A then B
	SPare (SP) Gate: models the dynamic behavior of activating the spare input D after the failure of the main input C

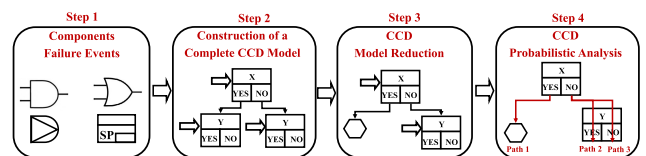


FIGURE 3. Overview of CCD analysis.

analysis: evaluate the probabilities of all CCD consequence paths. The paths in a CCD represent the likelihood of specific sequence scenarios that are possible to occur in a system so that *only one* scenario can occur [29]. This implies that all consequences in a CCD are disjoint (mutually exclusive) [14]. Assuming that all events associated with the decision boxes in a CCD model are mutually independent, then the CCD paths probabilities can be quantified as follows [15]:

- 1) Evaluate the probabilities of each outgoing branch stemming from a *decision box*, i.e., quantifying the associated FT models
- 2) Compute the probability of each *consequence path* by multiplying the individual probabilities of all events associated with the decision boxes

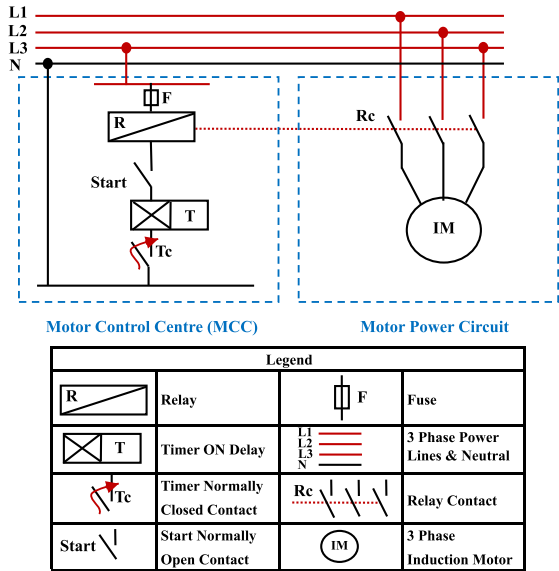


FIGURE 4. Schematic of an example MCC.

- 3) Determine the probability of a particular *consequence box* by summing the probabilities of all consequence paths ending with that consequence event

As an example, consider a Motor Control Centre (MCC) [31] consisting of three components *Relay*, *Timer* and *Fuse*, as shown in Fig. 4. The MCC is designed to control an Induction Motor (IM) and let it run for a specific period of time then stops. The IM power circuit is energized by the closure of the Relay Contacts (Rc), as shown in Fig. 4. Rc closes after the user press the Start button that energizes R and at the same time energizes an ON-delay Timer (T). The Timer opens its contacts (Tc) after a specific period of time t and consequently the IM stops. If the IM is overloaded than its design, then the Fuse (F) melts and protects both MCC and IM from damage. Assume that each component in the MCC has two operational states, i.e., operating or failing. The *four steps* of a CCD-based reliability analysis described by Andrews and Ridley [14] are as follows [29]:

- 1) *Components failure events*: Assign a FT to each component in the MCC, i.e., FT_{Relay} , FT_{Timer} , FT_{Fuse} .
- 2) *Construction of a complete CCD*: Construct a complete CCD model of the IM control operation, as shown in Fig. 5. For instance, if the condition of the first decision box is either satisfied or not, i.e., YES or NO, then the next system components are considered in order, i.e., *Timer* and *Fuse*, respectively. Each consequence in the CCD ends with either motor stops (MS) or motor runs (MR).
- 3) *CCD model reduction*: Apply the reduction process on the obtained complete CCD model. For instance, if the condition of the first decision box (Relay Contacts Open) is satisfied, i.e., YES box, then the IM stops regardless of the status of the rest of the components, as shown in Fig. 6. Similarly, if the condition of the second decision box (Timer Contacts Open) is satisfied,

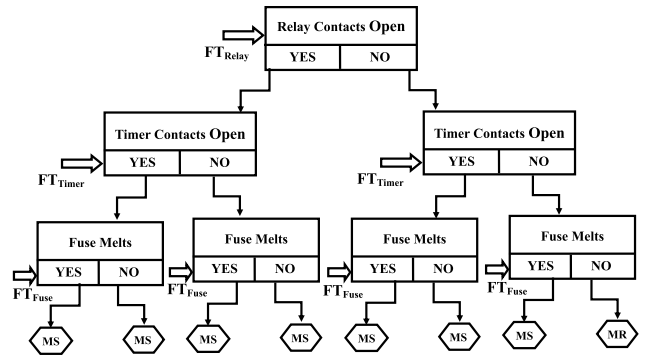


FIGURE 5. Complete CCD model of the MCC.

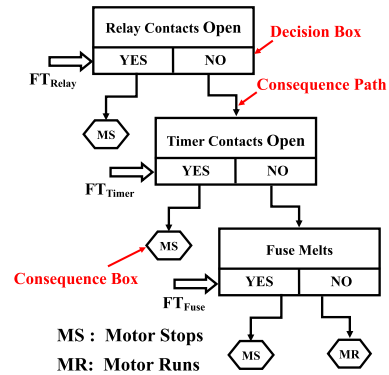


FIGURE 6. Reduced CCD model of the MCC.

then the IM stops. So, Fig. 6 represents the minimal CCD for the IM control operation.

- 4) *CCD probabilistic analysis*: The probabilities of the two consequence boxes MS and MR in Fig. 6 can be expressed mathematically as:

$$\begin{aligned}
 & \mathcal{P}(\text{Consequence_Box}_{MS}) \\
 &= \mathcal{P}(\text{Relay}_S) + \mathcal{P}(\text{Relay}_F) \times \mathcal{P}(\text{Timer}_S) \\
 & \quad + \mathcal{P}(\text{Relay}_F) \times \mathcal{P}(\text{Timer}_F) \times \mathcal{P}(\text{Fuses}_S) \quad (1)
 \end{aligned}$$

$$\begin{aligned}
 & \mathcal{P}(\text{Consequence_Box}_{MR}) \\
 &= \mathcal{P}(\text{Relay}_F) \times \mathcal{P}(\text{Timer}_F) \times \mathcal{P}(\text{Fuse}_F) \quad (2)
 \end{aligned}$$

where $\mathcal{P}(\mathcal{X}_F)$ is the unreliability function or the probability of failure for a component \mathcal{X} , i.e., $FT_{\mathcal{X}}$ model, and $\mathcal{P}(\mathcal{X}_S)$ is the reliability function or the probability of operating, i.e., the complement of the $FT_{\mathcal{X}}$ model.

In the next section, we present, in detail, the formalization of CCDs in the HOL4 theorem prover to analyze the failures at the subsystem level of a given safety-critical complex system and determine all their possible cascading dependencies of complete/partial reliability and failure events that can occur at the system level.

A. FORMAL CCD MODELING

We start the formalization of CCDs by formally model its basic symbols, as described in Table 4 in HOL4 as follows:

Definition 3:

$\vdash \text{DEC_BOX } p \ X \ Y =$
 if $X = 1$ then $\text{FST } Y$
 else if $X = 0$ then $\text{SND } Y$
 else $p_space \ p$

where Y is an ordered pair $(\text{FST } Y, \text{SND } Y)$ representing the reliability and unreliability functions in a decision box, respectively. The condition $X = 1$ represents the YES Box while $X = 0$ represents the NO Box. If X is neither 1 nor 0, for instance, $X = 2$, this represents the irrelevance of the decision box, which returns the probability space p to be used in the reduction process of CCDs.

Secondly, we define the CCD Consequence path by recursively applying the BRANCH ET constructor on a given \mathcal{N} list of decision boxes $(\text{DEC_BOX}_{\mathcal{N}})$ using the HOL4 recursive function FOLDL as:

Definition 4:

$\vdash \text{CONSEQ_PATH } p \ (\text{DEC_BOX}_1 :: \text{DEC_BOX}_{\mathcal{N}}) =$
 FOLDL
 $(\lambda a \ b. \text{ETREE } (\text{BRANCH } a \ b))$
 $\text{DEC_BOX}_1 \ \text{DEC_BOX}_{\mathcal{N}}$

Finally, we define the CCD Consequence box by mapping the defined function CONSEQ_PATH on a list using the HOL4 function MAP, then applies the NODE ET constructor:

Definition 5:

$\vdash \text{CONSEQ_BOX } p \ L_{\mathcal{M}} =$
 ETREE
 $(\text{NODE } (\text{MAP } (\lambda a. \text{CONSEQ_PATH } p \ a) \ L_{\mathcal{M}}))$

Using the above definitions, we can construct a complete CCD model (Step 2 in Fig. 3) for the MCC system shown in Fig. 5, in HOL4 as:

$\vdash \text{MCC_COMPLETE_CCD } \text{FT}_R \ \text{FT}_T \ \text{FT}_F =$
 $\text{CONSEQ_BOX } p \ [$
 $[\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 $[\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 $[\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 \vdots
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_F, \text{FT}_F)]]$

In CCD analysis [29], Step 3 in Fig. 3 is used to model the accurate functional behavior of systems in the sense that the irrelevant decision boxes should be removed from a complete CCD of a system. Upon this, the actual CCD model of the MCC system after reduction, as shown in Fig. 6, can be obtained by assigning X with neither 1 nor 0, for instance,

$X = 2$, which represents the irrelevance of the decision box, in HOL4 as:

$\vdash \text{MCC_REDUCED_CCD } \text{FT}_R \ \text{FT}_T \ \text{FT}_F =$
 $\text{CONSEQ_BOX } p \ [$
 $[\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 2 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 2 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 2 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_F, \text{FT}_F)]]$

Also, we can formally verify the above reduced CCD model of the MCC system, in HOL4 as:

$\vdash \text{MCC_REDUCED_CCD } \text{FT}_R \ \text{FT}_T \ \text{FT}_F =$
 $\text{CONSEQ_BOX } p \ [$
 $[\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_T, \text{FT}_T)];$
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 1 \ (\overline{\text{FT}}_F, \text{FT}_F)];$
 $[\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_R, \text{FT}_R);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_T, \text{FT}_T);$
 $\text{DEC_BOX } p \ 0 \ (\overline{\text{FT}}_F, \text{FT}_F)]]$

where $\overline{\text{FT}}_X$ for a component X is the complement of FT_X .

B. FORMAL CCD ANALYSIS

The important step in the CCD analysis is to determine the probability of each consequence path occurrence in the CCD [14]. For that purpose, we formally verify the following CCD generic probabilistic properties, in HOL4 as follows:

Property 1: The probability of a consequence path for one decision box assigned with a generic FT model, i.e., OR or AND, as shown in Fig. 7, under the Assumptions (A) described in Table 2, respectively as follows:

Theorem 1:

\vdash (A1): $\text{prob_space } p$
 (A2): $F_{\mathcal{N}} \in \text{events } p$
 (A3): $\text{MUTUAL_INDEP } p \ F_{\mathcal{N}} \Rightarrow$
 $\text{prob } p \ (\text{CONSEQ_PATH } p$
 $[\text{DEC_BOX } p \ X \ (\text{FTree } p \ (\text{NOT } (\text{OR } F_{\mathcal{N}})),$
 $\text{FTree } p \ (\text{OR } F_{\mathcal{N}}))]) =$
 if $X = 1$ then
 $\prod (\text{PROB_LIST } p \ (\text{COMPL_LIST } p \ F_{\mathcal{N}}))$
 else if $X = 0$ then
 $1 - \prod (\text{PROB_LIST } p \ (\text{COMPL_LIST } p \ F_{\mathcal{N}}))$
 else 1

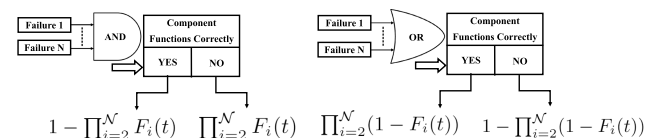


FIGURE 7. Decision boxes with FT gates.

For example, consider a system X consists of two components C₁ and C₂. Assuming the failure of either one them causes the system failure, i.e., C_{1F} or C_{2F}, We can formally model the FT of the system (FT_{system}), in HOL4 as:

$$\vdash \text{FT}_{\text{system}} \text{ p } C_{1F} \ C_{2F} = \text{FTree p (OR [C}_{1F}; C_{2F}])$$

Using Theorem 1, we can obtain the probability of a decision box YES/NO outcomes connected to the above FT model, respectively, in HOL4 as:

$$\vdash \text{prob p (CONSEQ_PATH p [DEC_BOX p 1 (FT}_{\text{system}}, \text{FT}_{\text{system}})])} = (1 - \text{prob p } C_{1F}) \times (1 - \text{prob p } C_{2F})$$

$$\vdash \text{prob p (CONSEQ_PATH p [DEC_BOX p 0 (FT}_{\text{system}}, \text{FT}_{\text{system}})])} = 1 - (1 - \text{prob p } C_{1F}) \times (1 - \text{prob p } C_{2F})$$

Theorem 2:

$$\begin{aligned} &\vdash \text{(A1): prob_space p} \\ &\text{(A2): } F_{\mathcal{N}} \in \text{events p} \\ &\text{(A3): MUTUAL_INDEP p } F_{\mathcal{N}} \Rightarrow \\ &\text{prob p (CONSEQ_PATH p} \\ &\quad [\text{DEC_BOX p X (FTree p (NOT (AND } F_{\mathcal{N}})),} \\ &\quad \quad \text{FTree p (AND } F_{\mathcal{N}}))]} = \\ &\quad \text{if X = 1 then } 1 - \prod (\text{PROB_LIST p } F_{\mathcal{N}}) \\ &\quad \text{else if X = 0 then } \prod (\text{PROB_LIST p } F_{\mathcal{N}}) \\ &\quad \text{else 1} \end{aligned}$$

For instance, in the above example, assume the failure of both components simultaneously only causes the system failure, i.e., C_{1F} and C_{2F}. We can formally model the FT of the system, in HOL4 as:

$$\vdash \text{FT}_{\text{system}} \text{ p } C_{1F} \ C_{2F} = \text{FTree p (AND[C}_{1F}; C_{2F}])$$

Using Theorem 2, we can obtain the probability of a decision box YES/NO outcomes connected to the above FT model, respectively, in HOL4 as:

$$\vdash \text{prob p (CONSEQ_PATH p [DEC_BOX p 1 (FT}_{\text{system}}, \text{FT}_{\text{system}})])} = 1 - \text{prob p } C_{1F} \times \text{prob p } C_{2F}$$

$$\vdash \text{prob p (CONSEQ_PATH p [DEC_BOX p 0 (FT}_{\text{system}}, \text{FT}_{\text{system}})])} = \text{prob p } C_{1F} \times \text{prob p } C_{2F}$$

Property 2: The probability of two-level decision boxes assigned to a consequence path with all combinations of FT gates (AND-OR/OR-AND, AND-AND and OR-OR), as shown in Fig. 8. Each combination has 4 possible operating

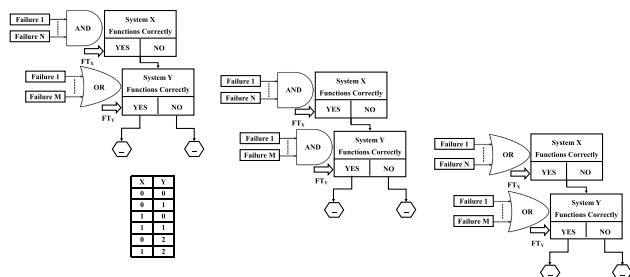


FIGURE 8. Two-level decision boxes for CCD analysis.

scenarios that can occur (0-0, 0-1, 1-0 and 1-1) and 2 other possible reduction scenarios that may be required in Step 3 (0-2 and 1-2), which represents the removal of the decision box Y from the path. The basic idea is to select different combinations of decision boxes to achieve the desired system behavior and also select the reduction combination (> 1) to remove irreverent decision boxes. This probabilistic expressions can be formally verified under all constrains, i.e., $\mathcal{F}_{\mathcal{N}} \text{ ++ } \mathcal{F}_{\mathcal{M}} \in \text{events p, prob_space p and MUTUAL_INDEP p } \mathcal{F}_{\mathcal{N}} \text{ ++ } \mathcal{F}_{\mathcal{M}}$, in HOL4 as:

Theorem 3:

$$\begin{aligned} &\vdash \text{(A1): prob_space p} \\ &\text{(A2): } (\forall y. y \in (F_{\mathcal{N}} \text{ ++ } F_{\mathcal{M}}) \Rightarrow y \in \text{events p}) \\ &\text{(A3): MUTUAL_INDEP p (F}_{\mathcal{N}} \text{ ++ } F_{\mathcal{M}}) \Rightarrow \\ &\text{prob p (CONSEQ_PATH p} \\ &\quad [\text{DEC_BOX p X (FTree p (NOT (AND } F_{\mathcal{N}})),} \\ &\quad \quad \text{FTree p (AND } F_{\mathcal{N}}))}; \\ &\quad \text{DEC_BOX p Y (FTree p (NOT (OR } F_{\mathcal{M}})),} \\ &\quad \quad \text{FTree p (OR } F_{\mathcal{M}}))]} = \\ &\quad \text{if X = 0 \wedge Y = 0 then} \\ &\quad \quad \prod (\text{PROB_LIST p } F_{\mathcal{N}}) \times \\ &\quad \quad (1 - \prod (\text{PROB_LIST p (COMPL_LIST p } F_{\mathcal{M}}))) \\ &\quad \text{else if X = 0 \wedge Y = 1 then} \\ &\quad \quad \prod (\text{PROB_LIST p } F_{\mathcal{N}}) \times \\ &\quad \quad \prod (\text{PROB_LIST p (COMPL_LIST p } F_{\mathcal{M}})) \\ &\quad \text{else if X = 1 \wedge Y = 0 then} \\ &\quad \quad (1 - \prod (\text{PROB_LIST p } F_{\mathcal{N}})) \times \\ &\quad \quad (1 - \prod (\text{PROB_LIST p (COMPL_LIST p } F_{\mathcal{M}}))) \\ &\quad \text{else if X = 1 \wedge Y = 1 then} \\ &\quad \quad (1 - \prod (\text{PROB_LIST p } F_{\mathcal{N}})) \times \\ &\quad \quad \prod (\text{PROB_LIST p (COMPL_LIST p } F_{\mathcal{M}})) \\ &\quad \text{else if X = 0 \wedge Y = 2 then} \\ &\quad \quad \prod (\text{PROB_LIST p } F_{\mathcal{N}}) \\ &\quad \text{else if X = 1 \wedge Y = 2 then} \\ &\quad \quad (1 - \prod (\text{PROB_LIST p } F_{\mathcal{N}})) \text{ else 1} \end{aligned}$$

Theorem 4:

$$\begin{aligned} &\vdash \text{prob p (CONSEQ_PATH p} \\ &\quad [\text{DEC_BOX p X (FTree p (NOT (AND } F_{\mathcal{N}})),} \\ &\quad \quad \text{FTree p (AND } F_{\mathcal{N}}))}; \\ &\quad \text{DEC_BOX p Y (FTree p (NOT (AND } F_{\mathcal{M}})),} \\ &\quad \quad \text{FTree p (AND } F_{\mathcal{M}}))]} = \\ &\quad \text{if X = 0 \wedge Y = 0 then} \\ &\quad \quad \prod (\text{PROB_LIST p } F_{\mathcal{N}}) \times \\ &\quad \quad \prod (\text{PROB_LIST p } F_{\mathcal{M}}) \\ &\quad \text{else if X = 0 \wedge Y = 1 then} \\ &\quad \quad \prod (\text{PROB_LIST p } F_{\mathcal{N}}) \times \\ &\quad \quad (1 - \prod (\text{PROB_LIST p } F_{\mathcal{M}})) \\ &\quad \quad \vdots \\ &\quad \text{else if X = 1 \wedge Y = 2 then} \\ &\quad \quad (1 - \prod (\text{PROB_LIST p } F_{\mathcal{N}})) \text{ else 1} \end{aligned}$$

Theorem 5:

$$\begin{aligned} &\vdash \text{prob p (CONSEQ_PATH p} \\ &\quad [\text{DEC_BOX p X (FTree p (NOT (OR } F_{\mathcal{N}})),} \\ &\quad \quad \text{FTree p (OR } F_{\mathcal{N}}))}; \\ &\quad \text{DEC_BOX p Y (FTree p (NOT (OR } F_{\mathcal{M}})),} \\ &\quad \quad \text{FTree p (OR } F_{\mathcal{M}}))]} = \\ &\quad \text{if X = 0 \wedge Y = 0 then} \\ &\quad \quad (1 - \prod (\text{PROB_LIST p (COMPL_LIST p } F_{\mathcal{N}}))) \times \\ &\quad \quad (1 - \prod (\text{PROB_LIST p (COMPL_LIST p } F_{\mathcal{M}}))) \\ &\quad \quad \vdots \\ &\quad \text{else if X = 1 \wedge Y = 2 then} \\ &\quad \quad \prod (\text{PROB_LIST p (COMPL_LIST p } F_{\mathcal{N}})) \text{ else 1} \end{aligned}$$

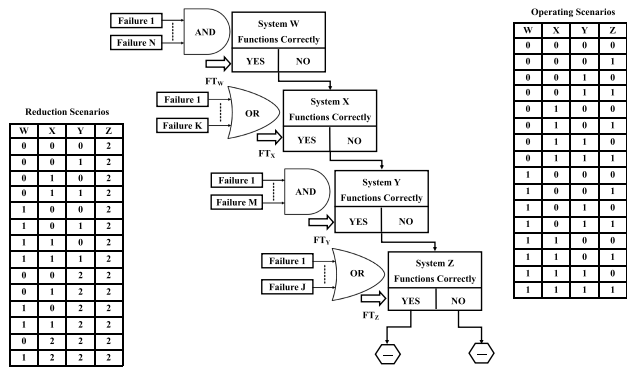


FIGURE 9. Four-level decision boxes for CCD analysis.

where the assumptions of Theorems 4 and 5 are similar to the ones used in Theorem 3.

Property 3: Probabilistic expressions for a CONSEQ_PATH consisting of *three-level* systems X–Y–Z associated with various FT gates (AND-OR-AND/AND-AND-OR/OR-AND-AND and OR-AND-OR/OR-OR-AND/AND-OR-OR). Each integration has 8 possible operating scenarios that can occur (0–0–0, 0–0–1, 0–1–0, 0–1–1, 1–0–0, 1–0–1, 1–1–0 and 1–1–1) and 6 other possible scenarios for the reduction process (0–0–2, 0–1–2, 1–0–2, 1–1–2, 0–2–2 and 1–2–2), which represents the removal of either the decision box Z only or both decision boxes Y and Z from the consequence path [32].

Property 4: A generic probabilistic property for a consequence path consisting of complex *four-level* decision boxes associated with different combination of FTs and each one consisting of \mathcal{N} components (AND-OR-AND-OR/OR-AND-OR-AND/AND-AND-OR/OR/OR-AND-AND), which has 16 possible operating scenarios that can occur and 14 other possible reduction possibilities, as shown in Fig. 9, in HOL4 as:

Theorem 6:

```

⊢ Let
WF = ∏ (PROB_LIST p FN);
W̄ = 1 - WF;
XF = 1 - ∏ (PROB_LIST p (COMPL_LIST p FK));
X̄ = 1 - XF;
YF = ∏ (PROB_LIST p FM);
Ȳ = 1 - YF;
ZF = 1 - ∏ (PROB_LIST p (COMPL_LIST p FJ));
Z̄ = 1 - ZF
in
prob p (CONSEQ_PATH p
[DEC_BOX p W (FTree p (NOT (AND FN)),
FTree p (AND FN)),
DEC_BOX p X (FTree p (NOT (OR FK)),
FTree p (OR FK)),
DEC_BOX p Y (FTree p (NOT (AND FM)),
FTree p (AND FM)),
DEC_BOX p Z (FTree p (NOT (OR FJ)),
FTree p (OR FJ))]) =
if W = 0 ∧ X = 0 ∧ Y = 0 ∧ Z = 0
then WF × XF × YF × ZF
else if W = 0 ∧ X = 0 ∧ Y = 0 ∧ Z = 1
then WF × XF × YF × Z̄

```

```

else if W = 0 ∧ X = 0 ∧ Y = 1 ∧ Z = 0
then WF × XF × Ȳ × ZF
...
else if W = 1 ∧ X = 1 ∧ Y = 2 ∧ Z = 2
then W̄ × X̄
else if W = 1 ∧ X = 2 ∧ Y = 2 ∧ Z = 2
then W̄ else 1

```

Property 5: For real-world systems consisting of \mathcal{N} -level decision boxes, we define a CCD recursive function N_DEC_BOXS that can automatically generate a specific sequence of decision boxes for a given index list N, e.g., [0;1;1;0;1;1;...], associated with a specific sequence of AND/OR FTs given in a list M, e.g., [(FT_{OR},FT_{OR});(FT_{OR},FT_{OR});(FT_{AND},FT_{AND});(FT_{OR},FT_{OR});...]. This can be done in HOL4 as:

Definition 6:

```

⊢ N_DEC_BOXS p (X:::N) (Y:::M) =
DEC_BOX p X Y:::N_DEC_BOXS p N M

```

Using the above-definition and Theorems 1 and 2, we write a pattern of simple *tactics* [32] so that a safety-analyst can easily verify a component-level reliability/failure probabilistic expression consisting of specific \mathcal{N} -level decision boxes based on the following theorem:

Theorem 7:

```

⊢ prob p (CONSEQ_PATH p
(N_DEC_BOXS p (X:::N) (Y:::M)) =
prob p (CONSEQ_PATH p [DEC_BOX p X Y]) ×
prob p (CONSEQ_PATH p (N_DEC_BOXS p N M))

```

Property 6: A generic probabilistic expression of CONSEQ_BOX for a certain event occurrence in the entire system as the sum of the individual probabilities of all \mathcal{M} CONSEQ_PATH ending with that event:

Theorem 8:

```

⊢ Let
CONSEQ_PATHS LM =
MAP (λa. CONSEQ_PATH p a) LM
in
(A1): prob_space p
(A2): disjoint (CONSEQ_PATHS LM)
(A3): MUTUAL_INDEP p LM
(A4): ALL_DISTINCT (CONSEQ_PATHS LM) ⇒
prob p (CONSEQ_BOX p LM) =
∑ (PROB_LIST p (CONSEQ_PATHS LM))

```

where the HOL4 function disjoint ensures that each pair of elements in a given list is mutually exclusive while the function ALL_DISTINCT ensures that each pair is distinct. The function ∑ is defined to sum the events for a given list. Remark that all above-mentioned CCD new formulations have been *formally verified* in HOL4, where the proof-script amounts to about 12,000 lines of HOL4 code, which can be downloaded for use from [32]. Also, this code can be extended, with some basic knowhow about HOL4, to perform dynamic failure analysis of dynamic subsystems where no dependencies exist in different subsystems using DFTs, such as PAND and SP, i.e, CCD reliability analysis of *Type B*.

To illustrate the applicability of our proposed approach, in the next section, we present the formal CCD step-analysis of the standard IEEE 39-bus electrical power network and verify its reliability indexes (*FOR* and *SAIDI*), which are commonly used as reliability indicators by electric power utilities.

V. ELECTRICAL POWER 39-BUS NETWORK SYSTEM

An electrical power network is an interconnected grid for delivering electricity from producers to customers. The power network system consists of three main zones [1]: (i) generating stations that produce electric power; (ii) transmission lines that carry power from sources to loads; and (iii) distribution lines that connect individual consumers. Due to the complex and integrated nature of the power network, failures in any zone of the system can cause widespread catastrophic disruption of supply [1]. Therefore a rigorous formal cause-consequence analysis of the grid is essential in order to reduce the risk situation of a blackout and enable back-up decisions [33]. For power network safety assessment, reliability engineers have been dividing the power network into three main hierarchical levels [12]: (a) generation systems; (b) composite generation and transmission (or bulk power) systems; and (c) distribution systems. We can use our proposed CCD formalization for the formal modeling and analysis of any hierarchical level in the power network. In this case study, we focus on the generation part only, i.e., hierarchical level I. Also, we can evaluate the Force Outage Rate (*FOR*) for the generation stations, which is defined as the probability of the unit unavailability to produce power due to unexpected equipment failure [33]. Additionally, we can determine the System Average Interruption Duration Index (*SAIDI*), which is used by power grid engineers to indicate the average duration for each customer served to experience a sustained outage. *SAIDI* is defined as the sum of all customer interruption durations (probability of load failures ℓ multiplying by the mean-time-to-repair the failures and the number of customers that are affected by these failures) over the total number of customers served [33]:

$$SAIDI = \frac{\sum_{\mathcal{X}} \mathcal{P}(\mathcal{X}_{\ell}) \times MTTR_{\mathcal{X}} \times CN_{\mathcal{X}}}{\sum_{\mathcal{X}} CN_{\mathcal{X}}} \quad (3)$$

where $CN_{\mathcal{X}}$ is the number of customers for a certain location \mathcal{X} while $MTTR_{\mathcal{X}}$ is the mean-time-to-repair the failure that occurred at \mathcal{X} . We formally define a function \sum_{ℓ}^T in HOL4 to sum all customer interruption durations. Also, we formally define a generic function *SAIDI* by dividing the output of \sum_{ℓ}^T over the total number of customers served, in HOL4 as:

Definition 7:

$$\vdash \sum_{\ell}^T (L::L_{\mathcal{M}}) (MTTR::MTTR_{\mathcal{M}}) (CN::CN_{\mathcal{M}}) p = \frac{\text{prob } p \text{ (CONSEQ_BOX } p \text{ } L_{\mathcal{M}})}{\sum_{\ell}^T L_{\mathcal{M}} MTTR_{\mathcal{M}} CN_{\mathcal{M}} p} \times MTTR \times CN +$$

Definition 8:

$$\vdash SAIDI L_{\mathcal{M}} MTTR_{\mathcal{M}} CN_{\mathcal{M}} p = \frac{\sum_{\ell}^T L_{\mathcal{M}} MTTR_{\mathcal{M}} CN_{\mathcal{M}} p}{\sum_{\ell}^T CN_{\mathcal{M}}}$$

where $L_{\mathcal{M}}$ is the list of CCD paths, $MTTR_{\mathcal{M}}$ is the list of meantime to repairs, and $CN_{\mathcal{M}}$ is the list of customer numbers. The function \sum_{ℓ}^T (Definition 7) models the numerator of Eq. 3, which is the sum of all customer interruption durations at different locations in the electrical power grid. Each probability of failure is obtained by evaluating a *CONSEQ_BOX* consisting of a list of \mathcal{M} *CONSEQ_PATH*, which cause that failure. Definition 8 represents the division of output of Definition 7 over the total number of customers at all those locations as described in Eq. 3.

Consider a standard *IEEE 39-bus* electrical power network test system consisting of 10 generators (G), 12 substations (S/S), 39 Buses (Bus), and 34 transmission lines (TL), as shown in Fig. 10 [34]. Assuming the generators G1-G10 are of two types: (i) solar photo-voltaic (PV) power plants G1-G5; and (ii) steam power plants G6-G10. Using the Optimal Power Flow (OPF) optimization [35], we can determine the flow of electricity from generators to consumers in the power network. Typically, we are only interested in evaluating the duration of certain failure events occurrence for specific loads in the grid. For instance, if we consider the failure of load A, which according to the OPF is supplied from G9 and G5 only, as shown in Fig. 10, then the failure of either one or both power plants will lead to a partial or a complete blackout failure at that load, respectively. Assuming the failure of two consecutive power plants causes a complete blackout of the load. Hence, considering the disruption cases of *only one* supply generator, then different partial failures for loads A, B, C and D, as shown in Fig. 10, can be obtained by observing different failures in the power network as:

- 1) $\mathcal{P}(\text{Load}_{A\ell}) = (1 - FOR_{G_9}) \times FOR_{G_5} + FOR_{G_9} \times (1 - FOR_{G_5})$
- 2) $\mathcal{P}(\text{Load}_{B\ell}) = (1 - FOR_{G_7}) \times FOR_{G_9} + FOR_{G_7} \times (1 - FOR_{G_9})$
- 3) $\mathcal{P}(\text{Load}_{C\ell}) = (1 - FOR_{G_1}) \times FOR_{G_2} + FOR_{G_1} \times (1 - FOR_{G_2})$
- 4) $\mathcal{P}(\text{Load}_{D\ell}) = (1 - FOR_{G_6}) \times (1 - FOR_{G_3}) \times (1 - FOR_{G_8}) \times FOR_{G_4} + (1 - FOR_{G_6}) \times (1 - FOR_{G_3}) \times FOR_{G_8} \times (1 - FOR_{G_4}) + (1 - FOR_{G_6}) \times FOR_{G_3} \times (1 - FOR_{G_8}) \times (1 - FOR_{G_4}) + FOR_{G_6} \times (1 - FOR_{G_3}) \times (1 - FOR_{G_8}) \times (1 - FOR_{G_4})$

Therefore, the assessment of *SAIDI* for the Grid (G) shown in Fig. 10, including an evaluation for the *FOR* of all its power plants, can be written mathematically as:

$$SAIDI_G = \frac{\mathcal{P}(\text{Load}_{A\ell}) \times MTTR_{\text{LoadA}} \times CN_{\text{LoadA}} + \dots}{CN_{\text{LoadA}} + CN_{\text{LoadB}} + CN_{\text{LoadC}} + CN_{\text{LoadD}}} \quad (4)$$

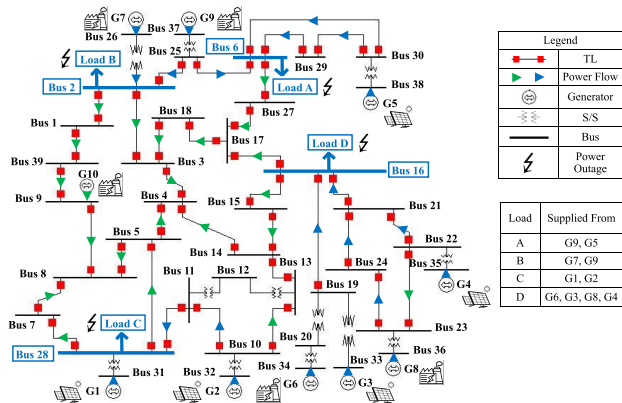


FIGURE 10. IEEE 39-bus electrical power network [34].

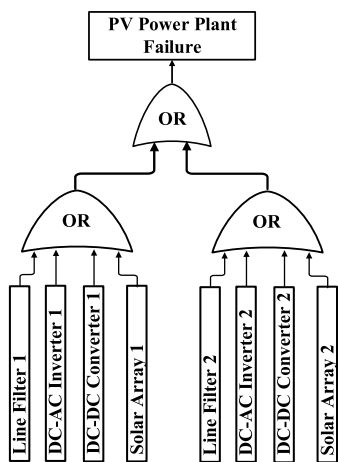


FIGURE 11. FT model of a PV power plant.

A. FORMAL CCD ANALYSIS IN HOL4

We can apply our *four* steps of CCD formalization to verify the expression of *SAIDI* in terms of the power plant generator components, in HOL4 as:

Step 1 (Component failure events):

The schematic FT models of a typically PV power plant consisting of 2 solar farms [36] and a steam power plant consisting of 3 generators [33] are shown in Fig. 11 and Fig. 12, respectively. Using the formal FT modeling, we can formally define the FT models of both plants, in HOL4 as:

Definition 9:

$$\vdash FT_{PV} \text{ p } [LF1;LF2] [DC_DC1;DC_DC2] [SA1;SA2] [DC_AC1;DC_AC2] = FT_{Tree} \text{ p } (OR [OR [LF1;DC_DC1;DC_AC1;SA1]; OR [LF2;DC_DC2;DC_AC2;SA2]])$$

Definition 10:

$$\vdash FT_{STEAM} \text{ p } [B01;B02;B03] [TA1;TA2;TA3] = FT_{Tree} \text{ p } (AND [AND [B01;TA1]; AND [B02;TA2]; AND [B03;TA3]])$$

Steps 2 and 3 (Construction of a CCD and Reduction):

Construct a formal complete CCD for all loads in our case study (Fig. 10), i.e., A, B, C, and D, then remove the

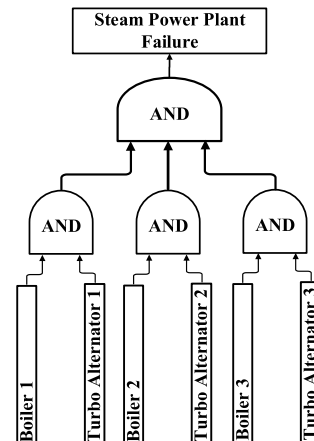


FIGURE 12. FT model of a steam power plant.

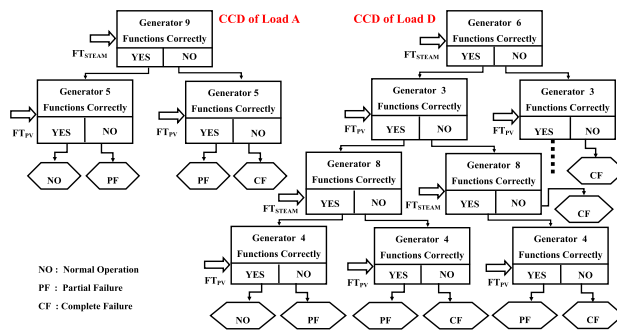


FIGURE 13. CCD analysis of loads A and D.

irrelevant decision boxes according to the electrical power network functional behavior. For instance, we can model the CCD models for loads A and D, as shown in Fig. 13, respectively, in HOL4 as:

Definition 11:

$$\vdash CCD_LOAD_A = CONSEQ_BOX \text{ p } [[DEC_BOX \text{ p } 1 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 1 (FT_{PV}, FT_{PV})]; [DEC_BOX \text{ p } 1 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 0 (FT_{PV}, FT_{PV})]; [DEC_BOX \text{ p } 0 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 1 (FT_{PV}, FT_{PV})]; [DEC_BOX \text{ p } 0 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 0 (FT_{PV}, FT_{PV})]]$$

Definition 12:

$$\vdash CCD_LOAD_D = CONSEQ_BOX \text{ p } [[DEC_BOX \text{ p } 1 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 1 (FT_{PV}, FT_{PV})]; DEC_BOX \text{ p } 1 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 1 (FT_{PV}, FT_{PV})]; [DEC_BOX \text{ p } 1 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 1 (FT_{PV}, FT_{PV})]; DEC_BOX \text{ p } 1 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 0 (FT_{PV}, FT_{PV})]; ...; [DEC_BOX \text{ p } 0 (\overline{FT_{STEAM}}, FT_{STEAM}); DEC_BOX \text{ p } 0 (FT_{PV}, FT_{PV})]]$$

Step 4 (Probabilistic analysis):

We can use our proposed formal approach to express component-level failure/reliability probabilistic expressions of electrical power grids, which enable us to analyze the cascading dependencies with many subsystem levels, based on any probabilistic distribution and failure rates. In this work, we assumed that the failure of each component in the power plants is exponentially distributed. This can be formalized in HOL4 as:

Definition 13:

$$\vdash \text{EXP_DISTRIB } p \times \lambda_X = \forall t. 0 \leq t \Rightarrow (\text{CDF } p \times t = 1 - e^{(-\lambda_X t)})$$

where the failure function CDF, as described in Section III-B, takes a component X and returns a set of all the values less or equal to a value t , i.e., $X \leq t$. λ_X is the failure rate of the variable X and t is a time index.

1) FOR ANALYSIS

Using Definitions 9 and 10 with the assumption that the failure states of components are exponentially distributed, we can formally specify the probabilistic FOR expression for both PV and steam power plants, in HOL4 as:

Definition 14:

$$\vdash \text{FOR}_{PV} p [LF1; LF2] [DC_DC1; DC_DC2] [SA1; SA2] [DC_AC1; DC_AC2] = \text{prob } p (\text{FT}_{PV} p (\downarrow [LF1; LF2]) (\downarrow [DC_DC1; DC_DC2]) (\downarrow [SA1; SA2]) (\downarrow [DC_AC1; DC_AC2])))$$

Definition 15:

$$\vdash \text{FOR}_{STEAM} p [BO1; BO2; BO3] [TA1; TA2; TA3] = \text{prob } p (\text{FT}_{STEAM} p (\downarrow [BO1; BO2; BO3]) (\downarrow [TA1; TA2; TA3]))$$

where the function \downarrow takes a list of \mathcal{N} components and assigns an exponential failing event to each component in the list.

We can formally verify the above-expressions of FOR_{PV} and FOR_{STEAM} , in HOL4 as:

Theorem 9:

$$\vdash \text{FOR}_{PV} p [LF1; LF2] [DC_DC1; DC_DC2] [SA1; SA2] [DC_AC1; DC_AC2] = 1 - e^{(-\lambda_{LF1} t)} \times e^{(-\lambda_{LF2} t)} \times e^{(-\lambda_{DC_DC1} t)} \times e^{(-\lambda_{DC_DC2} t)} \times e^{(-\lambda_{SA1} t)} \times e^{(-\lambda_{SA2} t)} \times e^{(-\lambda_{DC_AC1} t)} \times e^{(-\lambda_{DC_AC2} t)}$$

Theorem 10:

$$\vdash \text{FOR}_{STEAM} p [BO1; BO2; BO3] [TA1; TA2; TA3] = (1 - e^{(-\lambda_{BO1} t)}) \times (1 - e^{(-\lambda_{BO2} t)}) \times (1 - e^{(-\lambda_{BO3} t)}) \times (1 - e^{(-\lambda_{TA1} t)}) \times (1 - e^{(-\lambda_{TA2} t)}) \times (1 - e^{(-\lambda_{TA3} t)})$$

2) SAIDI ANALYSIS

Using Theorems 1-10 with the assumption that the failure states of components are exponentially distributed, we can formally verify SAIDI_G expression (Eq. 4), in HOL4 as:

Theorem 11:

$$\vdash \text{SAIDI} [[\text{CONSEQ_PATH } p [\text{DEC_BOX } p 1 (\text{FTree } p (\text{NOT } (\text{FT}_{STEAM} p (\downarrow [BO1; BO2; BO3]) (\downarrow [TA1; TA2; TA3]))), \text{FT}_{STEAM} p (\downarrow [BO1; BO2; BO3]) (\downarrow [TA1; TA2; TA3]))); \text{DEC_BOX } p 0 (\text{FTree } p (\text{NOT } (\text{FT}_{PV} p (\downarrow [LF1; LF2]) (\downarrow [DC_DC1; DC_DC2]) (\downarrow [SA1; SA2]) (\downarrow [DC_AC1; DC_AC2]))), \text{FT}_{PV} p (\downarrow [LF1; LF2]) (\downarrow [DC_DC1; DC_DC2]) (\downarrow [SA1; SA2]) (\downarrow [DC_AC1; DC_AC2]))); [\text{DEC_BOX } p 0 (\text{FTree } p (\text{NOT } (\text{FT}_{STEAM} p (\downarrow [BO1; BO2; BO3]) (\downarrow [TA1; TA2; TA3]))), \text{FT}_{STEAM} p (\downarrow [BO1; BO2; BO3]) (\downarrow [TA1; TA2; TA3]))); \text{DEC_BOX } p 1 (\text{FTree } p (\text{NOT } (\text{FT}_{PV} p (\downarrow [LF1; LF2]) (\downarrow [DC_DC1; DC_DC2]) (\downarrow [SA1; SA2]) (\downarrow [DC_AC1; DC_AC2]))), \text{FT}_{PV} p (\downarrow [LF1; LF2]) (\downarrow [DC_DC1; DC_DC2]) (\downarrow [SA1; SA2]) (\downarrow [DC_AC1; DC_AC2])))]]; \dots] [\text{MTTR_LoadA; MTTR_LoadB; MTTR_LoadC; MTTR_LoadD}] [\text{CN_LoadA; CN_LoadB; CN_LoadC; CN_LoadD}] p = \frac{((1 - (1 - e^{(-\lambda_{BO1} t)}) \times (1 - e^{(-\lambda_{BO2} t)}) \times (1 - e^{(-\lambda_{BO3} t)}) \times (1 - e^{(-\lambda_{TA1} t)}) \times (1 - e^{(-\lambda_{TA2} t)}) \times (1 - e^{(-\lambda_{TA3} t)})) \times (1 - e^{(-\lambda_{LF1} t)} \times e^{(-\lambda_{LF2} t)} \times e^{(-\lambda_{DC_DC1} t)} \times e^{(-\lambda_{DC_DC2} t)} \times e^{(-\lambda_{DC_AC1} t)} \times e^{(-\lambda_{DC_AC2} t)} \times e^{(-\lambda_{SA1} t)} \times e^{(-\lambda_{SA2} t)} + (1 - e^{(-\lambda_{BO1} t)}) \times (1 - e^{(-\lambda_{BO2} t)}) \times (1 - e^{(-\lambda_{BO3} t)}) \times (1 - e^{(-\lambda_{TA1} t)}) \times (1 - e^{(-\lambda_{TA2} t)}) \times (1 - e^{(-\lambda_{TA3} t)}) \times e^{(-\lambda_{LF1} t)} \times e^{(-\lambda_{LF2} t)} \times e^{(-\lambda_{DC_DC1} t)} \times e^{(-\lambda_{DC_DC2} t)} \times e^{(-\lambda_{DC_AC1} t)} \times e^{(-\lambda_{DC_AC2} t)} \times e^{(-\lambda_{SA1} t)} \times e^{(-\lambda_{SA2} t)}) \times \text{MTTR_LoadA} \times \text{CN_LoadA} + \dots)}{\text{CN_LoadA} + \text{CN_LoadB} + \text{CN_LoadC} + \text{CN_LoadD}}$$

To further facilitate the exploitation of our proposed approach for power grid reliability engineers, we defined a Standard Meta Language (SML) functions [32] that can numerically evaluate the above-verified expressions of FOR_{PV} , FOR_{STEAM} , and SAIDI . Subsequently, we compared our results with MATLAB CCD algorithm based on Monte-Carlo Simulation (MCS) and also with other existing subsystem-level reliability analysis techniques, such as HiP-HOPS and FMR, to ensure the accuracy of our computations, which is presented in the next section.

B. EXPERIMENTAL RESULTS AND DISCUSSION

Considering the failure rates of the power plant components λ_{BO} , λ_{TA} , λ_{LF} , λ_{DC_DC} , λ_{DC_AC} and λ_{SA} are 0.91, 0.84,

0.96, 0.67, 0.22, and 0.56 per year [37], respectively. Also, assuming that $MTTR_{Load_A}$, $MTTR_{Load_B}$, $MTTR_{Load_C}$, and $MTTR_{Load_D}$ are 12, 20, 15, and 10 hours/interruption [38] and CN_{Load_A} , CN_{Load_B} , CN_{Load_C} , and CN_{Load_D} are 500, 1800, 900, and 2500 customers, respectively. The reliability study is undertaken for 1 year, i.e., $t = 8760$. Based on the given data, we can evaluate FOR and $SAIDI$ for the electrical power network (Fig. 10) using following techniques:

- 1) Our proposed SML functions to evaluate the *verified* expressions of FOR_{PV} , FOR_{STEAM} , and $SAIDI$ in HOL4 (Theorems 9-11), as shown in Fig. 14.
- 2) MATLAB MCS-based toolbox that uses a random-based algorithm to obtain FOR and $SAIDI$ for the electrical grid. The steps followed in this technique are as follows [39]:

- Read the values of failure rate λ in *f/hours* and repair time r in hours for each component
- Generate a random number U
- Calculate the predicted next Time to Fail (TTF) and Time to Repair (TTR) from the equations

$$TTF = \frac{-\ln U}{\lambda} \quad TTR = \frac{-\ln U}{r} \quad (5)$$

- Repeat the above iterative process till the number of iterations exceeds $1e5$

Based on the above-mentioned MCS steps, we obtain different results of FOR and $SAIDI$ every run of the algorithm depending on the generated random number with a tolerance error between 4-9%. So, we present in Table 7 the best-estimated results of FOR and $SAIDI$ in MATLAB based on the MCS approach with the least errors. Subsequently, we take the mean average of all the obtained FOR and $SAIDI$ results for the power grid.

- 3) The Failure Mode Reasoning (FMR) approach, which identifies all the failure modes of safety-critical system

```
> FOR_PV = 0.991933212861 /year
> FOR_STEAM = 0.0388700719343 /year
> SAIDI = 6.37276953475 (Hours / System Customer)
> val it = (): unit
*** Emacs/HOL command completed ***
```

FIGURE 14. SML Functions: FOR and $SAIDI$ Results.

TABLE 7. MATLAB MCS: FOR and $SAIDI$ results.

Run	FOR_{PV}	FOR_{STEAM}	$SAIDI$
1	88.55e-2	36.18e-3	5.8023
2	107.19e-2	40.03e-3	6.5045
3	93.52e-2	36.35e-3	6.0222
5	110.17e-2	43.03e-3	7.0495
4	95.24e-2	38.66e-3	6.3960
Average	98.93e-2	38.85e-3	6.3549

inputs that can result in an undesired state at its output. The FMR process consists of four main stages [10]:

- a) *Composition*: Failure mode variables are defined and a set of logical implication statements is generated that express local failure modes.
- b) *Substitution*: Local statements will be combined to create a single global implication statement between the critical-system inputs and outputs.
- c) *Simplification*: The complex formula is simplified, where we trim off any redundant statements.
- d) *Calculation*: The probability of failure is evaluated using the component failure rates.

Based on the above-mentioned FMR procedures, we can express the component-level failure analysis of the PV power plant (Fig. 11) as:

$$(\hat{o} = \dot{f}) \Rightarrow (\hat{x}_1 = \dot{f} \vee \hat{x}_2 = \dot{f}) \quad (6)$$

The above equation means that if the output o is *False* by fault then either one of its inputs to the OR gate, i.e., x_1 or x_2 , must be *False* by fault. We now need to determine what can cause $\hat{x}_1 = \dot{f}$ and $\hat{x}_2 = \dot{f}$. Similar to Eq. 6, we can write:

$$(\hat{x}_1 = \dot{f}) \Rightarrow (\hat{x}_3 = \dot{f} \vee \hat{x}_4 = \dot{f} \vee \hat{x}_5 = \dot{f} \vee \hat{x}_6 = \dot{f}) \quad (7)$$

$$(\hat{x}_2 = \dot{f}) \Rightarrow (\hat{x}_7 = \dot{f} \vee \hat{x}_8 = \dot{f} \vee \hat{x}_9 = \dot{f} \vee \hat{x}_{10} = \dot{f}) \quad (8)$$

where $x_3, x_4, x_5, x_6, x_7, x_8, x_9, x_{10}$ are $LF_1, DC_DC_1, DC_AC_1, SA_1, LF_2, DC_DC_2, DC_AC_2, SA_2$, respectively. Similarly, we can express the component-level failure analysis of the steam power plant (Fig. 12) as:

$$(\hat{o} = \dot{f}) \Rightarrow (\hat{x}_{11} = \dot{f} \wedge \hat{x}_{12} = \dot{f} \wedge \hat{x}_{13} = \dot{f}) \quad (9)$$

$$(\hat{x}_{11} = \dot{f}) \Rightarrow (\hat{x}_{14} = \dot{f} \wedge \hat{x}_{15} = \dot{f}) \quad (10)$$

$$(\hat{x}_{12} = \dot{f}) \Rightarrow (\hat{x}_{16} = \dot{f} \wedge \hat{x}_{17} = \dot{f}) \quad (11)$$

$$(\hat{x}_{13} = \dot{f}) \Rightarrow (\hat{x}_{18} = \dot{f} \wedge \hat{x}_{19} = \dot{f}) \quad (12)$$

where $x_{14}, x_{15}, x_{16}, x_{17}, x_{18}, x_{19}$, are $BO_1, TA_1, BO_2, TA_2, BO_3, TA_3$, respectively. Table 8 shows the probabilistic results of FOR_{PV} , FOR_{STEAM} , and $SAIDI$ based on FMR analysis using the assumed failure rates of the power plant components.

According to Jahanian *et al.* [11], the soundness of the obtained FMR equations (Eq. 6 to Eq. 12) needs to be proven mathematically.

- 4) The HiP-HOPS software for failure analysis, which can perform FMECA analysis by given architectural blocks that hierarchically describe a safety-critical system at the subsystem level. Fig. 15 and Fig. 16 depict the

TABLE 8. FMR: FOR and $SAIDI$ results.

FOR_{PV}	FOR_{STEAM}	$SAIDI$
99.19e-2	38.87e-3	6.3728

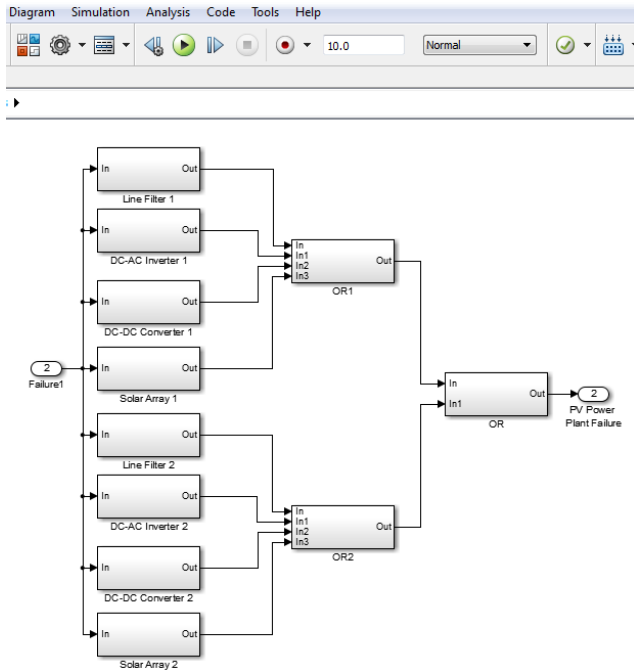


FIGURE 15. HiP-HOPS: PV Plant FMECA analysis.

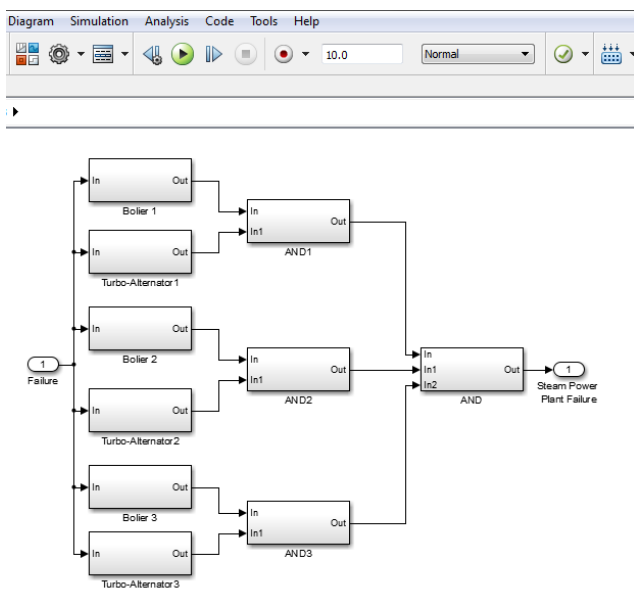


FIGURE 16. HiP-HOPS: Steam plant FMECA analysis.

FMECA analysis of the PV and steam power plants using the HiP-HOPS software, respectively. The probabilistic results of FOR_{PV} , FOR_{STEAM} , and $SAIDI$ based on HiP-HOPS analysis are equivalent to the FMR analysis results presented in Table 8.

It can be observed that $SAIDI$ result obtained from our formal HOL4 analysis are approximately equivalent to the corresponding ones calculated using FMR and HiP-HOPS approaches. On the other hand, MATLAB MCS-based uses a random-based algorithm, which estimates different results

of FOR and $SAIDI$ every generation of a random number with errors between 4-9%. This clearly demonstrates that our analysis is not only providing the correct result but also with a *formally proven* reliability expressions (Theorems 9-11) compared to simulation tools, i.e., the soundness of subsystem-level reliability analysis. By performing the formal CCD step-analysis of a real-world 39-bus electrical power network, we demonstrated the practical effectiveness of the proposed CCD formalization in HOL4, which will help power design engineers to meet the desired quality requirements. Also, our proposed formal approach can be used to analyze larger scale CCD models of other complex electrical power system applications, such as Smartgrids [1].

VI. CONCLUSION

In this paper, we developed a formal approach for Cause-Consequence Diagrams (CCD), which enables safety engineers to perform N -level CCD analysis of safety-critical systems within the sound environment of the HOL4 theorem prover. Our proposed approach provides new CCD mathematical formulations, which their correctness was verified in the HOL4 theorem prover. These formulations are capable of performing CCD analysis of *multi-state* system components and based on any given probabilistic distribution and failure rates. These features are not available in any other existing approaches for subsystem-level reliability analysis. The proposed formalization is limited to perform CCD-based reliability analysis at the subsystem level that integrates static dependability analysis. However, this formalization is *generic* and can be extended to perform dynamic failure analysis of dynamic subsystems where no dependencies exist in different subsystems. We demonstrated the practical effectiveness of the proposed CCD formalization by performing the formal CCD step-analysis of a standard *IEEE 39-bus* electrical power network system and also formally verified the power plants Force Outage Rate (FOR) and the System Average Interruption Duration Index ($SAIDI$). Eventually, we compared the FOR and $SAIDI$ results obtained from our formal CCD-based reliability analysis with the corresponding ones using MATLAB based on Monte-Carlo Simulation (MCS), the HiP-HOPS software tool, and the Failure Mode Reasoning (FMR) approach. As future work, we plan to integrate Reliability Block Diagrams (RBDs) [40] as reliability functions in the CCD analysis, which will enable us to analyze hierarchical systems with different component success configurations, based on our CCD formalization in the HOL4 theorem prover.

REFERENCES

- [1] X. Fang, S. Misra, G. Xue, and D. Yang, "Smart grid—The new and improved power grid: A survey," *IEEE Commun. Surveys Tuts.*, vol. 14, no. 4, pp. 944–980, Dec. 2011.
- [2] M. Rahman, "Power electronics and drive applications for the automotive industry," in *Proc. Conf. Power Electron. Syst. Appl.*, 2004, pp. 156–164.
- [3] J. D. Andrews and L. M. Ridley, "Reliability of sequential systems using the cause—Consequence diagram method," *Proc. Inst. Mech. Eng. E, J. Process Mech. Eng.*, vol. 215, no. 3, pp. 207–220, 2001.

- [4] M. Towhidnejad, D. R. Wallace, and A. M. Gallo, "Fault tree analysis for software design," in *Proc. 27th Annu. NASA Goddard/IEEE Softw. Eng. Workshop*, 2002, pp. 24–29.
- [5] I. A. Papazoglou, "Mathematical foundations of event trees," *Rel. Eng. Syst. Saf.*, vol. 61, no. 3, pp. 169–183, Sep. 1998.
- [6] O. Bäckström, Y. Butkova, H. Hermanns, J. Krčál, and P. Krčál, "Effective static and dynamic fault tree analysis," in *Computer Safety, Reliability, and Security*, vol. 9922. Cham, Switzerland: Springer, 2016, pp. 266–280.
- [7] Y. Papadopoulos, M. Walker, D. Parker, E. Rude, R. Hamann, A. Uhlig, U. Grätz, and R. Lien, "Engineering failure analysis and design optimisation with HiP-HOPS," *Eng. Failure Anal.*, vol. 18, no. 2, pp. 590–608, Mar. 2011.
- [8] *HiP-HOPS*. Accessed: 2020. [Online]. Available: <https://hip-hops.co.uk/>
- [9] S. Kabir, K. Aslanefat, I. Sorokos, Y. Papadopoulos, and Y. Gheraibia, "A conceptual framework to incorporate complex basic events in HiP-HOPS," in *Model-Based Safety and Assessment* (Lecture Notes in Computer Science), vol. 11842. Cham, Switzerland: Springer, 2019, pp. 109–124.
- [10] H. Jahanian, "Failure mode reasoning," in *Proc. Int. Conf. Syst. Rel. Saf.*, 2019, pp. 295–303.
- [11] H. Jahanian, D. Parker, M. Zeller, A. McIver, and Y. Papadopoulos, "Failure mode reasoning in model based safety analysis," 2020, *arXiv:2005.06279*. [Online]. Available: <http://arxiv.org/abs/2005.06279>
- [12] M. Čepin, *Assessment of Power System Reliability: Methods and Applications*. London, U.K.: Springer-Verlag, 2011.
- [13] T. Liu, J. Tong, and J. Zhao, "Probabilistic risk assessment framework development for nuclear power plant," in *Proc. IEEE Int. Conf. Ind. Eng. Eng. Manage.*, Dec. 2008, pp. 1330–1334.
- [14] J. D. Andrews and L. M. Ridley, "Application of the cause–consequence diagram method to static systems," *Rel. Eng. Syst. Saf.*, vol. 75, no. 1, pp. 47–58, Jan. 2002.
- [15] L. M. Ridley, "Dependency modelling using fault-tree and cause–consequence analysis," Ph.D. dissertation, Dept. Aeronaut. Automot. Eng., Loughborough Univ., Loughborough, U.K., 2000.
- [16] M. Bevilacqua, M. Braglia, and R. Gabbrielli, "Monte Carlo simulation approach for a modified FMECA in a power plant," *Qual. Rel. Eng. Int.*, vol. 16, no. 4, pp. 313–324, 2000.
- [17] R. E. Mackiewicz, "Overview of IEC 61850 and benefits," in *Proc. Power Eng. Soc. Gen. Meeting (PES TD)*, 2006, pp. 623–630.
- [18] B. Gallina, E. Gómez-Martínez, and C. B. Earle, "Deriving safety case fragments for assessing MBASafe's compliance with EN 50128," in *Proc. Conf. Softw. Process Improvement Capability Determination*. Cham, Switzerland: Springer, 2016, pp. 3–16.
- [19] R. Palin, D. Ward, I. Habli, and R. Rivett, "ISO 26262 safety cases: Compliance and assurance," in *Proc. 6th IET Int. Conf. Syst. Saf.*, 2011, pp. 1–6.
- [20] O. Hasan and S. Tahar, "Formal verification methods," in *Encyclopedia of Information Science and Technology*, 3rd ed. Hershey, PA, USA: IGI Global, 2015, pp. 7162–7170.
- [21] *HOL Theorem Prover*. Accessed: 2020. [Online]. Available: <https://hol-theorem-prover.org>
- [22] J. J. Grainger and W. D. Stevenson, *Power System Analysis*. New York, NY, USA: McGraw-Hill, 2003.
- [23] F. Ortmeier, W. Reif, and G. Schellhorn, "Deductive cause-consequence analysis (DCCA)," *IFAC Proc. Volumes*, vol. 38, no. 1, pp. 62–67, 2005.
- [24] *SMV*. Accessed: 2020. [Online]. Available: <http://www.cs.cmu.edu/mod-elcheck/smv.html>
- [25] D. Miller and G. Nadathur, *Programming With Higher-Order Logic*. Cambridge, U.K.: Cambridge Univ. Press, 2012.
- [26] W. Ahmad and O. Hasan, "Towards formal fault tree analysis using theorem proving," in *Intelligent Computer Mathematics* (Lecture Notes in Computer Science), vol. 9150. Cham, Switzerland: Springer, 2015, pp. 39–54.
- [27] Y. Elderhalli, O. Hasan, and S. Tahar, "A framework for formal dynamic dependability analysis using HOL theorem proving," in *Proc. Conf. Intell. Comput. Math.* Cham, Switzerland: Springer, 2020, pp. 105–122.
- [28] M. Abdelghany, W. Ahmad, and S. Tahar, "A formally verified HOL4 algebra for event trees," 2020, *arXiv:2004.14384*. [Online]. Available: <http://arxiv.org/abs/2004.14384>
- [29] G. Vyzaita, S. Dunnett, and J. Andrews, "Cause–consequence analysis of non-repairable phased missions," *Rel. Eng. Syst. Saf.*, vol. 91, no. 4, pp. 398–406, Apr. 2006.
- [30] H. Xu and J. B. Dugan, "Combining dynamic fault trees and event trees for probabilistic risk assessment," in *Proc. Annu. Symp. Rel. Maintainability (RAMS)*, 2004, pp. 214–219.
- [31] L. R. Olsen, J. A. Kay, and M. Van Krey, "Enhanced safety features in motor control centers and drives for diagnostics and troubleshooting," in *Proc. IEEE IAS Electr. Saf. Workshop*, Jan. 2015, pp. 1–9.
- [32] M. Abdelghany, *Cause-Consequence Diagrams Formalization in HOL4*. Accessed: 2020. [Online]. Available: <https://github.com/hvg-concordia/CCD>
- [33] R. N. Allan, *Reliability Evaluation of Power Systems*. New York, NY, USA: Springer, 2013.
- [34] G. Bhatt and S. Afflulla, "Analysis of large scale PV penetration impact on IEEE 39-bus power system," in *Proc. IEEE 58th Int. Scientific Conf. Power Electr. Eng. Riga Tech. Univ. (RTUCON)*, Oct. 2017, pp. 1–6.
- [35] D. Gan, R. J. Thomas, and R. D. Zimmerman, "Stability-constrained optimal power flow," *IEEE Trans. Power Syst.*, vol. 15, no. 2, pp. 535–540, May 2000.
- [36] A. Alferidi and R. Karki, "Development of probabilistic reliability models of photovoltaic system topologies for system adequacy evaluation," *Appl. Sci.*, vol. 7, no. 2, p. 176, Feb. 2017.
- [37] W. Li, *Reliability Assessment of Electric Power Systems Using Monte Carlo Methods*. New York, NY, USA: Springer, 2013.
- [38] G. J. Anders and A. Vaccaro, *Innovations in Power Systems Reliability*. London, U.K.: Springer-Verlag, 2011.
- [39] A. K. Pradhan, S. K. Kar, and P. Dash, "Implementation of Monte Carlo simulation on the distribution network for its reliability assessment," in *Innovation in Electrical Power Engineering, Communication, and Computing Technology*. Singapore: Springer, 2020, pp. 219–228.
- [40] H. Xu, L. Xing, and R. Robidoux, "DRBD: Dynamic reliability block diagrams for system reliability modelling," *Int. J. Comput. Appl.*, vol. 31, no. 2, pp. 132–141, Jan. 2009.



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