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# Compact Metamaterial Based $4 \times 4$ Butler Matrix With Improved Bandwidth for 5G Applications

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**ABSTRACT** This paper proposes a novel compact  $4 \times 4$  butler matrix (BM) with improved bandwidth based on open-circuit coupled-lines and interdigital capacitor unit-cell to develop composite right/left handed (CRLH) transmission-line (TL) metamaterial structure. The BM is implemented by the combination of compact 3dB quadrature hybrid couplers, 0dB crossover and  $45^\circ$  phase shifter on a single FR4 substrate ( $\epsilon_r = 4.3$  and  $h = 1.66$  mm). The simulated and measured result shows that the return loss and isolation loss are better than 14 dB at all the ports, good insertion loss of  $-7 \pm 2$  dB, which cover the frequency range of 3.2 GHz to 3.75 GHz. The phase difference of  $-45^\circ$ ,  $135^\circ$ ,  $-135^\circ$  and  $+45^\circ$  are achieved with a maximum average phase tolerance of  $5^\circ$ . The overall dimension of the BM is  $70\text{mm} \times 73.7\text{mm}$ , which shows the compactness of the proposed design that is 75% size reduction and 8.2 times improvement in the bandwidth (550MHz) as compared to conventional BM. The CST microwave studio is used to design and perform the simulations. Additionally, the simulated and measured scattering parameters and phase differences show that they are in good agreement. This compact and improved bandwidth of the proposed BM is suitable for 5G antenna array beamforming network.

**INDEX TERMS** 5G, composite right/left handed (CRLH) transmission-line, metamaterial, beamforming network (BFN), Butler matrix (BM), branch line coupler (BLC).

## I. INTRODUCTION

The evolution in wireless communication systems in terms of enhanced performance by improving data-rate, power dissipation and latency leads to the emergence of 5G [1], [2]. It is predicted that upcoming 5G technology will provide the end-users; data-rates of  $\sim 10$  GB/s (optical fibre-like experience), reduced end-to-end latency and improved capacity of up to several billion users as compared to the previous wireless systems [3]–[5]. Another significant feature of the planned 5G technology is to exploit microwave (around 3–6 GHz) as well as millimetre-wave (mm-wave) frequency bands [2], [5]. This leads to greater spectral bandwidths and directional antenna arrays which can transmit focus radiated

power in any desired direction by using beamforming networks (BFNs) [5]. In addition to the smartphones, tablets and laptop computers, the huge influx of compact wireless-enabled wearable devices leads to an exponential increase in the wireless end-users who demand constant anytime, anywhere connectivity [6]. To provide good quality of service (QoS) with constant connectivity, the researchers have put a lot of efforts in the area of phased array antennas and their respective BFNs.

It is envisioned that in the upcoming 5G systems, phased array antennas (PAAs) will be playing a crucial role to get the desired output at the transceivers [7], [8]. The PAA consists of a BFN and antenna array. The main beam of an antenna is steered by the BFN network with the given phase and amplitude. There are many different types of BFNs reported in the literature [9]–[14], [16]–[18], [22]–[27]. The BFNs have two

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categories 1) Rotman lens and 2) Circuit-based beamformer. The Rotman lens [14] provide wideband characteristics, but due to its extremely large size, it is not considered a promising candidate for 5G applications. The circuit based beamformers are further classified into three types, which are Nolen, Blass and Butler Matrix, respectively. The most commonly used BFN due to its easy fabrication process and low-cost is the butler matrix (BM) which is used for the antenna array feed. It requires  $N$  input ports ( $N$  beams),  $N$  output ports,  $(N/2) \log_2(N)$  hybrid couplers, and  $(N/2) \log_2(N-1)$  fixed phase shifters to form the  $N \times N$  network [9]. Typical design of BM is a bilateral structure, which consists of three main components, i.e. couplers, crossovers and phase shifters.

The issue with conventional BM is that it has limited bandwidth and large size due to the hybrid couplers and phase shifters. So, the researcher started to introduce new designs of BM with an open-stub, modified hybrid branch line coupler (BLC), without crossover, without phase shifter and metamaterial transmission to minimize the size [10]–[15]. In [10], a miniaturized BM network implemented with the stub-loaded transmission lines is proposed and experimentally verified for multi-beam antenna array system. The size of the BM is reduced by 55% as compared to the conventional BM design. Another miniaturized BM design is presented in [12] by employing dumbbell-shaped cross-slot patch hybrid couplers and meandered lines based crossover. It achieves the fractional bandwidth improvement of 37.5% and a size reduction of 17%.

In [16], it is shown that the use of  $45^\circ$  and  $90^\circ$  phase shifters and four BLCs miniaturized by employing open-stubs in each transmission-line allows the size reduction of 42.68% as compared to the conventional BM. In [11], a miniaturized BM using cross-slot patch hybrid couplers and  $45^\circ$  phase shifters using short-circuited stubs are presented. The bandwidth of the BM is improved by 14%, and the size is reduced by 56% as compared to conventional BM, respectively. A novel compact BM without phase shifter is presented in [17] that contains couplers with  $-45^\circ$  and  $-90^\circ$  phase difference and a crossover. In [18], a novel electromagnetic metamaterial transmission-line (EM-MTM TL) is proposed by using the structure of symmetric double spiral lines (SDSLs). As per the literature review presented here, most of the discussed designs demonstrate a reduction in the area without much improvement in the bandwidth of the BM.

Therefore, in this paper, a compact and improved bandwidth BM is proposed by employing four 3dB BLCs based on the open-circuit coupled-lines technique and interdigital capacitor (IDC) CRLH-TL metamaterial structure [21], one 0dB crossover (instead of two 0 dB crossover in the conventional BM) and phase shifters of  $45^\circ$ . The crossover is designed by cascading two 3dB BLCs. The proposed  $4 \times 4$  BM is designed by using the CST microwave studio software for 5G applications. The proposed BM is designed using the flame-resistant (FR4) copper-clad substrate [19] with relative permittivity ( $\epsilon_r$ ) dielectric constant of 4.3 and thickness of 1.66 mm. The simulation and measured results

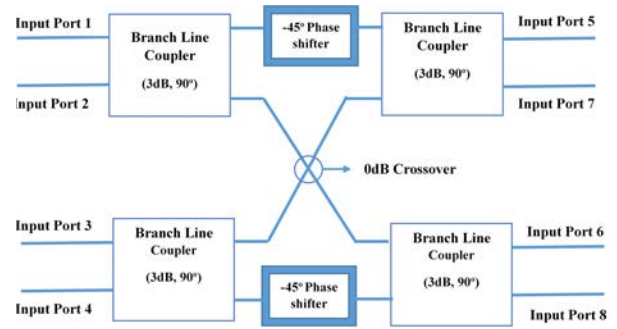


FIGURE 1. Block diagram showing the design of the proposed BM.

TABLE 1. Phase relation between ports at output of BM feed.

	Input Port 1	Input Port 2	Input Port 3	Input Port 4
Output Port 5	$135^\circ$	$45^\circ$	$90^\circ$	$0^\circ$
Output Port 6	$90^\circ$	$180^\circ$	$-45^\circ$	$45^\circ$
Output Port 7	$45^\circ$	$-45^\circ$	$180^\circ$	$90^\circ$
Output Port 8	$0^\circ$	$90^\circ$	$-45^\circ$	$135^\circ$
Phase Difference Between Consecutive Output Ports	$-45^\circ$	$+135^\circ$	$-135^\circ$	$+45^\circ$

suggest that the designed BM achieves excellent performance and it can be used as an ideal candidate for the beamforming network in the upcoming 5G antenna array systems.

The rest of the paper is organized as follows: Section 2 discusses the design configuration of the proposed BM and its relevant simulation and measured results. Section 3 presents the comparison of proposed work with the existing design, and finally, Section 4 draws conclusion.

## II. BUTLER MATRIX DESIGN CONFIGURATION

The proposed BM comprises of four  $90^\circ$  BLCs, one crossover and two phase shifters which generate  $-45^\circ$  phase shift. As shown in Fig. 1, it has four input ports labelled as port 1, port 2, port 3, and port 4 and four output ports labelled as port 5, port 6, port 7, and port 8, respectively.

Table 1 shows the phases that will be generated on each output port based on the selection of the input port. It has four cases; the phase difference between the consecutive output ports will be  $-45^\circ$  when the input port 1 is excited. When the input port 2 is excited, the phase difference between the consecutive output ports will be  $135^\circ$ . For the third case, the phase difference between the consecutive output ports will be  $-135^\circ$ , when the input port 3 is excited. When the input port 4 is excited, the phase difference between the output ports will be  $45^\circ$ .

The TL width and length of the BM element is calculated using microstrip feedline method [20], CST Microwave Studio software is used to perform all the design simulation and optimization of the individual components and also the

TABLE 2. Design specifications.

Parameters	Coupler	Crossover	Butler Matrix
Operating Frequency	3.5GHz	3.5GHz	3.5GHz
Method	Metamaterial transmission line	Metamaterial transmission line	Metamaterial transmission line
Required BW	1GHz	1GHz	500MHz
Phase Difference	90°	0°	-45°, 135°, -135°, 45°
Insertion Loss	-3dB±2	0dB	-7±2dB
Return loss	Below -10dB	Below -10dB	Below -10dB
Isolation loss	Below -10dB	Below -10dB	Below -10dB

combination of coupler and crossover to construct the BM. The proposed BLC, crossover and BM were validated with the design specifications provided in Table 2. The detailed explanation and design of BM components are given below.

A. BRANCH LINE COUPLER (BLC)

The BLC used in this work is a compact design based on the CRLH-TL metamaterial structure. The CRLH-TL metamaterial structure is achieved by inserting the open-circuit coupled-lines and interdigital capacitor (IDC) unit-cell for the vertical and horizontal arm of the BLC as shown in Fig. 2 (a-b). The impedance of the horizontal and vertical arms TL of the BLC is 35Ω and 50Ω, respectively. The width and length of both the arms of the BLC can be calculated by using the formula [20], [21] based on the impedances mentioned above. The optimized dimensions of the IDC-CRLH unit-cell finger width ( $w_c$ ), length ( $l_c$ ) and gap ( $s$ ) between the fingers are calculated and found to be 0.4mm, 3.9mm and 0.4mm, respectively based on Eq. (1-2) [22], [23]:

$$w_c \approx \frac{w}{\left(\frac{5N}{3} - \frac{2}{3}\right)} \tag{1}$$

$$s = \frac{2w_c}{3} \tag{2}$$

where in eq. (1),  $N$  indicates the number of fingers and  $w = 5\text{mm}$  refers to the width required to obtain the horizontal arm impedance of 35Ω. The dimension of the horizontal and vertical arm IDC unit-cell finger length  $l_c$  is optimized to be 3.9mm and 2.2mm, respectively for achieving the BLC center frequency of 3.5GHz. The horizontal and vertical IDC unit-cell finger length has a different dimension due to the impedance matching [20]. The open-circuit coupled-line inner strip width is 0.4mm. The IDC unit-cell and the fabricated BLC prototype are shown in Fig. 2(a-b), respectively. The BLC is considered as an important component for the BM design. The simulated and measured scattering parameter and phase difference results for the proposed BLC are

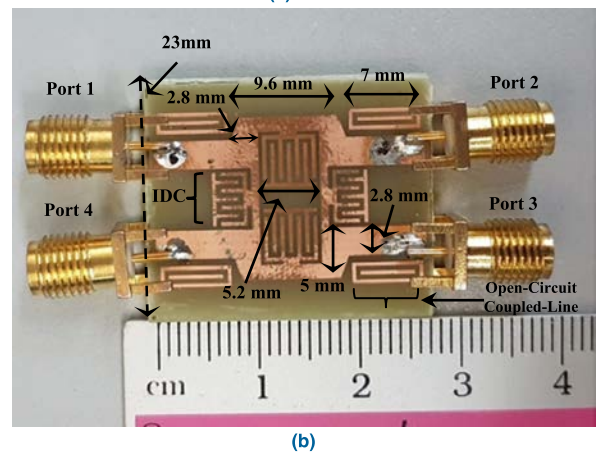
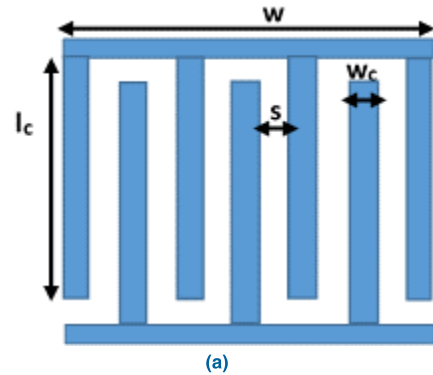


FIGURE 2. (a) IDC unit-cell (b) Fabricated prototype of the proposed BLC using FR4 substrate showing the respective BLC Ports 1-4, respectively and the PCB dimensions.

summarized in Fig. 3(a-b). The fabricated coupler operates between 2.74 GHz to 4.15 GHz frequency band for the return loss and isolation loss of below -10dB. The insertion loss result also shows very good performance and a variation of  $3 \pm 0.2\text{dB}$  in the same frequency band. The measured phase difference between the output ports is found to be 88°. The  $\lambda/4$  open-circuit coupled-lines and the IDC unit-cell are used to provide wide bandwidth and size reduction in the circuit.

B. 0dB CROSSOVER

The 0dB crossover is a four-port network with two input ports and two output ports named as port 1, port 4 and port 2, port 3, respectively. The crossover is designed by using two separate techniques. The first design consists of cascading two 3dB BLC combined together by using the copper tape and the glue, as shown in Fig. 4(a). It can be seen from Fig. 4(a) that in this design, both BLC shares the same ground plane, and it has an overall area of 59mm × 23mm, respectively. The second design of the crossover is considered compact because it is fabricated using single FR4 PCB with an overall area of 36mm x 23mm, as shown in Fig. 4(b). The total area of the compact crossover design shown in Fig. 4(b) is reduced by 39% as compared to the first design shown in Fig. 4(a). Since, the insertion loss  $S_{31}$  and  $S_{24}$  and the phase difference between  $S_{31}$  and  $S_{24}$  introduced by the two designs

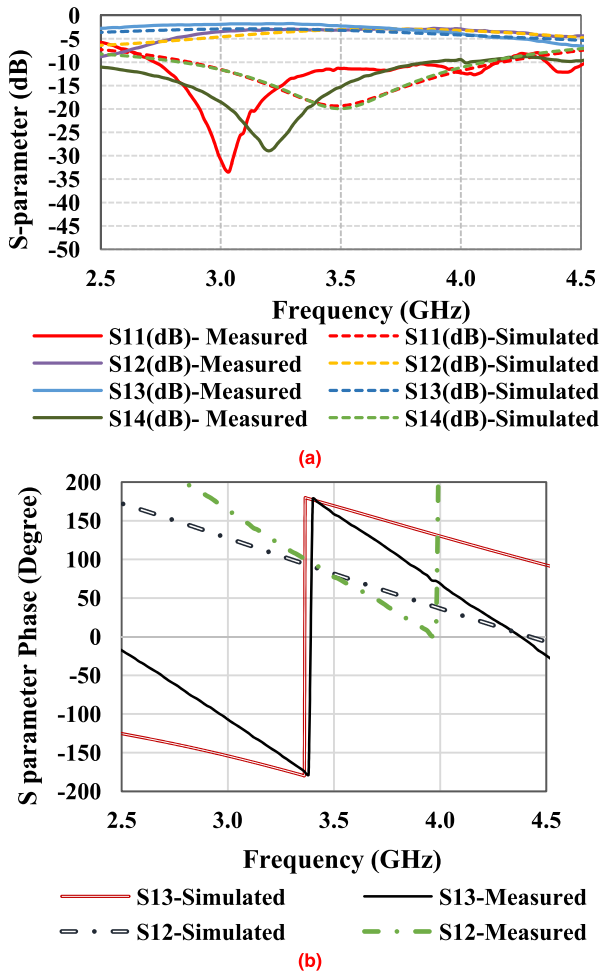


FIGURE 3. (a) S-Parameter response of the proposed simulated and fabricated BLC (b) Phase difference between the proposed simulated and fabricated prototype of the BLC.

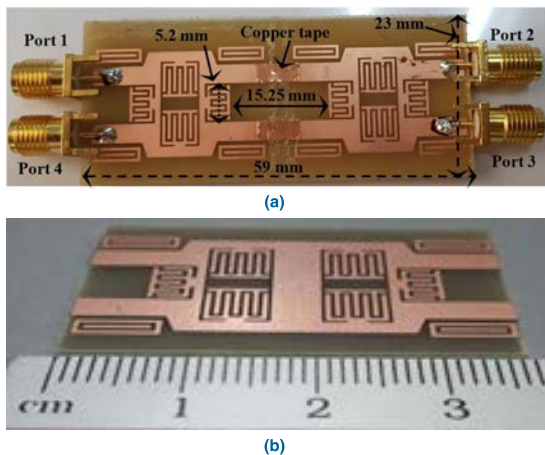


FIGURE 4. (a) Fabricated prototype of the 0dB crossover by cascading two 3dB BLC using FR4 substrate and copper tape, (b) Fabricated prototype of the compact 0dB crossover two 3dB BLC using FR4 substrate.

are same, so the compact designed crossover is selected for the development of the BM. The simulated and measured insertion loss is -0.19 dB and -0.5dB, respectively, at the operating frequency of 3.5GHz. Similarly, the simulated and

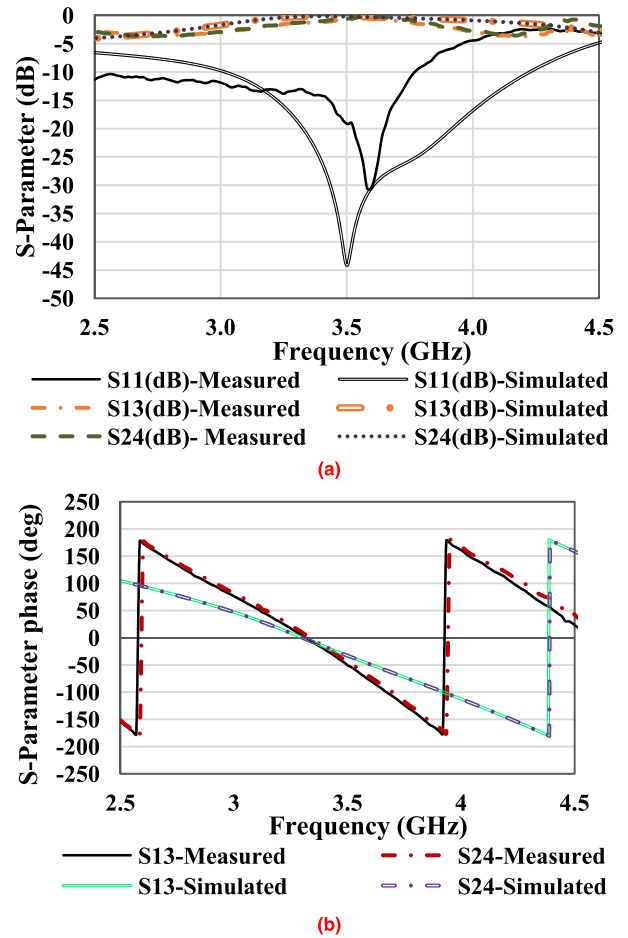


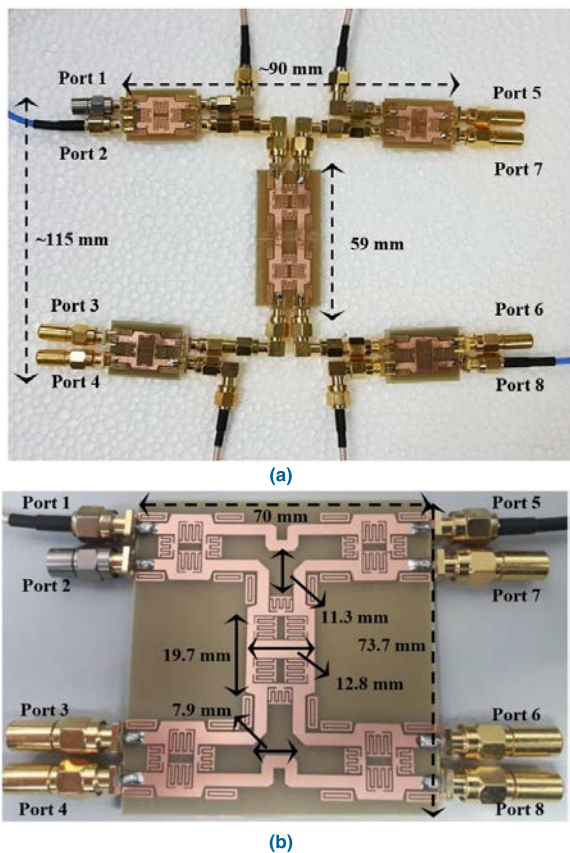
FIGURE 5. (a) S-Parameter response of the proposed simulated and fabricated compact 0dB crossover (b) Phase difference between the proposed simulated and fabricated prototype of the compact 0dB crossover.

measured phase difference between  $S_{31}$  and  $S_{24}$  is  $0^\circ$ , and  $5^\circ$ , respectively, which mean the phase shift introduced is very small and close to  $0^\circ$ . Fig. 5(a) shows the S-parameter response of simulated and measured compact 0dB crossover, whereas Fig. 5(b) shows the simulated and measured phase difference between  $S_{31}$  and  $S_{24}$ , respectively. The measured insertion loss and the phase difference validated the design specification mentioned in Table 2.

### C. 4 × 4 BUTLER MATRIX PERFORMANCE

The planar single layer implementation of the 4 × 4 compact BM with improved bandwidth is performed by the combination of above designed 3dB BLC using open coupled lines and IDC-CRLH TL, 0dB crossover and  $45^\circ$  phase shifter at the centre frequency of 3.5GHz. As shown in Fig. 6(a), we have used the RF coaxial cables of 20 cm to develop the  $45^\circ$  phase shift, SMA L-shaped male-female RF jacks and, SMA male-female RF connectors to make the connection between the coupler and the crossover. Fig. 6(a) shows the hybrid implementation of the 4 × 4 BM which is good for the proof-of-concept but cannot be implemented as the BFN in 5G antenna array systems because of size constraints and



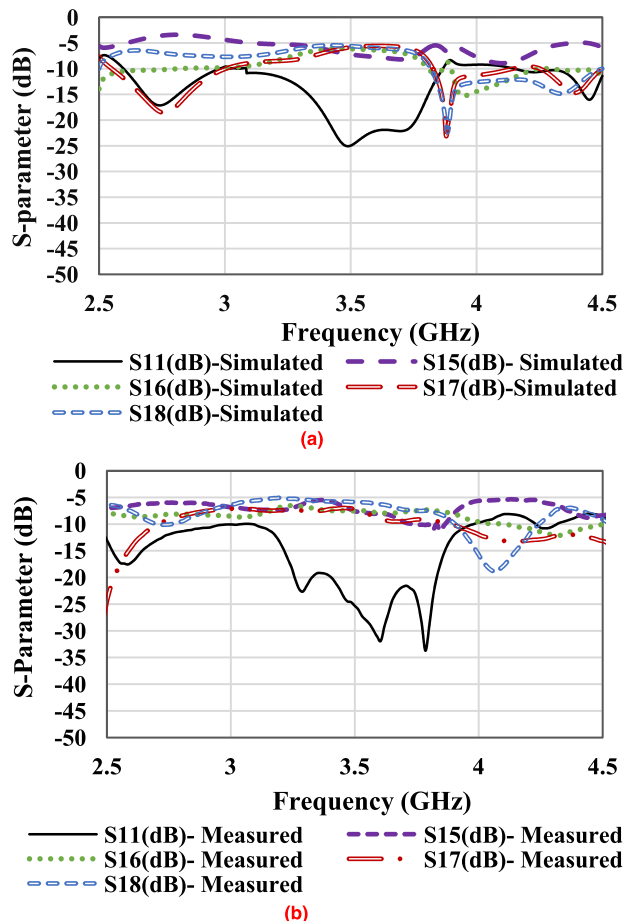


**FIGURE 6.** (a) Hybrid 4 × 4 BM by using the combination of BLCs and Crossovers with the help of SMA male-female RF connectors, SMA L-shape male-female RF jacks and RF coaxial cables (b) Compact 4 × 4 BM fabricated on a single FR4 substrate.

the extra losses induced by the SMA transitions and coaxial cables. In order to make the design compact and realistic for the implementation in 5G antenna array systems, it is fabricated by combining all the components on a single FR4 PCB sharing a common ground plane, as shown in Fig. 6(b). The compact design has a total area of 70mm × 73.7mm, as shown in Fig. 6(b). The simulated and measured results from both the 4 × 4 BM structures presented in Fig. 6(a-b) shows a good match, although there were some variations observed in the results which could be due to the extra losses and phase shifts induced by the cables and connectors. So, the discussion related to the results from now onwards will be based on the compact structure of the BM as shown in Fig. 6(b).

Fig. 6(b) shows the configuration of 4 × 4 BM, when a signal given to the input ports (port 1, port 2, port 3, port 4) is transferred to the output ports (port 5, port 6, port 7, port 8) with equal amplitude and specified phase difference as mentioned in Table 1.

Fig. 7(a-b) shows the simulated and measured results of the insertion and return losses, respectively when port 1 is excited, and all the other ports are terminated with 50Ω loads. These results show that the measured and simulated range of return loss is better than 13 dB and insertion loss detected at port 5, port 6, port 7 and port 8 is  $-7 \pm 2$  dB



**FIGURE 7.** Shows the (a) Simulated and (b) Measured response of the insertion loss and return loss for port 1 excitation, respectively.

from 3.2 GHz to 3.8GHz, respectively. So, the power splitting among the four output ports is approximately equal as per the insertion loss result. The measured return loss is  $-25$ dB and the average insertion loss ( $S_{15}$ ,  $S_{16}$ ,  $S_{17}$ ,  $S_{18}$ ) is  $-7$ dB at the operating frequency of 3.5GHz. The results obtained from the below graph validated our design specification mentioned in Table 2 are very promising and can be used in upcoming 5G systems.

Fig. 8(a-b) illustrates a good agreement between the simulated and measured results of the phase shift of adjacent output ports, when port 1 is excited. The phase difference between adjacent ports should be  $-45^\circ$  as per the design consideration in Table 1. In the simulation, phase difference was  $-44.2^\circ$  between port 5 and port 6 ( $S_{16} - S_{15}$ ),  $-52.8^\circ$  between ports 6 and port 7 ( $S_{17} - S_{16}$ ) and  $-40.3^\circ$  between ports 7 and port 8 ( $S_{18} - S_{17}$ ), respectively. So, the errors were 0.8, 7.8 and 4.7 degrees introducing an average error of 4.4 degrees, respectively. In the measured results, the phase difference was  $-42.7^\circ$  between port 5 and port 6 ( $S_{16} - S_{15}$ ),  $-51.4^\circ$  between ports 6 and port 7 ( $S_{17} - S_{16}$ ) and  $-54^\circ$  between ports 7 and port 8 ( $S_{18} - S_{17}$ ), respectively. So, the errors were 2.3, 6.4 and 9 degrees introducing an average error of 5.9 degrees. This small error in phase is due to the

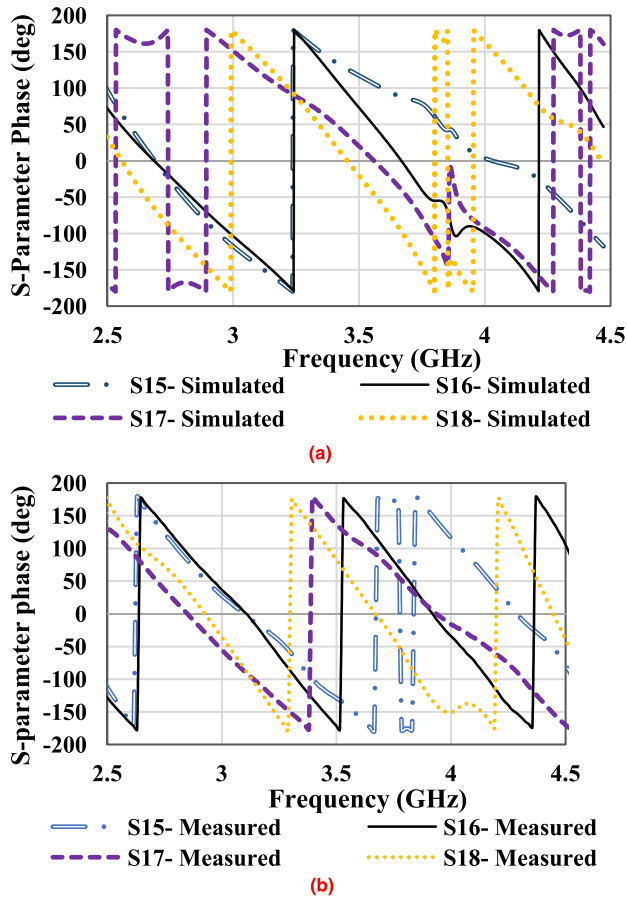


FIGURE 8. Shows the (a) Simulated and (b) Measured results of the phase shift of adjacent output ports, when Port 1 is excited.

variation in the electrical permittivity parameter of the lossy FR4 substrate. This frequency shift problem can be solved in future by using a low-loss Rogers substrate. These results suggest a very good agreement between the simulated and the measured phase difference in value, although the quadrant is different, as mentioned in the below Table 3.

Fig. 9(a-b) show the simulated and measured results of the insertion and return loss, when port 2 is excited, and all other ports are terminated with 50Ω load. These results in Fig. 9 (a-b) show that the measured and simulated range of return loss is better than 14 dB and insertion loss detected at port 5, port 6, port 7 and port 8 is  $-7 \pm 2$  dB from 3.2 GHz to 3.7GHz, respectively. So, the power splitting among the four output ports is approximately equal as per the insertion loss results. The measured return loss is  $-15$  dB and the average insertion loss ( $S_{25}$ ,  $S_{26}$ ,  $S_{27}$ ,  $S_{28}$ ) is  $-7$  dB at the operating frequency of 3.5GHz. The results in Fig. 9(a-b) are in agreement with each other when port 2 is excited, which suggest excellent BM performance.

Fig. 10(a-b) illustrates the results when port 2 is excited, and it shows a good agreement between the simulated and measured results of the phase shift of adjacent output ports. The phase difference between the adjacent ports should be  $135^\circ$  as per the design consideration provided in Table 1.

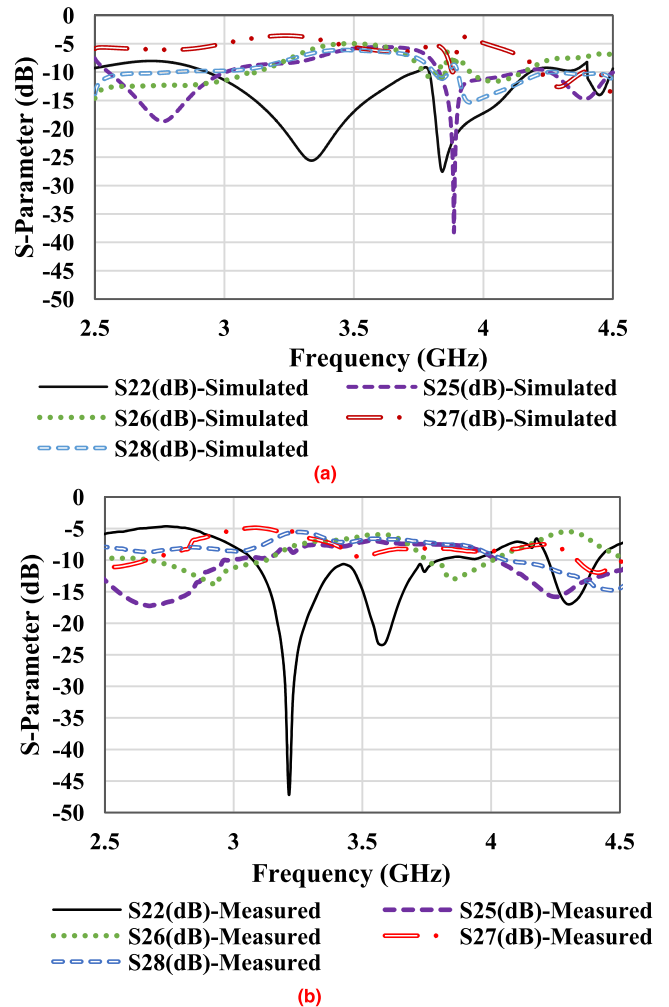


FIGURE 9. Shows the (a) Simulated and (b) Measured results of the insertion and return loss for the Port 2 excitation, respectively.

In the simulation, it is found that the phase difference between the output ports is  $145^\circ$ ,  $129^\circ$ ,  $141^\circ$ , respectively at the center frequency. These values differ from the desired value of  $135^\circ$  by 10, 6 and 6 degrees introducing an average error of 7.3 degrees. In the measured results, the phase difference between output ports is  $139^\circ$ ,  $142^\circ$ ,  $130.3^\circ$ , respectively, at the centre frequency of 3.5GHz. So, the errors were 4, 7 and 4.7 degrees introducing an average error of 5.2 degrees and very good agreement between simulated and measured phase difference in value.

Fig. 11(a-b), shows the simulated and measured results of the insertion and return loss, when port 3 is excited, and all other ports are terminated with 50Ω load. These results in Fig. 11 (a-b) show that the measured and simulated range of return loss is better than 13 dB and the insertion loss detected at port 5, port 6, port 7 and port 8 is  $-7 \pm 2$  dB between 3.2 GHz to 3.75GHz frequency band. So, the power splitting among the four output ports is approximately equal as per the insertion loss results. The measured return loss is  $-17$  dB and the average insertion loss ( $S_{35}$ ,  $S_{36}$ ,  $S_{37}$ ,  $S_{38}$ ) is  $-7.7$  dB at the operating frequency of 3.5GHz. The result obtained from the

TABLE 3. Simulated and measured phase difference of the proposed butler matrix at 3.5 GHz.

Ports	Phase difference between Port 5 and Port 6 (Degrees)		Phase difference between Port 6 and Port 7 (Degrees)		Phase difference between Port 7 and Port 8 (Degrees)		Desired Phase Difference (Degrees)
	Simulated	Measured	Simulated	Measured	Simulated	Measured	
Port 1	-44.2	-42.7	-52.8	-51.4	-40.3	-54	-45
Port 2	145	139	129	142	141	130.3	135
Port 3	-141.6	-136.5	-128	-141	-146.5	146	-135
Port 4	42.6	47.4	51	44	50	46.8	45

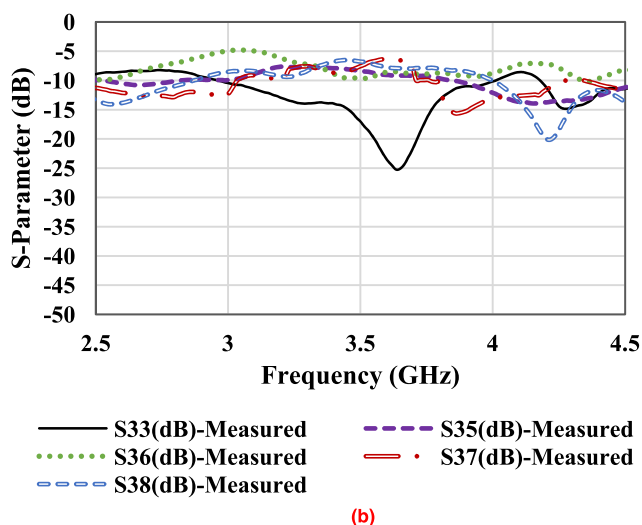
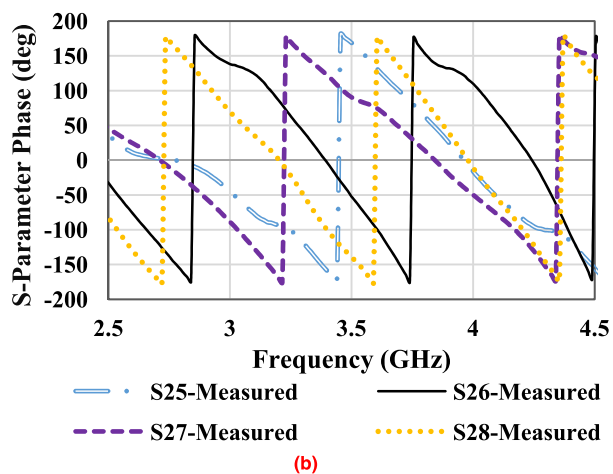
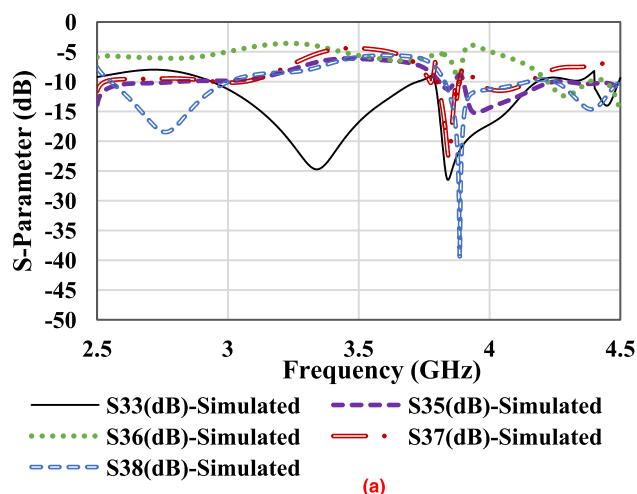
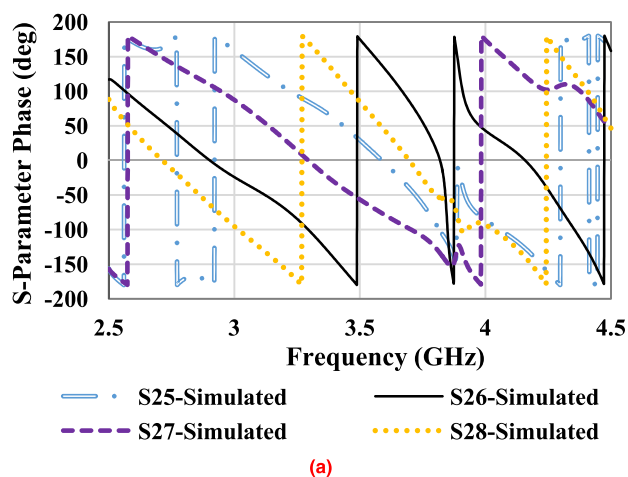


FIGURE 10. Shows the (a) Simulated and (b) Measured results of the phase shift of adjacent output ports, when Port 2 is excited.

FIGURE 11. Shows the (a) Simulated and (b) Measured results of the insertion and return loss for Port 3 excitation, respectively.

below graph shows good agreement between simulated and measure results.

Fig. 12(a-b), illustrates a good agreement between the simulated and measured results of the phase shift of adjacent output ports, when port 3 is excited. The phase difference between the adjacent ports should be  $-135^\circ$  as per the design consideration in Table 1. In the simulation results; the phase difference was  $-141.6^\circ$  between port 5 and 6 ( $S_{36}-S_{35}$ ),  $-128^\circ$  between ports 6

and 7 ( $S_{37}-S_{36}$ ) and  $-146.5^\circ$  between ports 7 and 8 ( $S_{38} - S_{37}$ ), respectively. So, the errors were 6.6, 7 and 11.5 degrees introducing an average error of 8.3 degrees, respectively. Whereas, in the measured results, the phase difference was  $-136.5^\circ$  between port 5 and 6 ( $S_{36}-S_{35}$ ),  $-141^\circ$  between ports 6 and 7 ( $S_{37}-S_{36}$ ) and  $-146^\circ$  between ports 7 and 8 ( $S_{38}-S_{37}$ ), respectively. So, the errors were

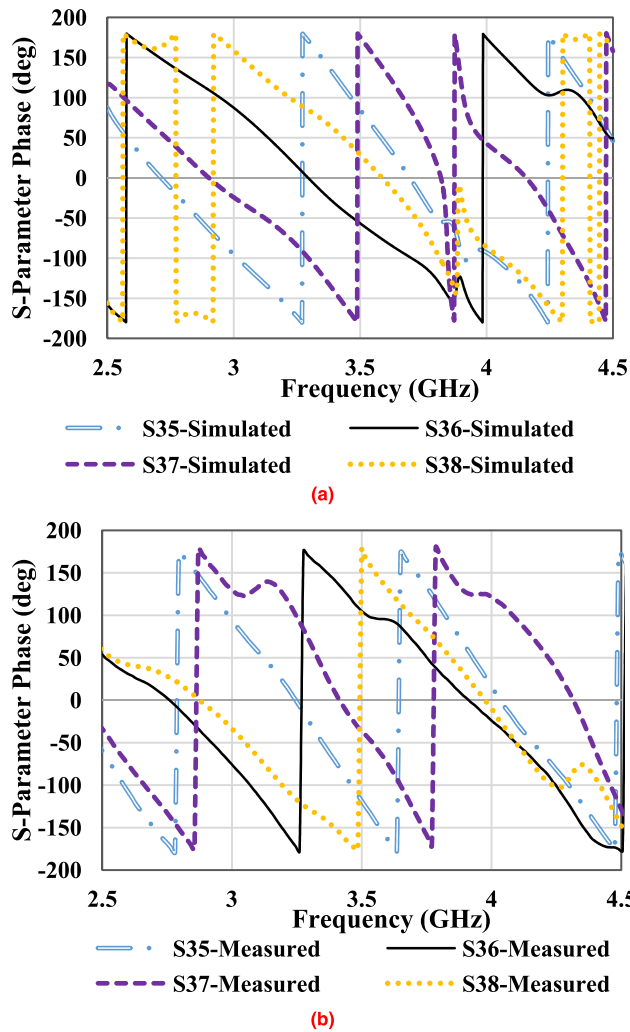


FIGURE 12. Shows the (a) Simulated and (b) Measured results of the phase shift of adjacent output ports, when Port 3 is excited.

1.5, 6 and 11 degrees, respectively introducing an average error of 6.1 degrees and very good agreement between the simulated and measured phase difference in value but the quadrant is different as summarized in Table 3.

Fig. 13(a-b) shows the simulated and measured insertion and the return loss results, when port 4 is excited, and all other ports are terminated with a 50Ω load. These results show that the measured and simulated range of return loss is better than 13 dB and insertion loss detected at port 5, port 6, port 7 and port 8 is  $-7 \pm 2$  dB between the 3.2 GHz to 3.75GHz frequency band. So, the power splitting among the four output ports is approximately equal as per the insertion loss results shown in Fig 13 (a-b). The measured return loss is -16.3dB and the average insertion loss ( $S_{45}$ ,  $S_{46}$ ,  $S_{47}$ ,  $S_{48}$ ) is  $-6.75$ dB at the operating frequency of 3.5GHz.

Fig. 14(a-b) illustrates a good agreement between the simulated and measured results of the phase shift of adjacent output ports, when port 4 excited. The phase difference between the adjacent ports should be 45° as per the design

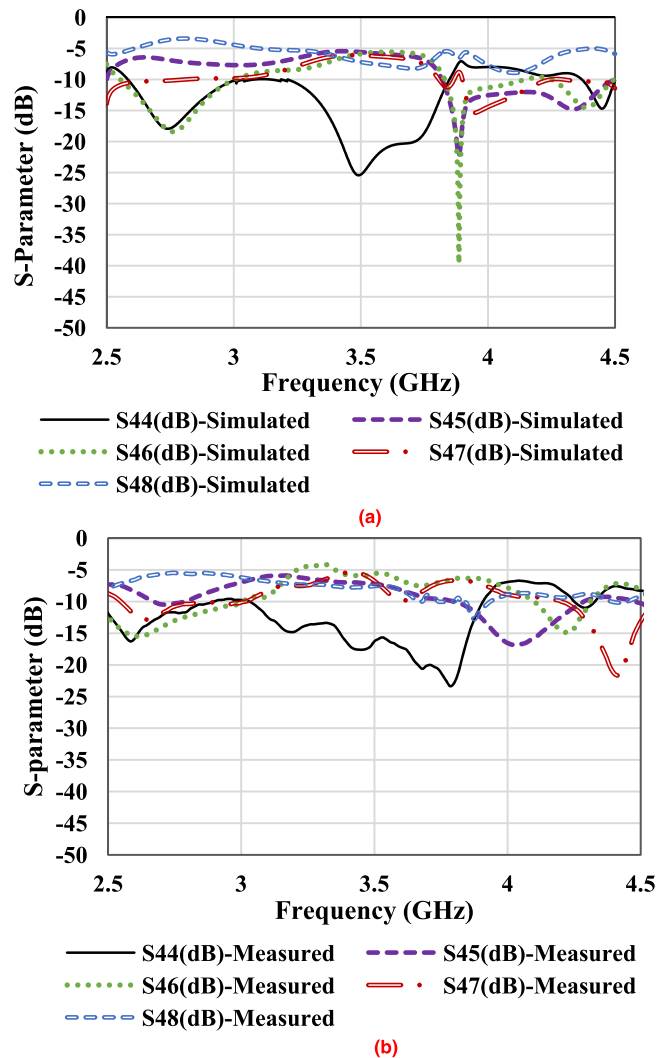


FIGURE 13. Shows the (a) Simulated and (b) Measured results of the insertion and return loss for Port 4 excitation.

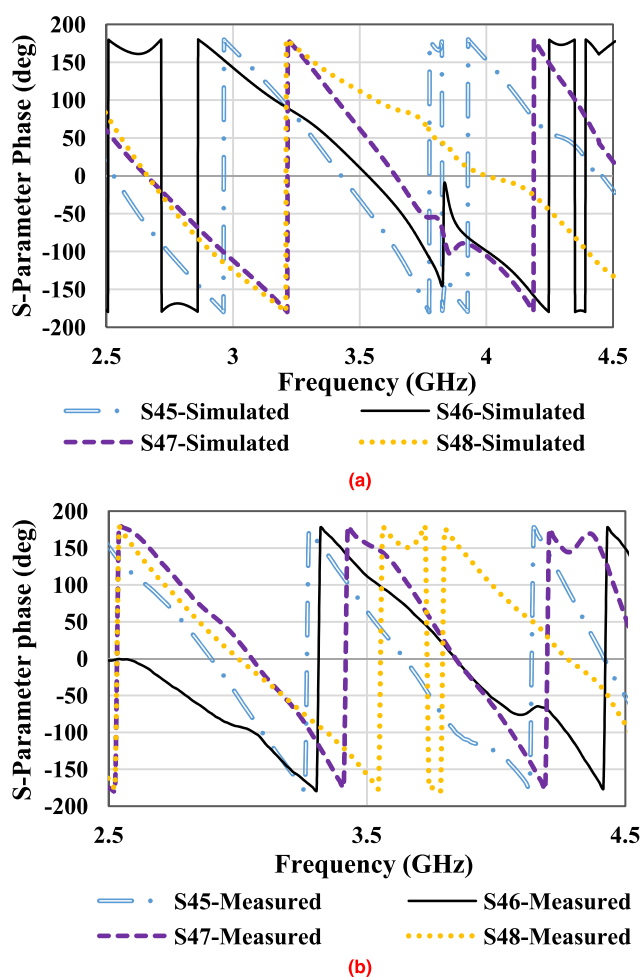
consideration in summarized Table 1. In simulation results; it is found that the phase difference between the output ports is 42, 51°, 50° respectively at the centre frequency. These values differ from the desired value by 2.3, 6 and 5 degrees introducing an average error of 4.4 degrees. In the measured results, the phase difference between output ports is 47.4°, 44°, 46.8° respectively, at the centre frequency of 3.5GHz. So, the errors were 2.4, 1 and 1.8 degrees introducing an average error of 1.7 degrees and very good agreement between simulated and measured phase difference in value but the quadrant is different as summarized in Table 3.

Table 3 and Table 4 summarize the simulated and measured phase difference between the ports and the S-parameter of the proposed BM at the operating frequency of 3.5GHz, respectively. It can be seen from the Table 3 and Table 4 that at the 3.5GHz operating frequency, the phase difference between all the ports has a very good agreement with the desired value and the magnitude tolerance of  $\pm 2$  dB.



**TABLE 4.** Simulated and measured s-parameter of the proposed butler matrix at 3.5 GHz.

Ports	Insertion Loss (dB) Port 5		Insertion Loss (dB) Port 6		Insertion Loss (dB) Port 7		Insertion Loss (dB) Port 8		Desired Value (dB)
	Simulated	Measured	Simulated	Measured	Simulated	Measured	Simulated	Measured	
Port 1	-7.2	-7.5	-6.1	-7.5	-5.85	-7.2	-5.6	-5.8	$-7 \pm 2$
Port 2	-5.8	-7.1	-5	-6.2	-5.6	-8	-6.1	-6.8	$-7 \pm 2$
Port 3	-6.1	-8.3	-5.6	-8.4	-5	-7.5	-5.85	-6.7	$-7 \pm 2$
Port 4	-5.6	-7.2	5.9	-5.6	-6.1	-6.7	-7.2	-7.5	$-7 \pm 2$



**FIGURE 14.** Shows the (a) Simulated and (b) Measured results of the phase shift of adjacent output ports, when Port 4 excited.

**III. COMPARITIVE STUDY**

A comparison of the proposed planar and compact BM with the existing works is shown in Table 5. The comparison table shows that our proposed BM has good improvement both in terms of bandwidth and size reduction which are major design consideration for the future 5G system deployments. The return loss, insertion loss and the phase difference for the proposed BM achieved design specification summarized in Table 1 and 2. The references cited in Table 5 were selected

**TABLE 5.** Performance of the proposed butler matrix at 3.5 GHz with existing planar technology based BM.

Reference / Year	Frequency $f_0$ (GHz)	Bandwidth (MHz)	Size (cm)
[24] / 2011	1.8	200	$11.9 \times 10.9$
[18] / 2014	0.86	150	$10.9 \times 8.93$
[28] / 2016	2.4	300	$15.4 \times 11.7$
[16] / 2017	2.5	200	$11.5 \times 6.4$
[26] / 2017	2.5	400	$12 \times 15$
[25] / 2018	6	2300	$10 \times 11$
[27] / 2019	2.44	200	$9.6 \times 9.6$
[12] / 2019	2.4	900	$17 \times 17$
[This Work] / 2019	3.5	550	$7 \times 7.37$

based on the planar single-layer BM and their respective operating frequency. From Table 5, it is very important to note that in most of the presented designs, the researchers are either trying to improve the bandwidth or reducing the size. Although, the work presented in this paper shows the improvement in the bandwidth and the size reduction at the same time.

**IV. CONCLUSION**

In this paper, a novel  $4 \times 4$  butler matrix has been proposed, designed and fabricated for the 5G antenna array system. The proposed butler matrix is designed using the composite right/left handed transmission-line metamaterial structure, which is based on open-circuits coupled-lines and interdigital capacitors. The proposed butler matrix has the advantage of compact size ( $70\text{mm} \times 73.7\text{mm}$ ) and improved bandwidth (550MHz) as compared to the previous designs discussed in the literature and summarized in the previous section. Moreover, the circuit size has been reduced by 75%, and the overall bandwidth has been improved by 8.2 times more than the conventional butler matrix [29].

The proposed butler matrix is fabricated using two different techniques. The first one was the hybrid  $4 \times 4$  butler matrix design by using the combination of BLCs, crossovers, SMA male-female RF connectors and the coaxial cables. The second  $4 \times 4$  butler matrix design was fabricated on a

single FR4 substrate in which the BLCs, crossovers and phase shifters are sharing the common ground plane which leads to a compact structure with improved bandwidth. The simulated and measured return loss, insertion loss and phase difference between the ports shows good correlation with the design specification summarized in Table 2. The measured phase difference between the output ports are  $-45^\circ$ ,  $135^\circ$ ,  $-135^\circ$  and  $+45^\circ$ , respectively achieved with a maximum average phase tolerance of  $5^\circ$  at 3.5GHz based on the excitation of the input port, so it is possible to switch in a different direction at the same time. Based on the results, the proposed butler matrix design is considered to be a suitable candidate for the beamforming network in 5G antenna array systems.

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