

Received March 25, 2019, accepted April 22, 2019, date of publication April 29, 2019, date of current version May 15, 2019. *Digital Object Identifier 10.1109/ACCESS.2019.2913911*

Passive Synthesis of Immittance for Fractional-Order Three-Element-Kind Circuit

GUISHU LIANG^{®[1](https://orcid.org/0000-0003-4239-715X)} AND JIAWEI HA[O](https://orcid.org/0000-0003-0964-0920)^{®1,2}

¹Department of Electric Engineering, North China Electric Power University, Baoding 071003, China ²State Grid Tianjin Electric Power Company, Tianjin 300010, China Corresponding author: Jiawei Hao (ncepuhjw@163.com)

This work was supported in part by the Natural Science Foundation of Beijing Municipality under Grant 3192039, and in part by the Natural Science Foundation of Hebei Province under Grant E2018502121.

ABSTRACT As an interdisciplinary research area, fractional circuits and systems have attracted extensive attention of scholars and researchers for their superior performance and potential applications. The passive realization of the fractional-order (FO) immittance function plays an important role in fractional circuits' theory, which is useful in fractional circuit design and modeling. This paper deals with the passive synthesis of FO three-element-kind circuits. First, the method is given for judging the immittance functions of FO three-element-kind circuits. Then, by making use of impedance scaling and variable substitution, the synthesis method of such an FO immittance function is proposed, which is based on the bivariate reactance synthesis method. Finally, a procedure is proposed to realize such immittance functions using the three-element-kind circuit. And several examples are given to illustrate the proposed method.

INDEX TERMS Fractional circuits, passive synthesis, fractional-order impedance, impedance scaling, multivariable synthesis.

I. INTRODUCTION

In recent years, Fractional Calculus has been widely used in modeling the dynamics of many natural phenomena, which is because of its higher capability of providing accurate description than integer dynamical systems. Applications of Fractional Calculus have been reported in many areas [1]–[7], such as Physics, Biology, Biomedical Engineering, Financial Market, Signal Processing and so on. And in electrical engineering, the application of Fractional Calculus has also growing, such as modeling of electrical equipment [8]–[12] and wireless power transmission system design [13]. Furthermore, the fractional electrical circuits have been studied from diverse aspects, for instance: synthesis of filters [14]–[16], realization of oscillators [17], stability analysis [18]–[20], frequency domain analysis [21]–[27], time-frequency domain analysis [28]–[32], energy efficiency [33], sensitivity analysis [34], fractional-order (FO) reconfigurable filters [35], FO resonator [36], [37], FO general impedance converter (GIC) [38].

With the development of numerical calculations about fractional calculus equations [39], [40], it is feasible to simulate and analyze the FO systems using fractional models.

Meanwhile, due to the progresses in fabrication of fractional capacitors and inductors [41]–[43], the FO systems can also be realized directly with real fractional elements [44]. At present, most of the developed FO impedances are fractional capacitors [41], while the floating or grounded fractional inductors can be realized by active devices like GIC [38], operational transconductance amplifier [42], and current carrying conveyor [43]. Obviously, both the fractional circuits modeling and design motivate the need to study the synthesis methods for fractional circuits. At present, several attempts into this goal have been done. In [45], [46], the necessary and sufficient conditions for the realization of FO impedance function with a passive RLC biport terminated one fractional element is found. By transforming the immittance function to multivariable positive-real function, a synthesis method for a class of FO immittance function is proposed in [47], where the *n*-variable positive-real function $Z(p_1, p_2, \dots, p_n)$ contains a high-order variable and *n*-1 one-order variables p_2, p_3, \cdots, p_n , and for $i = 2, 3, \cdots, n$, $\partial Z(p_1, p_2, \dots, p_n) / \partial p_i = \pm$ (a perfect square). The positive-real property and passivity of the FO circuits were discussed in [48]–[50].

In multivariable synthesis theory, Ozaki and Kasami [51] firstly introduced the concept of positive real functions of several variables in 1960, and then multivariable synthesis

The associate editor coordinating the review of this manuscript and approving it for publication was Sara Dadras.

theory has made a great progress [52]–[54]. In the case of bivariate, [55] can realize lossless reciprocal circuit, and arbitrarily reactance matrix is realizable as shown in [56]. Furthermore, there are some *k*-variable theoretically reactance matrix synthesis methods [57], [58].

Due to the fact that the synthesis for fractional circuits is much more complex than that for classical circuits, the proposed synthesis methods for fractional circuits are only applicable to several kinds of specific immittance function (matrix) forms. [58] shows the synthesis of two-elementkind network, and this paper is a continue work of [58]. This paper mainly deals with the passive synthesis of FO immittance using three kinds of elements, where the order of fractional elements included can be any real number between 0 and 1, and the number of fractional elements is arbitrary.

The rest of the paper is organized as follows. Some preliminaries are presented in Section 2. In Section 3, the method for judging the immittance functions of FO three-element-kind circuits is obtained. And Section 4 elaborates the synthesis method and specific synthesis procedure for the fractional circuits composed of three kinds of element, and several examples are presented. The conclusions are drawn in Section 5.

II. PRELIMINARIES

This section is devoted to presenting some preliminaries.

Fractional Calculus is an extension of integral order calculus. At present, there are three widely used definitions of fractional differential [3], namely Riemann-Liouville (RL), Grünwald-Letnikov (GL), and Caputo definitions. More details can be found in [3].

The Laplace transform of fractional differential [3] at zero initial condition is given by

$$
L\left\{ {}_{0}D_{t}^{\alpha}f\left(t\right)\right\} =s^{\alpha}F\left(s\right) \tag{1}
$$

where $_0D_t^{\alpha} = \frac{d^{\alpha}}{dt^{\alpha}}$ $\frac{d^{n}}{dt^{a}}$ denotes the fractional derivative operator of order α.

In this paper, we assume that $\alpha \in [0, 1]$.

The common fractional elements are fractional capacitor and inductor [2].

The characteristic equation of a fractional capacitor is

$$
i_{\alpha}(t) = C_{\alpha} \frac{d^{\alpha} u_{\alpha}(t)}{dt^{\alpha}}
$$
 (2)

where $\alpha \in (0, 1]$ is the order of the passive fractional capacitor, C_{α} with unit Fs^{$\alpha-1$} denotes the pseudo-capacitance of the fractional capacitor, u_{α} (*t*) and i_{α} (*t*) are respectively the voltage and current of the fractional capacitor. The impedance of the fractional capacitor in the Laplace domain equals $Z(s) =$ $1/C_{\alpha}s^{\alpha}$. The circuit symbol of the fractional capacitor [23] is depicted in Fig. 1(a).

The characteristic equation of a fractional inductor is

$$
u_{\beta}(t) = L_{\beta} \frac{d^{\beta} i_{\beta}(t)}{dt^{\beta}}
$$
 (3)

where $\beta \in (0, 1]$ is the order of the passive fractional inductor, L_β with unit Hs^{$\beta-1$} denotes the pseudo-inductance

FIGURE 1. Symbols of fractional elements. (a) Fractional capacitor. (b) Fractional inductor.

of the fractional inductor, $u_{\beta}(t)$ and $i_{\beta}(t)$ are respectively the voltage and current of the fractional inductor. The impedance of the fractional inductor in the Laplace domain equals $Z(s) = L_{\beta} s^{\beta}$. The circuit symbol of fractional inductor [23] is depicted in Fig. 1(b).

Fractional capacitors and fractional inductors are collectively referred to as fractional reactance elements (fractances for short). Distinct with classical reactance elements, fractances are lossy when the orders of the fractional elements are in (0, 1).

Then, the definition of multivariable positive real function and multivariable reactance function are described below.

Definition 1 [51]: $Z(p_1, p_2, \dots, p_n)$ is a *n*-variable positive real function, if

- 1) *Z* (p_1, p_2, \dots, p_n) is a rational real function of, and
- 2) $ReZ \ge 0$ in the domain $Rep_i > 0$ ($i = 1, 2, \dots, n$).

Definition 2 [51]: $Z(p_1, p_2, \dots, p_n)$ is a *n*-variable reactance function, if

- 1) *Z* (p_1, p_2, \cdots, p_n) a *n*-variable positive real function, and
- 2) $Z(p_1, p_2, \cdots, p_n) = -Z(-p_1, -p_2, \cdots, -p_n).$

In this paper, the order of fractional element referred to as the element-order. The network that the element-orders are all the same is called the fractional commensurate network (commensurate network for short). A network only composed of fractances is called a FO reactance network. Differently with the reactance network, the FO reactance network is lossy when the orders of the fractional elements are in (0, 1).

III. JUDGEMENT METHOD OF IMMITTANCE FOR FO THREE-ELEMENT-KIND CIRCUITS

This section discusses the method for judging the immittance functions of FO three-element-kind circuits. The method is obtained mainly in view of scaling impedance and variable substitution.

Fractional three-element-kind networks have up to three different element-orders, and thus can be classified into three kinds of networks: three element-orders networks, for example $L_{\beta_1}L_{\beta_2}C_\alpha$, $L_{\beta}C_{\alpha_1}C_{\alpha_2}$, $L_{\beta_1}L_{\beta_2}L_{\beta_3}$, and $C_{\alpha_1}C_{\alpha_2}C_{\alpha_3}$ networks, two element-orders networks such as $RL_{\beta}C_{\alpha}$, *RL*_{β1}*L*_{β2}, *RC*_{α1}*C*_{α2}, *L*_β*L*_α*C*_α and *L*_β*C*_β*C*_α networks, and commensurate networks like $RL_{\beta}C_{\beta}$ networks.

Theorem: By specific impedance scaling and variable substitution, the impedance function of FO *n-*element-kind circuit *Z* (*s*) can be converted into an reactance function of up to (*n*-1) variables.

Proof: For a passive circuit with *n* kinds of elements, without loss of generality, we set the elements' value to

be 1, then the impedances of each kind of element are *s*^{γ1}, *s*^{γ2}, ···, *s*^{γ*n*} respectively, where $-1 \le \gamma_1 < \cdots < \gamma_i <$ $\cdots < \gamma_1 < \cdots < \gamma_n \leq 1.$

With the scaling parameter s^{γ} , where

$$
\gamma = -(\gamma_i + \gamma_j)/2, \quad (i, j = 1, 2, \cdots, n; i \neq j)
$$
 (4)

the elements whose impedance are s^{γ_i} and s^{γ_j} can be transformed into elements of order $(\gamma_j - \gamma_i)/2$, whose impedance are $s^{(\gamma_1 - \gamma_1)/2}$ and $s^{(\gamma_1 - \gamma_1)/2}$. For these two kinds of elements, we can substitute them with variable $p_i = s^{(\gamma_j - \gamma_i)/2}$. The other kinds of elements can be represented by up to *n*-2 variables. Thus, after scaling the impedance levels of the elements and variable substitutions, the impedance function of network is transformed into a reactance function up to (*n*-1) variables.

Remark: The impedance function of FO three-elementkind circuit can be converted into a bivariate reactance function by suitable impedance scaling and variable substitution. And there are three scaling parameters available.

Then we show the judgment method based on the remark. The method contains the following three steps,

- *Step* 1 Figure out scaling parameter based on the elementorders.
- *Step* 2 Determine if the impedance function can be transformed to a bivariate reactance function.
- *Step* 3 Determine whether it is a fractional three-elementkind network.

For a impedance $Z(s^{\tau_1}, s^{\tau_2}, s^{\tau_3})$ of the three element-orders network, set $\gamma_i = \pm \tau_i$, $i = 1, 2, 3$, choosing the scaling parameter s^{γ} as $s^{-(\gamma_1+\gamma_2)/2}$, $s^{-(\gamma_1+\gamma_3)/2}$, and $s^{-(\gamma_2+\gamma_3)/2}$. Then using these scaling parameters and variable substitution, transform $s^{\gamma}Z$ ($s^{\tau_1}, s^{\tau_2}, s^{\tau_3}$) to a bivariate reactance function $Z(p_1, p_2)$. If there are three types of scaling parameter and variable substitution that can lead a bivariate reactance function, it should be a fractional threeelement-kind network. Moreover, if the three scaling parameters are γ_{01} , γ_{02} , γ_{03} , then γ_1 , γ_2 , γ_3 can be represented as $\gamma_1 = -\gamma_{01} - \gamma_{02} + \gamma_{03}$

$$
\gamma_2 = -\gamma_{01} + \gamma_{02} - \gamma_{03}, \quad \gamma_3 = \gamma_{01} - \gamma_{02} - \gamma_{03}.
$$

For two element-orders networks, let $\tau_3 = 0$, above method also works. As for commensurate networks, it can be determined just using variable substitution. After the variable substitution $p = s^{\beta}$, if the impedance of a $RL_{\beta}C_{\beta}$ network is positive real but is not a RL,RC, or LC function in p-plane, the network should be a fractional three-elementkind network.

It is worth mentioning that through the synthesis method in the next section, as long as we get a bivariate reactance function with suitable scaling impedance and variable substitution, the implementation of such function can be completed.

Example 1: Given the impedance function

$$
Z\left(s\right) = \frac{s^{\tau_1 + 2\tau_2 + \tau_3} + s^{\tau_2 + \tau_3} + 2s^{\tau_1 + \tau_2} + 1}{s^{2\tau_2 + \tau_3} + 2s^{\tau_2}}
$$
(5)

It can be observed that there are three types of scaling parameter and variable substitution to transform [\(5\)](#page-2-0) into a

bivariate reactance function if we set $\gamma_1 = \tau_1, \gamma_2 = -\tau_2$, $γ_3 = τ_3$.

1) According to the Theorem, we scaling the impedance levels of the elements with parameter $s^{\gamma} = s^{-(\gamma_3 + \gamma_2)/2}$. Then we have

$$
s^{\gamma} Z(s) = \frac{s^{\tau_1 + 2\tau_2 + \tau_3} + s^{\tau_2 + \tau_3} + 2s^{\tau_1 + \tau_2} + 1}{s^{\frac{3(\tau_2 + \tau_3)}{2}} + 2s^{\frac{\tau_2 + \tau_3}{2}}}
$$

Let $p_1 = s^{(\gamma_3 - \gamma_2)/2}$, $p_2 = s^{\gamma_1 - (\gamma_3 + \gamma_2)/2}$, we get a bivariate function from $s^{\gamma}Z(s)$,

$$
Z(p_1, p_2) = \frac{p_1^3 p_2 + p_1^2 + 2p_1 p_2 + 1}{p_1^3 + 2p_1}
$$
 (6)

Obviously, $Z(p_1, p_2) = s^{\gamma} Z(s)$, and it is a bivariate reactance function of p_1 and p_2 .

The other two types of scaling parameter and variable substitution are as follows,

2)
$$
s^{\gamma} = s^{-(\gamma_1 + \gamma_2)/2}
$$
, $p_1 = s^{(\gamma_2 - \gamma_1)/2}$, $p_2 = s^{\gamma_3 - (\gamma_1 + \gamma_2)/2}$
3) $s^{\gamma} = s^{-(\gamma_1 + \gamma_3)/2}$, $p_1 = s^{(\gamma_3 - \gamma_1)/2}$, $p_2 = s^{\gamma_2 - (\gamma_1 + \gamma_3)/2}$
and hence it is a fractional three-element-kind network.

IV. PASSIVE SYNTHESIS OF FO THREE-ELEMENT-KIND CIRCUITS

In this section, we shall first show the synthesis method for FO three*-*element-kind circuit. Then, the specific synthesis procedure for the fractional circuits composed of three kinds of elements is given.

A. SYNTHESIS METHOD

After getting the bivariate reactance function with suitable transformation, we synthesis the bivariate reactance function as a bivariate reactance network. Then, by inversely transformation, the passive fractional network is obtained.

The Lemma 1 and Lemma 2 show that a bivariate reactance function $Z(p_1, p_2)$ can be realized as a bivariate reactance network.

Lemma 1 [55]: A bivariate reactance function $Z(p_1, p_2)$ can be decomposed as

$$
Z(p_1, p_2) = Z_1(p_1) + Z_2(p_2) + Z_0(p_1, p_2)
$$
 (7)

where Z_1 and Z_2 are reactance functions in p_1 and p_2 , respectively, and Z_0 is a bivariate reactance function with no *p*-independent or *s*-independent poles.

Lemma 2 [56]: Every bivariate reactance function Z_0 (p_1 , p_2) can be realized as the impedance seen at the first one-ports of a lossless $(1+k)$ -port consisting of reactance in the p_1 -plane terminated at its last k ports with unit inductors in p_2 -plane. Furthermore, such a realization uses the minimum possible number of reactance.

The synthesis procedure of *Z* (*p*1, *p*2) is shown as follows,

- 1) Decompose $Z(p_1, p_2)$ as (x). And realize the univariate reactance function Z_1 and Z_2 using classical synthesis method 59].
- 2) Synthesize $Z_0(p_1, p_2)$.

Expand Z_0 (p_1 , p_2) in a Laurent series:

$$
Z_0(p_1, p_2) = A_{-1}(p_1) + \sum_{l=0}^{\infty} A_l(p_1) p_2^{-(k+1)},
$$

Find $g(p_1, p_2)$, the least common denominator of elements in Z_0 (p_1 , p_2) and express it in the form

$$
g(p_1, p_2) = \sum_{k=0}^{r} a_k(p_1) p_2^{r-k}.
$$

Form the $(r \times r)$ matrix N_{r-1} (p_1), defined by

$$
N_{r-1} = \begin{bmatrix} A_0 & \cdots & A_{r-1} \\ -A_1 & \cdots & -A_r \\ \vdots & \ddots & \vdots \\ (-1)^{r-1} A_{r-1} & \cdots & (-1)^{r-1} A_{2r-2} \end{bmatrix}.
$$

Factor $\hat{N}_{r-1}(p_1) = a_0^{2r}(p_1)N_{r-1}(p_1)$, a polynomial matrix, as

$$
\hat{N}_{r-1}(p_1) = M(p_1)M^T(-p_1)
$$

Unless simultaneously, $a_0(p_1) = -a_0(-p_1)$ and *r* is odd, in which case factor $-\overline{N}_{r-1}(P_1)$. The factorization must be such that *M* is a $(k \times r)$ polynomial matrix with $k = \text{rank}$ of $N_{r-1}(p_1)$ and M , the left inverse of M analytic in the open right plane. The existence of such a factorization is guaranteed by [56].

Partition $M(p_1)$ into $(1 \times k)$ blocks,

$$
\boldsymbol{M}(p_1) = \big[\boldsymbol{M}_0(p_1) \cdots \boldsymbol{M}_{r-1}(p_1)\big]
$$

Form the $(r \times r)$ matrix $\Omega(p_1)$, defined by

$$
\mathbf{\Omega}(p_1) = \begin{bmatrix} 0 & 1 & 0 & \cdots & 0 \\ 0 & 0 & 1 & \cdots & 0 \\ \vdots & \vdots & \vdots & \vdots & \vdots \\ 0 & 0 & 0 & \cdots & 1 \\ -\frac{a_r}{a_0} & -\frac{a_{r-1}}{a_0} & -\frac{a_{r-2}}{a_0} & \cdots & -\frac{a_1}{a_0} \end{bmatrix}.
$$

It can be verified that with

$$
z_{11} = A_{-1} (p_1) = Z_0 (p_1, \infty),
$$

\n
$$
z_{21} = \frac{M_0 (p_1)}{a_0' (p_1)}, z_{12} (p_1) = -z_{21}^T (-p_1),
$$

and

$$
z_{22} = \tilde{\boldsymbol{M}}^T (p_1) \boldsymbol{\Omega} (-p_1) \boldsymbol{M}^T (-p_1) ,
$$

where $\tilde{\boldsymbol{M}}^T(p_1)$ denotes the left inverse of \boldsymbol{M}^T , Z_0 (p_1, p_2) can be decomposed as follows:

$$
Z_0 (p_1, p_2) = z_{11} (p_1) - z_{12} (p_1) [z_{22} (p_1) + p_2 E_k]^{-1} z_{21} (p_1)
$$

Then, Z_0 (p_1, p_2) is realizable as in Fig. 2.

(3) Connect all three networks in series as shown in Fig. 3. The given $Z(p_1, p_2)$ is thus realized as a passive bivariate network.

FIGURE 2. Realization of $Z_0(p_1, p_2)$.

FIGURE 3. Realization of $Z(p_1, p_2)$.

Combining the Remark, Lemma 1, and Lemma 2, the immitance function of a fractional three-element-kind network can always be realized as a passive bivariate reactance network. Then, by inversely transformation, a fractional three-element-kind network can be obtained. And it is passive.

B. SYNTHESIS PROCEDURE

The specific implementation steps of the FO three-elementkind circuit are given below,

- 1) According to the element-order of *Z* (*s*), find possible scaling parameters and variable substitutions.
- 2) Transform *Z* (*s*) into *Z* (p_1 , p_2) by suitable impedance scaling and variable substitution, and $Z(p_1, p_2)$ = *s* ^γ *Z* (*s*).
- 3) Realize $Z(p_1, p_2)$ as a passive bivariate reactance network.
- 4) Obtain the passive FO three-element-kind circuit corresponding to *Z* (*s*) by inversely transformation for the bivariate reactance network.

And the process of synthesizing FO three-element-kind network is shown in Fig. 4.

Continue to Example 1, we finish the realization of [\(5\)](#page-2-0). *Example 2:* Consider the bivariate reactance function,

$$
Z(p_1, p_2) = \frac{p_1^3 p_2 + p_1^2 + 2p_1 p_2 + 1}{p_1^3 + 2p_1}
$$
 (8)

Realize [\(8\)](#page-3-0) as a passive bivariate reactance network, as shown in Fig. 5.

By using $p_1 = s^{(\tau_3 + \tau_2)/2}$, $p_2 = s^{\tau_1 - (\tau_3 - \tau_2)/2}$, we get a network as Fig. 6. Then by means of $s^{-\gamma} = s^{-(\tau_2 - \tau_3)/2}$,

FIGURE 4. Process of the synthesis method.

FIGURE 5. Realization of [\(8\)](#page-3-0) in p-plane.

FIGURE 6. Realization of [\(8\)](#page-3-0) in s-plane.

FIGURE 7. Realization of [\(5\)](#page-2-0) in s-plane.

the passive FO three-element-kind circuit corresponding to *Z* (*s*) is obtained. The result is Fig. 7.

In [60], the approximation result of some supercapacitors requires a fractional-order three-element-kind network to describe [60]–[62]. The synthesis for such supercapacitors are shown in Example 3.

Example 3: Given a two element-order impedance function

$$
Z\left(s\right) = \frac{3s^{\alpha+0.5} + s^{\alpha} + 4s^{0.5} + 1}{s^{\alpha+0.5} + s^{0.5}}
$$
\n⁽⁹⁾

For the sake of simplicity, set $\alpha = 0.9$. Then, choosing $s^{\gamma} = s^{0.45}$ and $p_1 = s^{0.45}$, $p_2 = s^{0.05}$, we have a bivariate reactance function,

$$
Z(p_1, p_2) = \frac{3p_1^3p_2 + 4p_1p_2 + p_1^2 + 1}{p_1^2p_2 + p_2}
$$
 (10)

FIGURE 8. Realization of [\(10\)](#page-4-0) in p-plane.

FIGURE 9. Realization of [\(10\)](#page-4-0) in s-plane.

FIGURE 10. Realization of [\(9\)](#page-4-1) in s-plane.

FIGURE 11. Voltage excitation. (a) Sinusoidal steady-state voltage. (b) Transient voltage.

The realization of [\(10\)](#page-4-0) is shown in Fig. 8. By using $p_1 =$ $s^{0.45}$, $p_2 = s^{0.05}$, we get the network drawn in Fig. 9. Then, with the scaling parameter $s^{-\gamma} = s^{-0.45}$, the passive network of [\(9\)](#page-4-1) is obtained, which is shown in Fig. 10.

In order to valid the results of the paper, we set $\gamma_1 = 0.2$, $\gamma_2 = 0.3, \gamma_3 = 0.7$ in Example 2, and two voltage excitations that shown in Fig. 11 are applied to fractional order impedance function and fractional order network respectively, then gets two voltages by frequency domain analysis.

The simulation of circuits in Example 2 and 3 are shown in Fig. 12 and 13 respectively. The ''Function Calculation'' means mathematical calculation of fractional order impedance function *Z* (*s*) based on *I* (*s*) = *U* (*s*) /*Z* (*s*); "Circuit Simulation'' means fractional order network port-voltage obtained by modified nodal approach. In Fig. 12 and 13, ''Function Calculation'' and ''Circuit Simulation'' are consistent, the results of two synthesis networks are correct.

Example 4: Consider the impedance function

$$
Z\left(s\right) = \frac{s^{2\tau_1 + 2\tau_2} + s^{\tau_1 + 2\tau_2} + s^{\tau_1} + 1}{s^{\tau_1 + 2\tau_2} + s^{\tau_1 + \tau_2} + s^{\tau_2} + 1} \tag{11}
$$

By choosing $s^{\gamma} = s^{-\tau_1/2}$ and $p_1 = s^{\tau_1/2}, p_2 = s^{\tau_2 + (\tau_1/2)},$ we can get the bivariate reactance function,

$$
Z(p_1, p_2) = s^{\gamma} Z(s) = \frac{(p_1^2 + 1)(p_2^2 + 1)}{(p_1 + p_2)(p_1 p_2 + 1)}
$$
(12)

FIGURE 12. Port-current response of Example 2. (a) Sinusoidal steady-state voltage. (b) Transient voltage.

FIGURE 13. Port-current response of Example 3. (a) Sinusoidal steady-state voltage. (b) Transient voltage.

Then we synthesis *Z* (p_1, p_2) . The impedance matrix *Z* (p_1) is obtained using the method in section 4.

The $\mathbf{Z}(p_1)$ can be verified to be lossless. According to Lemma 2, $Z(p_1, p_2)$ in [\(12\)](#page-4-2) can be realized as a impedance seen at the first one port and terminated at its last two ports

$$
\mathbf{Z}(p_1) = \begin{bmatrix} \frac{p_1^2 + 1}{2p_1} & \frac{p_1^2 + 1}{\sqrt{2}p_1} & -\frac{p_1^2 + 1}{\sqrt{2}p_1} \\ \frac{p_1^2 + 1}{\sqrt{2}p_1} & \frac{p_1^2 + 1}{2p_1} & -\frac{(p_1 + 1)^2}{2p_1} \\ -\frac{p_1^2 + 1}{\sqrt{2}p_1} & -\frac{(p_1 - 1)^2}{2p_1} & \frac{p_1^2 + 1}{2p_1} \end{bmatrix}
$$

by unit p2-plane inductors.

After that, by making use of $p_1 = s^{\tau_1/2}, p_2 = s^{\tau_2 + (\tau_1/2)},$ the passive network of [\(12\)](#page-4-2) is obtained. Finally, with the scaling parameter $s^{-\gamma}$ == $s^{\tau_1/2}$, we get the passive FO circuit of [\(11\)](#page-4-3).

As for the commensurate networks composed of three kinds of elements, their immittance function can be realized with classical synthesis method [59].

V. CONCLUSIONS

Passive synthesis of the FO three-element-kind circuits is discussed in this paper. The FO three-element-kind circuits are more diverse than integer-order three-element-kind circuits. It can be classified into 3 kinds of networks: three element-orders networks, for example $L_{\beta 1}L_{\beta 2}C_{\alpha}$, $L_{\beta}C_{\alpha 1}C_{\alpha 2}$, $L_{\beta1}L_{\beta2}L_{\beta3}$, and $C_{\alpha1}C_{\alpha2}C_{\alpha3}$ networks, two element-orders networks such as $RL_{\beta}C_{\alpha}$, $RL_{\beta1}L_{\beta2}$, $RC_{\alpha1}C_{\alpha2}$, $L_{\beta}L_{\alpha}C_{\alpha}$ and $L_{\beta}C_{\beta}C_{\alpha}$ networks, and commensurate network like $RL_{\beta}C_{\beta}$ network.

A method for judging the immittance functions of FO three-element-kind circuits is given. Then, a passive synthesis method is proposed for fractional circuits contain three kinds of elements. Also, a specific synthesis procedure is given.

The investigation in this paper further enriched the synthesis methods of the fractional electrical networks.

ACKNOWLEDGMENT

Many thanks to my tutor and my family.

REFERENCES

- [1] C.-C. Hua, D. Liu, and X.-P. Guan, "Necessary and sufficient stability criteria for a class of fractional-order delayed systems,'' *IEEE Trans. Circuits Syst., II, Exp. Briefs*, vol. 61, no. 1, pp. 59–63, Jan. 2014.
- [2] A. S. Elwakil, ''Fractional-order circuits and systems: An emerging interdisciplinary research area,'' *IEEE Circuits Syst. Mag.*, vol. 10, no. 4, pp. 40–50, Nov. 2010.
- [3] I. Petras, *Fractional-Order Nonlinear Systems: Modeling, Analysis and Simulation*. New York, NY, USA: Springer, 2011.
- [4] M. D. Ortigueira, ''An introduction to the fractional continuous-time linear systems: The 21st century systems,'' *IEEE Circuits Syst. Mag.*, vol. 8, no. 3, pp. 19–26, 3rd Quart., 2008.
- [5] A. Elwakil, B. Maundy, L. Fortuna, and G. Chen, ''Guest editorial fractional-order circuits and systems,'' *IEEE Trans. Emerg. Sel. Topics Circuits Syst.*, vol. 3, no. 3, pp. 297–300, Sep. 2013.
- [6] J. T. Machado, ''And I say to myself: What a fractional world!'' *Fractional Calculus Appl. Anal.*, vol. 14, no. 4, pp. 635–654, 2011.
- [7] R. Magin, M. D. Ortigueira, I. Podlubny, and J. Trujillo, "On the fractional signals and systems,'' *Signal Process.*, vol. 91, no. 3, pp. 350–371, Mar. 2011.
- [8] T. J. Freeborn, B. Maundy, and A. S. Elwakil, ''Fractional-order models of supercapacitors, batteries and fuel cells: A survey,'' *Mater. Renew. Sustain. Energy*, vol. 4, no. 3, p. 9, Sep. 2015.
- [9] G. Liang, S. Gao, Y. Wang, Y. Zang, and X. Liu, ''Fractional transmission line model of oil-immersed transformer windings considering the frequency-dependent parameters,'' *IET Gener. Transmiss. Distrib.*, vol. 11, no. 5, pp. 1154–1161, Mar. 2017.
- [10] H. Y. Dong, L. Z. Xu, G. S. Liang, X. Liu, and X. Cheng, ''Wide-band modeling of grounding grid based on the fractional order differential theory,'' *Adv. Mater. Res.*, vols. 860–863, no. 4, pp. 2292–2295, Dec. 2013.
- [11] C. Wu, G. Si, Y. Zhang, and N. Yang, "The fractional-order state-space averaging modeling of the Buck–Boost DC/DC converter in discontinuous conduction mode and the performance analysis,'' *Nonlinear Dyn.*, vol. 79, no. 1, pp. 689–703, Jan. 2015.
- [12] N. Bertrand, J. Sabatier, O. Briat, and J.-M. Vinassa, ''Fractional non-linear modelling of ultracapacitors,'' *Commun. Nonlinear Sci. Numer. Simul.*, vol. 15, no. 5, pp. 1327–1337, May 2010.
- [13] X. Shu and Z. Bo, "A fractional-order method to reduce the resonant frequency of integer-order wireless power transmission system,'' *Trans. China Electrotech. Soc.*, vol. 32, no. 18, pp. 83–89, 2017.
- [14] A. G. Radwan, A. M. Soliman, and A. S. Elwakil, "First-order filters generalized to the fractional domain,'' *J. Circuits, Syst. Comput.*, vol. 17, no. 1, pp. 55–56, 2008.
- [15] M. C. Tripathy, D. Mondal, K. Biswas, and S. Sen, "Design and performance study of phase-locked loop using fractional-order loop filter,'' *Int. J. Circuit Theory Appl.*, vol. 43, no. 6, pp. 776–792, 2015.
- [16] G. Tsirimokou, C. Laoudias, and C. Psychalinos, "0.5-V fractionalorder companding filters,'' *Int. J. Circuit Theory Appl.*, vol. 43, no. 9, pp. 1105–1126, Sep. 2015.
- [17] A. G. Radwan, A. S. Elwakil, and A. M. Soliman, "Fractional-order sinusoidal oscillators: Design procedure and practical examples,'' *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 55, no. 7, pp. 2051–2063, Aug. 2008.
- [18] A. G. Radwan, A. M. Soliman, A. S. Elwakil, and A. Sedeek, ''On the stability of linear systems with fractional-order elements,'' *Chaos, Solitons Fractals*, vol. 40, no. 5, pp. 2317–2328, Jun. 2009.
- [19] M. S. Semary, A. G. Radwan, and H. N. Hassan, ''Fundamentals of fractional-order LTI circuits and systems: Number of poles, stability, time and frequency responses,'' *Int. J. Circuit Theory Appl.*, vol. 44, no. 12, pp. 2114–2133, Dec. 2016.
- [20] A. G. Radwan, "Stability analysis of the fractional-order $RL\beta C\alpha$ circuit," *J. Fract. Calc. Appl*, vol. 3, no. 1, pp. 1–5, Jul. 2012.
- [21] A. G. Radwan and K. N. Salama, "Passive and active elements using fractional LβC^α Circuit,'' *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 58, no. 10, pp. 2388–2397, Oct. 2011.
- [22] A. G. Radwan and K. N. Salama, ''Fractional-order *RC* and *RL* circuits,'' *Circuits, Syst., Signal Process.*, vol. 31, no. 6, pp. 1901–1915, 2012.
- [23] M. A. Long and G. S. Liang, "Characteristics and applications of fractional LC circuits,'' *Sci. Technol. Eng.*, 2017.
- [24] A. G. Radwan, "Resonance and quality factor of the $RL\alpha$ fractional circuit,'' *IEEE J. Emerg. Sel. Topics Circuits Syst.*, vol. 3, no. 3, pp. 377–385, Sep. 2013.
- [25] A. Jakubowsk, J. Walczak, and A. Jakubowska, "Resonance in series fractional order RLβCα circuit," Przegląd Elektrotechniczny, vol. 90, no. 4, 2014, pp. 210–213, 2014.
- [26] L. J. Diao, X. F. Zhang, and D. Y. Chen, ''Fractional-order multiple RLβCα circuit,'' *Acta Physica Sinica*, vol. 63, no. 3, 2014, Art. no. 38401.
- [27] J. Walczak and A. Jakubowska, ''Resonance in parallel fractional-order reactance circuit,'' in *Proc. 23rd Symp. Electromagn. Phenomena Nonlinear Circuits (EPNC)*, Pilsen, Czech Republic, 2014, pp. 1–2.
- [28] F. Gomez, J. Rosales, and M. Guia, ''RLC electrical circuit of non-integer order,'' *Central Eur. J. Phys.*, vol. 11, no. 10, pp. 1361–1365, Oct. 2013.
- [29] A. M. A. El-Sayed, H. M. Nour, W. E. Raslan, and E. S. El-Shazly, ''Fractional parallel RLC circuit,'' *Alex. J. Math.*, vol. 3, no. 1, pp. 11–23, Jun. 2012.
- [30] T. Kaczorek and K. Rogowski, *Fractional Linear Systems and Electrical Circuits*. Bialystok, Poland: Springer, 2015, pp. 49–80.
- [31] A. Jakubowska and J. Walczak, *Analysis of the Transient State in a Series Circuit of the Class RL*β*C*α. Basel, Switzerland: Birkhäuser Boston, 2016.
- [32] A. Jakubowska-Ciszek and J. Walczak, "Analysis of the transient state in a parallel circuit of the class RLβCα,'' *Appl. Math. Comput.*, vol. 319, pp. pp. 287–300, Apr. 2017.
- [33] T. T. Hartley, R. J. Veillette, J. L. Adams, and C. F. Lorenzo, "Energy storage and loss in fractional-order circuit elements,'' *IET Circuits, Devices Syst.*, vol. 9, no. 3, pp. 227–235, May 2015.
- [34] G. Liang and L. Ma, "Sensitivity analysis of networks with fractional elements,'' *Circuits, Syst., Signal Process.*, vol. 36, no. 10, pp. 4227–4241, Oct. 2017.
- [35] J. Jerabek, J. Dvorak, J. Polak, D. Kubanek, N. Herencsar, and J. Koton, ''Reconfigurable fractional-order filter with electronically controllable slope of attenuation, pole frequency and type of approximation,'' *J. Circuits Syst. Comput.*, vol. 26, no. 10, Art. no. 1750157, 2017.
- [36] A. Adhikary, S. Sen, and K. Biswas, "Practical realization of tunable fractional order parallel resonator and fractional order filters,'' *IEEE Trans. Circuits Systems. I. Reg. Papers*, vol. 63, no. 8, pp. 1142–1151, Aug. 2016.
- [37] G. Tsirimokou, C. Psychalinos, A. S. Elwakil, and K. N. Salama, "Electronically tunable fully integrated fractional-order resonator,'' *IEEE Trans. Circuits Syst., II, Exp. Briefs*, vol. 65, no. 2, pp. 166–170, Feb. 2018.
- [38] A. Adhikary, S. Choudhary, and S. Sen, "Optimal design for realizing a grounded fractional order inductor using GIC,'' *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 65, no. 8, pp. 2411–2421, Aug. 2018.
- [39] A. M. Elshurafa, M. M. Almadhoun, K. N. Salama, and H. N. Alshareef, ''Microscale electrostatic fractional capacitors using reduced graphene oxide percolated polymer composites,'' *Appl. Phys. Lett.*, vol. 102, no. 23, May 2013, Art. no. 232901.
- [40] C. Li, A. Chen, and J. Ye, ''Numerical approaches to fractional calculus and fractional ordinary differential equation,'' *J. Comput. Phys.*, vol. 230, no. 9, pp. 3352–3368, May 2011.
- [41] A. Adhikary, M. Khanra, J. Pal, and K. Biswas, "Realization of fractional order elements,'' *INAE Lett.*, vol. 2, no. 2, pp. 41–47, Jun. 2017.
- [42] I. Dimeas, G. Tsirimokou, C. Psychalinos, and A. S. Elwakil, ''Realization of fractional-order capacitor and inductor emulators using current feedback operational amplifiers,'' in *Proc. Int. Symp. Nonlinear Theory ITS Appl. (NOLTA)*, Dec. 2015, pp. 1–4.
- [43] A. Soltan, A. G. Radwan, and A. M. Soliman, "CCII based fractional filters of different orders,'' *J. Adv. Res.*, vol. 5, no. 2, pp. 157–164, Mar. 2014.
- [44] M. S. Tavazoei and M. Tavakoli-Kakhki, "Minimal realizations for some classes of fractional order transfer functions,'' *IEEE J. Emerg. Sel. Topics Circuits Syst.*, vol. 3, no. 3, pp. 313–321, Sep. 2013.
- [45] M. S. Sarafraz and M. S. Tavazoei, "Realizability of fractional-order impedances by passive electrical networks composed of a fractional capacitor and RLC components,'' *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 62, no. 12, pp. 2829–2835, Dec. 2015.
- [46] M. S. Sarafraz and M. S. Tavazoei, "Passive realization of fractional-order impedances by a fractional element and RLC components: Conditions and procedure,'' *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 64, no. 3, pp. 585–595, Mar. 2017.
- [47] G. Liang, Y. Jing, C. Liu, and L. Ma, "Passive synthesis of a class of fractional immittance function based on multivariable Theory,'' *J. Circuits Syst. Comput.*, vol. 27, no. 5, 2017, Art. no. 1850074.
- [48] G. Liang and C. Liu, "Positive-real property of passive fractional circuits in *W*-domain,'' *Int. J. Circuit Theory Appl.*, vol. 46, no. 4, pp. 893–910, Apr. 2017.
- [49] X. Liu, C. Ti, and G. Liang, "Wide-band modelling and transient analysis of the multi-conductor transmission lines system considering the frequency-dependent parameters based on the fractional calculus theory,'' *IET Gener. Transmiss. Distrib.*, vol. 10, no. 13, pp. 3374–3384, Oct. 2016.
- [50] G. Liang and L. Ma, "Multivariate theory-based passivity criteria for linear fractional networks,'' *Int. J. Circuit Theory Appl.*, vol. 46, no. 7, pp. 1358–1371, Jul. 2018.
- [51] H. Ozaki and T. Kasami, ''Positive real functions of several variables and their applications to variable networks,'' *IRE Trans. Circuit Theory*, vol. 7, no. 3, pp. 251–260, Sep. 1960.
- [52] G. Ansell, ''Networks of transmission lines and lumped reactance,'' *Netw. Transmiss. Lines Lumped Reactance*, vol. 11, no. 2, 1964, pp. 214–223.
- [53] D. C. Youla, ''N-port synthesis via reactance extraction-part I,'' in *Proc. IEEE Int. Conuention Rec.*, 1966, pp. 183–208.
- [54] A. Kummert, ''The synthesis of two-dimensional passive n-ports containing lumped elements,'' *Multidimensional Syst. Signal Process.*, vol. 1, no. 4, pp. 351–362, Dec. 1990.
- [55] T. Koga, ''Synthesis of finite passive n-ports with prescribed two-variable reactance matrices,'' *IEEE Trans. Circuit Theory*, vol. 13, no. 1, pp. 31–52, Mar. 1996.
- [56] T. N. Rao, ''Minimal synthesis of two-variable reactance matrices,'' *Bell Syst. Tech. J.*, vol. 48, no. 1, pp. 163–199, Jan. 1969.
- [57] T. Koga, ''Synthesis of finite passive n-ports with prescribed positive real matrices of several variables,'' *IEEE Trans. Circuit Theory*, vol. 15, no. 1, pp. 31–52, Mar. 1968.
- [58] G. Liang and J. Hao, ''Analysis and passive synthesis of immittance for fractional-order two-element-kind circuit,'' *Circuits, Syst. Signal Process.*, vol. 14, 2018.
- [59] G. Temes and J. Lapatra, *Introduction to Circuit Synthesis and Design*. New York, NY, USA: McGraw-Hill, 1977.
- [60] R. Martin, J. J. Quintana, A. Ramos, and I. de la Nuez, ''Modeling electrochemical double layer capacitor, from classical to fractional impedance,'' in *Proc. 14th IEEE Medit. Electrotech. Conf.*, May 2008, pp. 61–66.
- [61] C. Zou, L. Zhang, X. Hu, Z. Wang, T. Wik, and M. Pecht, ''A review of fractional-order techniques applied to lithium-ion batteries, lead-acid batteries, and supercapacitors,'' *J. Power Sources*, vol. 390, pp. 286–296, Jun. 2018.
- [62] X. Cheng, G. S. Liang, X. Liu, and L. Z. Xu, ''A new supercapacitors fractional order nonlinear model,'' *Adv. Mater. Res.*, vols. 860–863, pp. 2279-2282, Dec. 2013.

GUISHU LIANG received the B.S., M.S., and Ph.D. degrees in electric engineering from North China Electric Power University, China, in 1982, 1987, and 2008, respectively, where he is currently a Professor and a Doctoral Tutor and also serves as the Director of the Electrical Technician Department. He has been involved in research and teaching electrical theory and new technology. He has published nearly 60 papers over the past five years. More than 30 papers of them were collected by

SCI, EI, and ISTP. His research interests include fractional-order circuits and systems. He is a Fellow of the National Electrical Terminology Standardization Technical Committee.

JIAWEI HAO was born in Shijiazhuang, Hebei, China, in 1992. He received the B.S. degree in electrical engineering from the Changsha University of Science and Technology, in 2016. He is currently pursuing the M.S. degree in electrical engineering with North China Electric Power University. And his tutor is Prof. G. Liang. His current research area includes fractional-order circuits and systems.