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A New Optical Information Processing Device Design for Internet of Brain Things Application

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ABSTRACT With the development and application of Internet of things, new requirements for optical communication technology are put forward, and this text proposed new optical information processing device design logic-oriented-based for Internet of brain things application. Three micro rings were coupled with three different shapes of optical waveguide components, their wavelengths were varied with the change of the applied voltages (logical input) so as to control the propagation direction of the input optical signal and the final output forms of lighting or darkness representing different logic level values as the result of the logical operations. The working wavelength of the device was determined as $1.5146 \mu m$ through static simulations with MATLAB tools, and it was proved that the functions of flexible optical switch logic operations ''and/not'', ''or/nor'', and ''xor/xnor'' can be realized in the dynamic simulations, the results indicated that the device with low-power consumption, high-bandwidth, flexible structure, and multiple logical operation functions has been accomplished and can be used as components of VLSI designs in future. This paper also gives a useful reference to many Internet of brain things applied field.

INDEX TERMS Electro-optical logic gate, Internet of brain things, logical operation, micro ring resonator.

I. INTRODUCTION

Communication technology is one of the important technologies in the transport layer of the Internet of things. With the development of the new generation information technology, the Internet of brain things technology has been widely applied. At the same time, higher requirements for optical communication technology are put forward. For example, in recent years, under the requirement of miniaturization, integration and high-speed processing, the micro ring resonator (MRR) formed by a closed loop waveguide coupled with a straight one is widely used in the fields of weapon equipment, environmental monitoring, disease diagnosis and so on owing to its mature technology and high integration of optoelectronic [1]. Rabus has put forward a race tracker ring resonator transverse multi-coupled made of an integrated semiconductor optical amplifier (SOA) with the tuned filter function. Grover adopted a new method which a vertical coupled micro ring resonator and a frequency separation filter were realized in the GaInAs P-InP material system. Ibrahim has made an all-optical switch with single micro ring resonator used in the time-division multiplex data transmission channel, with approximately 8 decibels crosstalk in space. However, any transmission intensity in any number of channels can not be controlled, therefore, these structures were not flexible enough in terms of applications in optical switch or optical filter. In this paper, multiple micro ring resonators coupled with different shapes of optical waveguides and the modulated characteristic of the refractive index of electro-optic polymer materials applied, the optical logicoriented device with different logic operation functions was designed and realized. This device has the features of compact structures, low power consumptions and flexible control of the input and output.

The concept of logic-oriented was a principle of Boolean logic operation proposed by American scientist James Hardy and Israeli scientist Joseph Shamir in 2007. The switch states and directions of the light propagation in the optical network are controlled by logic operands, moreover, the results of the logic operation are output in the form of light at the specific ports [2], [3], accordingly, a method of optical processing logic-oriented-based is studied and a multifunctional electrooptical logic gate device has been designed and realized in this paper.

The paper structure is as follows. Section 2 first analyzes Design of logic-oriented-based device for Internet of brain things communication layer. Section 3 presents the Design

FIGURE 1. Various connection logic for the Internet of brain things.

FIGURE 2. Principle diagram of the optical switches logic-oriented.

method of switching logic devices based on the micro-ring resonator. Section 4 reports the Static simulation of the multifunctional electro-optical logic device function under IOT. Section 5 reports the logic function dynamic simulation of the optic-oriented device and its IOT application.

II. DESIGN OF LOGIC-ORIENTED-BASED DEVICE FOR INTERNET OF BRAIN THINGS COMMUNICATION LAYER A. INTERNET OF BRAIN THINGS COMMUNICATION

CONNECT AND OPTICAL LOGIC-ORIENTED

Communication connection technology is one of the core technologies in the application of Internet of brain things, Figure.1 shows various connection logic of the Internet of brain things. As we known, Design of logic-oriented-based device is a key technology of optical communication application. The logic operands of the logic-oriented device are represented the temperature field, electric field or light field. The optical input manipulated by different logic operands can be guided to different output as the results of different logic operations [4], [5]. Furthermore, some complex combinational logic operations can also be achieved by designing different switching circuits as indicated in Figure.2.

Due to its linear propagation in the logic-oriented device, the manufacturing processes of the devices are simple, and logical operation function is easy to achieve [6]. The micro ring polymer-based consists of three parts, as shown in Figure.2, The core of electro-optics waveguide with the refractive index of n_1 , the loss factor of α_1 and the thickness

of *b*1, is a kind of material polymer-based with the effect of electro-optics. The neighbor upper and lower buffer layers with the refractive index of n_2 , the loss factor of α_2 and thickness of b_2 , and the upper and lower electrodes with the refractive index of n_3 , the amplitude transfer factor of k_3 and thickness of b_3 are both non-polarized polymer-based material, therefore, n_2 and n_3 will not vary with the applied voltages.

The temperature of the electro-optic material was varied with the applied voltages U on the upper and lower electrodes firstly, then the effective refractive index of the electro-optic waveguide core material was changed and modulated by the thermo-optic effect of this polymer material, at the same time, the wavelength of the input optical signal was blue shifted or red shifted representing the switch states of ''on/off'' and output as the results of logical operations. Different optical circuit structures logic-oriented-based with different logical operation functions can also be designed applying multiple micro rings polymer-based coupled with different shapes of optical waveguides. A variety of optical switch logic operations such as and/or, or/nor and xor/xnor can be completed by modifying the logic input of different ports.

B. THE ELECTRO-OPTIC PROPERTIES OF THE POLYMER-BASED

The property of the electro-optic polymer-based is studied using the main axis O-XYZ coordinate system applied voltages for the electro-optical modulation. It is assumed that the polymeric material has been polarized in the direction of its thickness and this polarized direction is set as axis *z* [7]. The polarized polymer material has the characters of anisotropic and the effect of electro-optic. The electrode structure, as shown in Figure.3, is symmetrical and each distribution $E_y(x, y)$ of the electric field along the axis *y* caused by the modulation voltage *U* was expressed as:

$$
E_y(x, y) = E_{1y} + E_{2y}(x, y) + E_{3y}(x, y)
$$
 (1)

Where, E_{1y} represents a uniform electric field between the upper and lower electrodes, the non-uniform fields $E_{2y}(x, y)$ and $E_{3y}(x, y)$ formed by the two upper and lower surface electrodes may be neglected due to its weakness. E'_{1y} represents electric field in the buffer layer. By applying the continuity

FIGURE 3. Diagram of cross section of waveguide in an electro-optical coupled area.

	Download port	Pass-through	
		port	
Working		$-40dB$	
wavelength in the	Logic"1"	Logic $"0"$	
non-resonant area	$-60dB$		
High-level voltage	Logic $"0"$	Logic \lq ¹	

TABLE 2. Working wavelength in the resonant area.

conditions $n_1^2 E_{1y} = n_2^2 E'_{1y}$ of the electric displacement at the interface of the dielectric layer and the relationship $U =$ $E_{1y}b_1 + 2E'_{1y}b_2$ between the applied voltage and electric fields, the uniform electric field E_{1v} in waveguide core was deduced as equation [\(2\)](#page-2-0).

$$
E_{1y} = \frac{n_2^2 U}{2n_1^2 b_2 + n_2^2 b_1}
$$
 (2)

Accordingly, the refractive index change of the electrooptic material with the electro-optic coefficient r_{33} in the waveguide core was described as equation [\(3\)](#page-2-1).

$$
\Delta n_1 = \frac{1}{2} n_1^3 r_{33} E_{1y} = \frac{n_1^3 n_2^2 r_{33} U}{2(2n_1^2 b_2 + n_2^2 b_1)}
$$
(3)

Since the buffer layer in the waveguide is a kind of nonelectro-optic material, the refractive indexes n_2 and n_3 will not vary with the applied electric field.

C. OPERATING MODES OF MULTI-FUNCTIONAL ELECTRO-OPTICAL LOGIC DEVICES BASED ON POLYMER

The same as the ''positive logic'' and ''negative logic'' applied in digital circuits, the working wavelength in the resonant or non-resonant area is a dual operating mode in the optical logic-oriented [8]. The working wavelength is determined by the spectral response at the downloading port after the broadband light of different wavelength is input to the logic circuit, furthermore, the operating mode and the modulation voltage can be gotten.

The first one is in a high-level voltage with its wavelength in the non-resonant area, similar to the ''positive level'' in the electrical logic. The second one is in a low-level voltage with the wavelength in the resonant area, similar to the ''negative level'' in the electrical logic as summarized in Table 1, 2 above.

III. DESIGN OF SWITCHING LOGIC DEVICES BASED ON THE MICRO-RING RESONATOR

The structure of the switch logic device based on the micro ring resonator is shown in Figure.4. It consists of three micro rings coupled with a straight, a U-shaped and a curved waveguide. The varied voltages applied on Ring1, Ring2 and Ring3 are used as logic input of this device and the real optical signal input is output to the ports of Output1, Output2 or Output3 as the result of different logic operations such as and/or, or/nor and xor/xnor.

FIGURE 4. Diagram of the device structure.

It is presumed that all of the amplitude transfer factors *k* in the corresponding coupled area of three annular waveguides are the same, then the corresponding amplitude transmission factor *t* is the same too. Assuming that the radius of the micro ring is *R*, the corresponding coupled area can be expressed as:

$$
\begin{bmatrix}\nA_2 \\
B_2\n\end{bmatrix} = \begin{bmatrix}\n\frac{t}{jk} & -\frac{1}{jk} \\
\frac{1}{jk} & -\frac{t}{jk}\n\end{bmatrix} \begin{bmatrix}\nA_1 \\
B_1\n\end{bmatrix},
$$
\n
$$
\begin{bmatrix}\nC_1 \\
D_1\n\end{bmatrix} = \begin{bmatrix}\n\frac{t}{jk} & -\frac{1}{jk} \\
\frac{1}{jk} & -\frac{t}{jk}\n\end{bmatrix} \begin{bmatrix}\nC_2 \\
D_2\n\end{bmatrix},
$$
\n
$$
\begin{bmatrix}\nE_2 \\
F_2\n\end{bmatrix} = \begin{bmatrix}\n\frac{t}{jk} & -\frac{1}{jk} \\
\frac{1}{jk} & -\frac{t}{jk}\n\end{bmatrix} \begin{bmatrix}\nE_1 \\
F_1\n\end{bmatrix} \qquad (4)
$$
\n
$$
\begin{bmatrix}\nG_1 \\
H_1\n\end{bmatrix} = \begin{bmatrix}\n\frac{t}{jk} & -\frac{1}{jk} \\
\frac{1}{jk} & -\frac{t}{jk}\n\end{bmatrix} \begin{bmatrix}\nG_2 \\
H_2\n\end{bmatrix},
$$
\n
$$
\begin{bmatrix}\nI_2 \\
J_2\n\end{bmatrix} = \begin{bmatrix}\n\frac{t}{jk} & -\frac{1}{jk} \\
\frac{1}{jk} & -\frac{t}{jk}\n\end{bmatrix} \begin{bmatrix}\nI_1 \\
J_1\n\end{bmatrix},
$$
\n
$$
\begin{bmatrix}\nK_1 \\
L_1\n\end{bmatrix} = \begin{bmatrix}\n\frac{t}{jk} & -\frac{1}{jk} \\
\frac{1}{jk} & -\frac{t}{jk}\n\end{bmatrix} \begin{bmatrix}\nK_2 \\
L_2\n\end{bmatrix} \qquad (5)
$$

Supposing the bend loss of the micro ring is α_R , the rules of light transmission in these three micro rings are shown as

the following equations(6-8).

$$
\begin{bmatrix} C_2 \\ D_2 \end{bmatrix} = \begin{bmatrix} 0 & \exp(-j\phi) \\ \exp(j\phi) & 0 \end{bmatrix} \begin{bmatrix} A_2 \\ B_2 \end{bmatrix}
$$
 (6)

$$
\begin{bmatrix} G_2 \\ G_2 \end{bmatrix} = \begin{bmatrix} 0 & \exp(-j\phi) \\ \exp(-j\phi) \end{bmatrix} \begin{bmatrix} E_2 \\ E_2 \end{bmatrix}
$$
 (7)

$$
\begin{bmatrix} G_2 \\ H_2 \end{bmatrix} = \begin{bmatrix} 0 & \exp(-j\phi) \\ \exp(j\phi) & 0 \end{bmatrix} \begin{bmatrix} E_2 \\ F_2 \end{bmatrix}
$$
 (7)

$$
\begin{bmatrix} K_2 \\ L_2 \end{bmatrix} = \begin{bmatrix} 0 & \exp(-j\phi) \\ \exp(j\phi) & 0 \end{bmatrix} \begin{bmatrix} I_2 \\ J_2 \end{bmatrix}
$$
 (8)

Where, the phase factor is calculated by the formula $\phi =$ $\pi R(\beta - j\alpha_R)$ and the mode transfer coefficient is obtained by the formula $\beta = 2\pi n_{\text{eff}}/\lambda$.

At the same time, it is assumed that the bend loss in the waveguide connected to MRR1 and MRR2 is α_1 and α_2 respectively. Ignoring the loss of straight waveguide, the performance of the optical signal transmission is derived from the following equations:

Between MRR1 and MRR3:

$$
I_1 = B_1 \tag{9}
$$

Between MRR1 and MRR3:

$$
E_1 = \exp(-j\pi R(\beta - j\alpha_1))D_1 \tag{10}
$$

Between MRR3 and MRR2:

$$
G_1 = \exp(-j\pi R(\beta - j\alpha_2))L_1 \tag{11}
$$

According to the above relationships, the normalized output light field intensity of the three output ports relative to the input port can be deduced. In this paper, the device with the polarized electro-photopolymer material with the electro-optic coefficient r_{33} has the character of the first-order linear electro-optic effect [10] and its refractive index described in the following equation [\(12\)](#page-3-0) varies with the applied voltages U [11].

$$
\Delta n_1 = \frac{1}{2} n_1^3 r_{33} E_1 = \frac{n_1^3 n_2^2 r_{33} U}{2(2n_1^2 d_2 + n_2^2 d_1)}
$$
(12)

Hence the modified effective refractive index is represented by equation [\(13\)](#page-3-1):

$$
n_{\text{eff}} = n_1 + \Delta n_1 \tag{13}
$$

Where, *U* is the voltage applied on the electrodes, n_2 and n_3 are the refractive indexes of the buffer layers and electrodes made from the non-polarized polymeric material, which will not vary with the applied voltages *U*. The logic operation functions of optical switches can be realized by the blueshift or redshift of the micro ring resonant wavelength caused by the variation of the applied voltages.

IV. STATIC SIMULATION OF THE MULTI-FUNCTIONGAL ELECTRO-OPTICAL LOGIC DEVICE FUNCTION UNDER IOT

According to the mathematical model, the static performances of the electro-optical logic gate device were simulated in Matlab software [32]–[34]. The set device parameters were listed in Table 3. A horizontal axis is represented as the logic voltages input to the micro rings and the vertical axis is

TABLE 3. Table of setting parameters in the simulations.

Parameters in the simulations Values					
Coupled distance between the straight	$0.23 \mu m$				
waveguide and the micro ring					
Amplitude coupled factor of k	0.1				
Bend radius of the micro ring of R	$10 \mu m$				
Bend loss of the micro ring a_R	0.25dB/km				
Refractive index n_2 of the buffer layer	1.461				
Thickness d_i of the core waveguide in the	$1.7 \mu m$				
micro ring					
Thickness d_{2} of the buffer layer in the	$2.5 \mu m$				
micro ring					
Effective refractive index	1.59				
Electro-optic coefficient of r_{33}	68pm/V				
Relations of coupled factor k and	$k^2 + t^2 = 1$				
amplitude transfer factor t					

TABLE 4. Applied voltages and the corresponding output spectrums of each micro-ring.

FIGURE 5. Diagram of the input and output of the logic gate.

shown as the output spectrum based on the results of different logic operations.

According to equation [\(12\)](#page-3-0), when *U* is equal to 51.8V, the change of refractive index of the electro-optic polymer is 0.001. Broadband light with different wavelengths is input

FIGURE 6. Diagrams of output spectrums of micro ring R1, R2, R3 applied with different voltages.

to this device, the logic voltage and normalized output spectrums corresponded are summarized below in Table 4 by changing the modulation voltage states of these three microrings.

As a logic gate of the logic circuit described in Figure.5, the corresponding relations between the logic output and the real ports of this device are logic output I corresponded to Output2, logic output II corresponded to Output3, logic

FIGURE 7. The modulated signals applied to the MRR.

output III corresponded to Output1 and logical output IV corresponded to Output1 plus Output3. A number of logical operation results can be achieved at the different ports by changing the logic input [35]–[39].

Compared with the ways of output shown in Figure.6 (ah), the resonant peak-wavelength 1.51463 μ m is tested and determined as the working wavelength. In the first working pattern, it is defined as a high-level with the control voltage 51.8V on the micro ring as logic ''1'' and a lowlevel *OV* as logic "0". If there is no voltage applied on the micro ring or the logic input is ''000'', the normalized light intensity output to Output1, Output2 and Output3 is-28.89dB,-28.87dB and 0dB respectively, which is defined as the logical ''001''. In the same way, the logical values of the normalized output light intensity corresponding to the different logic input can be obtained in the condition of the working wavelength [40]–[44].

V. THE LOGIC FUNCTION DYNAMIC SIMULAITON OF THE OPTIC-ORIENTED DEVICE AND ITS IOT APPLICAITONS

A high-level voltage *51.8V* and a low-level *0V* applied to the MRR are defined as logic "1" and logic "0" separately. Dynamic simulation experiments based on Matlab software were done and two kinds of modulated signals shown in Figure.7 are used as the logic input.

A. IMPLEMENTATION OF THE LOGIC OPERATIONS AND/NOT

X signal is applied to MRR1 and MRR3 and *y* signal to MRR2. The output signals of ports II and III are shown in Figure.8 (a) and (b). The normalized output intensity below the threshold of 0.2 and above the threshold of 0.8 is defined as logic ''0'' and logic ''1'', then the output of the logic signals are shown in Figure.8 (c) and (d).

Comparing the input signals *x* and *y* and analyzing the logic outputs of ports II and III, conclusions are drawn as the follows:

- 1) When the input is all of "1", the output of port II is logic "1", if there is one logic "0" between the input signals x and y , logic " 0 " is the output, the algorithm of the AND operation is satisfied.
- 2) The logical output corresponding to port III is reciprocal to the logical value of the input signal *x*. The algorithm of the NOT operation is realized.

FIGURE 8. Simulation results of logic operations of AND And NOT (a) Logic operation of AND at port II. (b) Logic operation of NOT at port III. (c) Logic values of AND (d) Logic values of NOT.

Above all, logic functions of AND and NOT of this device can be achieved.

B. IMPLEMETNTAITON OF THE LOGIC OPERATIONS OR/NOR

X signal is applied to MRR1 and MRR2 and *y* signal to MRR3. The output signals of ports II and III are shown in Figure.8 (a) and (b). Similarly, the normalized output intensity below the threshold of 0.2 or above the threshold of 0.8 is

FIGURE 9. Simulation results of logic operations OR and NOR (a) Logic operation of OR at port II. (b) Logic operation of NOR at port III. (c) Logic values of OR. (d) Logic values of NOR.

defined as logic "0" or logic "1", then the output of the logic signals are shown in Figure.9 (c) and (d).

Comparing the input signals *x* and *y* and analyzing the logic outputs of ports II and III, conclusions are drawn as the follows:

1) When the input is all of ''0'', logic ''0'' is the output of port II, if there is one logic "1" between the input signals x and *y*, logic "1" is the output, the algorithm of OR operation is satisfied.

2) When the input is all of ''0'', logic ''1'' is the output of port III, if there is one logic "1" between the input signals

FIGURE 10. Simulation results of logic operations XOR and XNOR (a) Logic operation of XOR at port I. (b) Logic operation of XNOR at port IV. (c) Logic values of XOR. (d) Logic values of XNOR.

x and *y*, logic ''0'' is the output, the algorithm of the NOR operation is satisfied.

Above all, logic functions of OR and NOR of this device can be achieved.

C. XOR/XNOR LOGIC OPERATIONS WITH IOT **APPLICATIONS**

The truth table of the logic operations XOR and XNOR are shown in Table 5. *x* signal is applied to MRR1 and *y* signal

TABLE 5. The truth table of logic operations XOR/XNOR.

Logic operation of XOR				Logic operation of XNOR		
Input	Input	Output	Input	Input	Output	

FIGURE 11. Internet of brain things application with optical communication technology.

to MRR2 and MRR3, the output signals of ports I and IV are shown in Figure.10 (a) and (b). Similarly, the normalized output intensity below the threshold of 0.2 or above the threshold of 0.8 is defined as logic ''0'' or logic ''1'', then the output logic signals are shown in Figure.10 (c) and (d).

Compared with the input signals *x* and *y*, and analyzing the logic outputs of ports I and IV, conclusions are drawn as the follows:

1) When the input logic is same, logic ''0'' is the output of port I, while the input logic is different, logic ''1'' is the output of port I, the algorithm of the XOR operation is satisfied.

2) When the input logic is same, logic ''1'' is the output of port IV, while the input logic is different, logic ''0'' is the output of port IV, the algorithm of the XNOR operation is satisfied.

Above all, logic functions of NOR and XNOR of this device can be achieved. And now Internet of things with optical communication technology used in many field [45]–[49], shown as in Figure.11.

VI. CONCLUSIONS

Compared with the characteristics of inorganic electrooptical materials, the polarized polymer material has the features of the higher electro-optical coefficient, easier to adjust the refractive index, faster response and lower threshold of switching voltage. In this paper, Multi-functional electrooptical logic gate applied this polarized polymer-based material is designed and modulated by the change in the refractive index. The working wavelength of this device is determined by the static simulation and a variety of the logic operations based on the different states of optical switch such as and/or, or/nor and xor/xnor is realized respectively with the variation of different logic input by the dynamic simulation. It is proved this device has the function of high-speed and efficient logic operations. This design method also give a useful reference to many IOT research and applied field [50]–[60], especially for Internet of brain things field.

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