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# An FPGA-Based Open Architecture Industrial Robot Controller

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**ABSTRACT** Most robot controllers are proprietary and, therefore, they have a closed architecture, and it is typical for robot vendors not to provide support for old control units; hence, when a deprecated robot requires an update or even a simple preventive maintenance, the manufacturer tends to recommend disposing such a unit and to acquire a new one. In this paper, the development of a field programmable gate array-based motion control system is described as part of an open-architecture and vendor-independent control system tested on a Fanuc S420F. As a way to measure the performance of the developed system, an experimental test is carried out to achieve a path tracking RMS error lower than 20 mm at a Cartesian velocity of 500 mm/s. Results demonstrate that the proposed system achieves the same feed rates as the original controller with the significant advantage of being modular, flexible, scalable, and reconfigurable.

**INDEX TERMS** Control-retrofit, FPGA, industrial-robot, motion-controller, open-architecture, servo-control.

## **I. INTRODUCTION**

The mechanical part of industrial robots has not evolved at the same rate as its electronics. Nowadays, robot control systems are faster, smaller, and even they incorporate sensory information such as force and vision. These technological advances have permitted the number of applications of such machinery to become widespread [1]; however, the closed architecture of their control systems involves a continuous dependency of the end-user on the (particular) robot vendor [2].

The above problem surfaces when maintenance, update, or the incorporation of new functionality to a working—and usually deprecated—unit is required. This is because most of the robot manufacturers do not provide support to deprecated control units and their controllers have limited scalability. A modular, open-architecture [3] and vendor-independent control system is required to solve this problem.

Nowadays, there is still not a standard robot control architecture; there are some formal proposals for standardizing robot control architectures, but in practice there is no de facto standard and each control system has a unique architecture, as is the case with most home-made or customized lowperformance CNC machinery systems and 3D printers [4]; however, a robot control system as the one mentioned above implies basically the development of three main components: a hardware platform, an operating system (OS)-based module, and a software application as Hong *et al.* describe in [5].

The hardware platform comprises as a minimum the following instances: robot (mechanical systems) and actuators, power amplifiers, data acquisition boards, servo control components, and a master device for coordination of tasks as it is addressed in [6].

Computer controlled hardware requires a way to communicate with the computer. OS modules are conceived as hardware drivers. It is mandatory that such drivers have a realtime (RT) behavior for specific applications such as robot trajectory generation since it is planned on the fly [7]. Those drivers are responsible for performing data acquisition, computing interpolation and solving inverse kinematics; on top of these drivers, the application software serves as a humanmachine interface that enables the user to coordinate with the robot controller by entering commands using both a friendly interface and a specific-purpose programming language.

Servo control is the most critical part in a robot control in the sense that its failure to maintain a closed-loop behavior may lead to a halt in the production process, the destruction of the robot itself or, in the worst-case scenario, the loss of human lives. Therefore, most researchers have implemented robot control systems with mainly three kinds of servo control solutions, namely: standard/industrial PC with data acquisition boards, digital signal controllers (DSC), field programmable gate arrays (FPGA), or any combination of them such as the programmable system on a chip (SoC) technology.

## **II. RELATED WORK**

Since the decade of the 90s, researchers have been developing customized robot controllers to avoid the problems mentioned above. For instance, Ferretti *et al.* [8] implemented a methodology [9] to extend the functionality of a Comau C3G-9000 robot controller. They incorporated sensor boards to the system which are connected to the main control units through a common bus. Both units, robot controller and servo control, include several microprocessors and digital signal processors (DSP). The objective of the authors was to perform rubbing tasks; however, they do not report any results of the developed robot controller performance. Furthermore, the cost of a multi-processor based solution is considerably higher compared with the reconfigurable logic technology. A simplified solution for the same robot controller is found in [10]. This latter research work deals with the implementation of a robot controller within the Linux RTAI OS [11]; however, as in the case above, the authors do not include information about the system performance.

Another similar hardware architecture is described in [12] to control an ABB industrial robot for spot and arc welding applications. Here, the authors propose a complete architecture for efficiently programming robotic systems. Nevertheless, given that the hardware architecture is also based on a hybrid multi-processor and DSP system, the proposed architecture is focused only on software layers, which does not allow the controller to exploit reconfigurable logic hardware techniques. Besides, the authors do not prove in a quantitative way that the suggested architecture streamlines the programming process.

In [5] Hong *et al.* implemented a PC-based robot controller following the OSACA approach [13]; the hardware module of the robot control system comprises a standard PC running Windows NT and a *Delta Tau PMAC* [14] motion controller card, and the other two components are hardware drivers and the application software. In contrast with previous research works, Hong *et al.* do include information related to the system performance; nevertheless, the software interface only supports off-line programming because the operating system has no RT capabilities and, therefore, it is necessary to compute the coordinate transformations for the whole trajectory before its execution.

In [15], a group of undergraduate students carried out a hardware retrofit of a *Unimate* PUMA 560 robot by replacing the VAL II control system with a standard computer and a data acquisition board. The software architecture was based on MATLAB/Simulink and ran under Windows 2000 and

WinCon for RT performance. However, the WinCon platform does not guarantee RT task scheduling, and hence, a more demanding application cannot be successfully performed. Another similar work is described in [16]. The authors replaced the VAL II of a PUMA robot with a PC based controller; however, as in the case above, the developed system does not involve an RTOS and only can be used with robots actuated by DC brushed motors.

In [17] a visual servoing system is implemented on an FPGA to control a four DoF robot. Same case as above, the motors of this robot are DC brushed motors and they do not require any kind of external commutation; therefore, this solution can not be used for industrial robots such as the Fanuc S420.

In this sense, most authors affirm that an industrial robot controller must have an RT behavior. In [7], they developed a robot control system (ROACS) for a PUMA 560 to cut fish slices, and claim that within an uncertain environment, the robot control system must be able to change its path according to feedback sensory information. The ROACS is software-based using the QNX real-time operating system (RTOS).

In [18] Visioli and Legnani experimented with a SCARA industrial robot. They implemented several control schemes with the aid of a standard PC and the QNX RTOS; nevertheless, their controller only concerns to position control, and they do not include any other feature of a whole robot controller.

Similarly, Chang *et al.* [19], using a *Texas Instruments TMS320C3X* from the DSP family, implemented a position control loop with a digital filter in the input to avoid vibration due to slewing motion for a linear motor. Although the DSP platform presents several processing advantages over a standard PC, the *TMS320C3X* device family only has two encoder quadrature interfaces; therefore, an industrial robot with six degrees-of-freedom (DoF) requires at least three devices which increases the cost of the solution and its hardware complexity.

An interesting control architecture is described by Shao and Sun [20], involving a mixed DSP-FPGA digital platform for the motion control of a two DoF Yamaha robot; they report that their system exhibits a better performance than the original control system. Similar work can be found in [21] and [22]. In both research works, the complexity of interpolation and kinematics is delegated to the DSP; a standard PC with an RTOS replaces the DSP which leads to having a system with a higher degree of openness [23].

Modern motion control implementations are based on FPGAs because of the advantages they have over DSPs and micro-controllers [24]. In [25] an FPGA-based motion controller is developed to control the position of an XY table actuated by permanent-magnet synchronous motors (PMSM); however, this research work only deals with two axes, and the amount of logic elements it utilized makes it difficult to implement such motion controller in most lowdensity FPGA families. Furthermore, the usage of the Nios

#### <span id="page-2-0"></span>**TABLE 1.** Former robotic research projects.



microprocessor limits its implementation to only a few device families.

Most of the related research work deals with small-size robots such as the SCARA, PUMA, and others mostly actuated by direct current (DC) brushed motors as it is summarized in Table [1.](#page-2-0) In contrast, the brushless motors of bigger robots require external commutation which leads to a major complexity in the motion control system.

Up to date, there is no evidence of the development of a functional control system suitable for robots such as the Fanuc S420F. The aim of this paper is to contribute to this gap with an FPGA-based motion controller for industrial robots actuated by PMSM and servo amplifiers with external commutation.

## **III. METHODOLOGY**

The industrial robot used in this research work was disposed from a factory and acquired by Universidad Autónoma de Querétaro as a second-hand industrial system, and no control unit was included with the purchase; therefore, the aim of this project was to design a new control system with modern technology in order to reassemble an operational robotic cell for arc welding processes. The control system had to accomplish three primary requirements: low cost, open architecture, and a path tracking error lower than 2% at a Cartesian velocity of 500 mm/s.

The proposed robot control architecture comprises a standard PC and an FPGA-based interface which involves a lower cost. Additional cost saving is achieved by using the opensource Linux OS in addition with the real-time application interface (RTAI) patch for RT scheduling of tasks.

The FPGA is responsible for decoding feedback position data, computing the control output and commutation signals, and transmitting such data to the actuators, whereas the PC interpolates movements, computes kinematics solutions, and transmits the position reference for each joint while it receives its current position. The entire system is illustrated in Fig. [1.](#page-2-1)

The modules within the system have been arranged according to their RT requirements as shown in Fig. [2,](#page-2-2) being the most critical tasks implemented in hardware, whereas non-critical tasks such as user-interface and robot animation remain as simple threads in the user-space of the Linux kernel.



**FIGURE 1.** Robot controller hardware setup.

<span id="page-2-1"></span>

<span id="page-2-2"></span>**FIGURE 2.** Robot control system layers.

#### A. INSTRUMENTATION

A reverse engineering process was carried out in order to identify the signals in the feedback encoder for each of the robot motors. Quadrature and phase control signals are available in the PCB of the feedback encoder as illustrated in Fig. 3a.

The quadrature signals are decoded for position feedback whereas phase control signals are used for commutation. With the aim of preserving the integrity of the signals on









**FIGURE 3.** Instrumentation of the feedback device: (a) original device and (b) modified version.

the transmission line, an additional PCB with a couple of *DS34C87TM* RS-422 line drivers has been attached to the feedback device as depicted in the Fig. 3b. On the other hand, a PCB with *DS34C86* RS-422 line receivers is connected to the FPGA board.

The Fanuc S-420F is actuated by AC permanent magnet synchronous motors (PMSM); therefore, six *Advanced Motion Controls* servo amplifiers with external commutation [26] are utilized as power stage.

These servo drivers require two analog current references for two of the motor coils while the third one is computed internally. Current references are given as voltage, i.e., a setpoint of  $+10$  V represents a current of  $+30$  A at a given motor coil. Two instances of the *Analog Devices* digital to analog converter DAC5668 are used to generate such current



<span id="page-3-0"></span>**FIGURE 4.** Architecture of the motion controller circuit.

references; however the DAC output ranges from 0 V to 5 V; therefore, another PCB includes a set of operational amplifiers to rearrange the output from  $-10$  V to  $+10$  V.

## B. HARDWARE DESCRIPTION

The motion controller circuit includes mainly a master interface circuit, several instances of a servo controller module and a digital to analog converter driver as illustrated in Fig. [4.](#page-3-0)

The *Master Interface* module serves as an interface between the PC and the motion controller. On the PC side, it has a parallel bus with an eight-bit width, and it is designed to be used with the LPT port due mainly to the following reasons:

- Synchronization of the PC and the FPGA. By using this port, it is possible to assure that the transfer of a word is always done at the required time instant.
- The parallel port is more reliable under high electrical noise conditions as in this case.
- A baud rate of 1.2Mbps is achieved with the enhanced parallel port (EPP), which is fast enough to send and receive the reference and position data of the six robot joints.
- And finally, because of its simplicity and versatility as it is documented in [27].

There are other and more common ways to link an external system to a PC, but this is done as a proof of concept showing the modularity of our proposed architecture. Moreover, in [28], the EPP, UART, and Ethernet ports are evaluated, obtaining the fastest baud rate with the parallel port.

Additionally, to the *DIO* data bus, there are four control signals, namely, *CS* (chip select), *PS* (port select), *RD* (read), and *WR* (write) as illustrated in Fig. [5.](#page-4-0)

It is necessary to enable *CS* to start a data transaction. The signal *PS* indicates the type of transaction; there are two types of transactions: data and command transactions—the data transactions are referred to the values of the registers, while command transactions determine whether it is a read or write process and the destination address. The PC or master device transfers a byte each time the *RD* or *WR* are asserted.

Given that the *Master Interface* module has a shift register internally for reading and writing operations, up to four bytes may be transferred in one single data transaction.



<span id="page-4-0"></span>**FIGURE 5.** Block diagram of the Master Interface module.

On the other hand, there are three buses and one control signal. *MISO* and *MOSI* have 32 bits of width, and they are used for data interchange. The bus *ADDR* is used to select the *Servo Controller* instance and the register. The signal *ALE* (address latch enable) acts as a trigger for the data interchange, i.e., the register with the specified address stores data when this signal is asserted.

The above process is possible since each *Servo Controller* instance has a unique address. The three most significant bits of the *ADDR* bus indicate the number of *Servo Controller* instance while the last four indicate the parameter to be accessed.



<span id="page-4-1"></span>FIGURE 6. Block diagram of the Servo Controller module.

The *Servo Controller* module is composed of an encoder quadrature interface, a summing block, a proportionalintegral-derivative (PID) controller, a sinusoidal commutator, and several registers used to store reference and controller parameters as illustrated in Fig. [6.](#page-4-1)

The *Encoder Quadrature Interface* decodes the mechanical position of each joint from A and B quadrature signals. The output of this module has a width of 32 bits. The position signal (POS) is subtracted to the set point signal (REF), and it is adjusted to 16 bits of width. The above to avoid saturation in forward computations. The resulting signal represents the position error, and it is labeled as ERR in Fig. [6.](#page-4-1)

The *PID Controller* is implemented as a fourth-order infinite-impulse-response (IIR) filter as that one described in [29] but without a derivative low-pass filter. The implemented formula is

$$
u(k) = K_p \Big\{ e(k) + \frac{T_s}{T_i} \sum_{i=1}^k e(i) + \frac{T_d}{T_s} [e(k) + 3e(k-1) - 3e(k-2) - e(k-2)] \Big\}, \quad (1)
$$

where  $K_p$ ,  $T_i$ ,  $T_d$ , and  $T_s$  are the proportional gain, integral time, derivative time, and sampling time, respectively. To avoid the storage of the error samples from time  $t = 0$ *s* until  $t = kT_s s$  and with the help of the relationship  $\Delta u(k) =$  $u(k) - u(k - 1)$ , a suitable function may be written as:

$$
u(k) = q_0 e(k) + q_1 e(k-1) + q_2 e(k-2)
$$

$$
+ q_3 e(k-3) + q_4 e(k-4) + u(k-1), \quad (2)
$$

where

$$
q_0 = K_p \Big( 1 + \frac{T_s}{T_i} + \frac{T_d}{6T_s} \Big),
$$
  
\n
$$
q_1 = -K_p \Big( 1 - \frac{T_d}{3T_s} \Big),
$$
  
\n
$$
q_2 = -K_p \frac{T_d}{T_s},
$$
  
\n
$$
q_3 = K_p \frac{T_d}{3T_s},
$$
  
\n
$$
q_4 = K_p \frac{T_d}{6T_s}.
$$

These coefficients are given in a fixed point 16.16 format whereas the error and output signal has 16 bits of width. The *PID Controller* computes the control signal UOUT with the aid of a multiplier-accumulator (MAC) unit as illustrated in Fig. [7.](#page-5-0) The control frequency is fixed at 1 KHz.

Most of the dynamic parameters of the robot are unknown; hence, it is not possible to tune-up the controller with an analytical approach. Methods such as the Ziegler-Nichols require taking the system to the stability limit, which is not recommended for a robot since it could be seriously damaged. Therefore, an empirical method is used to tune-up the PID controller for each joint. First, the proportional gain is set in order to reach the set-point within a short time; then, the derivative time is increased to provide a well damped response; and finally, the integral time is decreased to achieve a null or very close to zero error at steady state. Table [2](#page-5-1) enlists the obtained values for each joint.



<span id="page-5-0"></span>**FIGURE 7.** Block diagram of the PID controller.



<span id="page-5-2"></span>**FIGURE 8.** Block diagram of a Sinusoidal Commutator.

<span id="page-5-1"></span>**TABLE 2.** PID parameters for each robot joint.

<b>Joints</b>	$K_{\bm{n}}$	T,	$T_{d}$
J 1	8.0	0.15	0.12
$_{12}$	12.0	0.35	0.15
J3	16.0	0.17	0.22
J4	4.5	0.065	0.12
J5	6.0	0.065	0.12
J6	4.0	0.065	0.12

The *Sinusoidal Commutator* module reads the control output (UOUT) and the phase control signals (COM) to compute the phase current for each motor coil. Moreover, the phase currents have a sine waveform, and their amplitude depends on the value of the control signal. The frequency of these signals, commonly called *commutation frequency*, varies according to the motor velocity.

The computation of the phase currents requires the knowledge of the position of the poles with respect to the stator fluxes. The feedback device has an absolute encoder for this purpose, and its signal is labeled as COM in Fig. [8.](#page-5-2) The phase control signal has four bits of width, and it is used along with a look-up table to generate sixteen points of a sine wave.

The decoded sine value has eighteen bits, and it is given in a 2.16 format as listed in Table [3.](#page-5-3) The phase currents (PHA and PHB) are computed by multiplying the sine value times the filter output, which is internally extended also to eighteen bits. The second phase current PHB is shifted 112.5 electrical degrees. Both signals, PHA and PHB, are saturated and trimmed to sixteen bits.

<span id="page-5-3"></span>**TABLE 3.** Sine look-up table for commutation signals.

COM	$\alpha$ (deg)	$\sin(\alpha + 90)$	$\sin(\alpha - 22.5)$
0000	157.5	11.0001001101111110	00.1011010100000100
0001	180.0	11.0000000000000001	00.0110000111110111
0011	202.5	11.0001001101111110	00.0000000000000000
0010	225.0	11.0100101011111100	11.1001111000001001
0110	247.5	11.1001111000001001	11.0100101011111100
0111	270.0	00.0000000000000000	11.0001001101111110
0101	292.5	00.0110000111110111	11.0000000000000001
0100	315.0	00.1011010100000100	11.0001001101111110
1100	337.5	00.1110110010000010	11.0100101011111100
1101	0.0	00.111111111111111	11.1001111000001001
1111	22.5	00.1110110010000010	00.0000000000000000
1110	45.0	00.1011010100000100	00.0110000111110111
1010	67.5	00.0110000111110111	00.1011010100000100
1011	90.0	00.0000000000000000	00.1110110010000010
1001	112.5	11.1001111000001001	00.111111111111111
1000	135.0	11.0100101011111100	00.1110110010000010

Finally, the current references PHA and PHB are sent to the DAC through a serial peripheral interface (SPI) with a serial clock frequency of 25 MHz. This module, labeled as *DAC Driver*, is at the end of the data path depicted in Fig. [4.](#page-3-0)

#### C. INTERPOLATION AND KINEMATICS SOLUTION

A robot control system needs a way to describe the instructions provided to the robot. Of course, the instructions given to the robot are directly related to the actions the robot can perform, so describing a robot programming language is equivalent to describing the variables that influence the operation of the robot to be controlled, positions, speeds and accelerations of the described paths.

There are different types of paths that a robot can perform according to the purpose being sought; in general terms, there are two types of displacements: (a) those intended to move the end-effector from one point to another without performing useful work but merely to bring it to a point of interest, and (b) motions destined to do some useful work such as welding or painting a surface. The first type of motion can be performed in a coordinated way in the Cartesian coordinate system (usually as linear interpolation), or in a coordinated way in the space of the robot joints; the second type of movement is always coordinated in Cartesian coordinates following a well-defined trajectory that can be linear, circular, some particular type of trajectory (e.g., elliptical or parabolic) or interpolated (e.g., Bézier curves or splines); depending on the type of trajectory, it is possible to re-parameterize the curve to obtain a concordance between the velocities and accelerations specified by a previously chosen velocity profile (triangular, trapezoidal, trigonometric, etc.). Note that the paths that describe the movement of the robot occur in 6 dimensions because the orientation must also be specified in addition to the position; our control system uses the same speed profile for both position and orientation.

The desired trajectory for testing the system is a straight line over the space at a given maximum linear velocity  $\omega_{\text{max}}$ ; hence, a trapezoidal velocity profile is utilized as interpolator

according to the acceleration equation:

$$
\alpha(k) = \begin{cases}\n+a, & 0 \leq kT_s \leq \frac{T}{3}, \\
0, & \frac{T}{3} < kT_s \leq \frac{2T}{3}, \\
-a, & \frac{2T}{3} < kT_s \leq T,\n\end{cases}
$$
\n(3)

where  $a > 0$  is a constant acceleration,  $T$  is the duration of the motion, and  $T_s$  is the sampling period. The motion period is computed as

$$
T = \frac{3\theta_f}{2\omega_{\text{max}}},\tag{4}
$$

where  $\theta_f$  and  $\omega_{\text{max}}$  are the distance and maximum speed, respectively. On the other hand, the acceleration is obtained as

$$
a = \frac{3\omega_{\text{max}}}{T}.\tag{5}
$$

For the computation of the velocity function, the backward rectangular method (BRM) of numeric integration is used by setting

$$
\omega(k) = \omega(k-1) + \alpha(k) \times T_s.
$$
 (6)

Analogously, the position at any given time  $t = kTs$  is computed as

$$
\theta(k) = \theta(k-1) + \omega(k) \times T_s. \tag{7}
$$

An RT task within the kernel-space of the OS executes the computations for the six velocity profiles. These profiles describe dimensionless quantities; therefore, they may represent either angles or distances, it depends on the coordinate system used as described on Table [4.](#page-6-0)

<span id="page-6-0"></span>**TABLE 4.** Corresponding trapezoidal velocity profile function for each coordinate system.

Profile	Joints	Cartesian
1	J1 angle	X coordinate
2	J <sub>2</sub> angle	Y coordinate
3	J3 angle	Z coordinate
4	J4 angle	Roll angle
5	J5 angle	Yaw angle
6	J6 angle	Pitch angle

For Cartesian coordinates the first three velocity profiles describe the position coordinates while the last ones represent the orientation angles. Every single tool-center-point (TCP) [*p*] and orientation matrix [*Q*] is mapped to joint coordinates by solving the position inverse kinematics problem.

A set of coordinate frames  $F_i$  are attached to the robot joints as Fig. [9](#page-6-1) shows, according to the Denavit-Hartenberg (DH) convention [30]; Table [5](#page-6-2) enlists those parameters. We describe vectors  $[a_i]_i$  in the  $F_i$  frame which represent the coordinates of the origin for the  $F_{i+1}$  frame as

$$
[a_i]_i = \begin{bmatrix} a_i \cos \theta_i \\ a_i \sin \theta_i \\ b_i \end{bmatrix}.
$$
 (8)

**TABLE 5.** Denavit-Hartenberg parameters for the Fanuc S420F robot.

<span id="page-6-2"></span>

Joint	a(mm)	b(mm)	$\alpha$ (deg)
	270.0	1000.0	90.0
2	900.0	0.0	0.0
$\mathcal{F}$	270.0	0.0	90.0
4	0.0	1300.0	90.0
5	0.0	0.0	90.0
6	0.0	$-270.0$	0.0



<span id="page-6-1"></span>**FIGURE 9.** Coordinate frames attached to the robot joints.

In order to compute the forward kinematics it is necessary to perform a vector sum  $\sum_{i=1}^{6} a_i$ ; however, these vectors are described in different coordinate frames and, therefore, a homogeneous transform matrix

$$
[Q_i]_i = \begin{bmatrix} \cos \theta_i & -\cos \alpha_i \sin \theta_i & \sin \alpha_i \sin \theta_i \\ \sin \theta_i & \cos \alpha_i \cos \theta_i & -\sin \alpha_i \cos \theta_i \\ 0 & \sin \alpha_i & \cos \alpha_i \end{bmatrix} \quad (9)
$$

is used to map vectors from the  $(i + 1)$ -th frame to the *i*-th frame.

By substituting the DH paramenters for each position vector above, the solution for the forward kinematics of position is obtained by computing equations

$$
[c] = [a1]1 + [Q1]1([a2]2 + [Q2]2([a3]3 + [Q3]3([a4]4)))
$$
\n(10)

and

$$
[p] = [c] + \left(\prod_{i=1}^{5} [Q_i]_i\right) [a_6]_6, \tag{11}
$$

where  $[c]$  is the wrist center of the manipulator, while the attitude is given by

$$
[Q] = \prod_{i=1}^{6} [Q_i]_i.
$$
 (12)

In the opposite direction, the complete solution for the inverse kinematics is computed as follows:

- Step 1. Compute wrist center  $[c] = [p] [Q][a_6]_7$ , being  $[c] = [X_c \ Y_c \ Z_c]^T$  and  $[a_6]_7 = [0 \ 0 \ b_6]^T$ .
- Step 2.  $\theta_1 = \text{Arg}(X_c, Y_c)$ .
- Step 3.  $\sin \phi = \phi_n/(2.38994 \times 10^6)$  where  $\phi_n = X_c^2 +$  $Y_c^2 + (Z_c - b_1)^2 - 2 a_1(c_1X_c + s_1Y_c) - 2.5 \times 10^6, c_1 =$  $\cos \theta_1$  and  $s_1 = \sin \theta_1$ .
- Step 4.  $\cos \phi = \pm \sqrt{1 \sin^2 \phi}$ .
- Step 5.  $\theta_3 = \text{Arg}(\cos \phi, \sin \phi) 0.204781$ , and let  $c_3 =$  $\cos \theta_3$  and  $s_3 = \sin \theta_3$ .
- Step 6. Solve the equation:

$$
\begin{bmatrix} \cos \theta_2 \\ \sin \theta_2 \end{bmatrix} = \begin{bmatrix} a_2 + a_3c_3 + b_4s_3 & a_3s_3 - b_4c_3 \\ -a_3s_3 + b_4c_3 & a_2 + a_3c_3 + b_4s_3 \end{bmatrix}^{-1} \times \begin{bmatrix} c_1X_c + s_1Y_c - a_1 \\ Z_c - b_1 \end{bmatrix}.
$$

- Step 7. Let  $\theta_2 = \text{Arg}(\cos \theta_2, \sin \theta_2)$ .
- Step 8. Using  $\theta_1$ ,  $\theta_2$ , and  $\theta_3$ , compute

$$
[R] = [Q_3]_3^T [Q_2]_2^T [Q_1]_1^T [Q] = \begin{bmatrix} r_{11} & r_{12} & r_{13} \\ r_{21} & r_{22} & r_{23} \\ r_{31} & r_{32} & r_{33} \end{bmatrix}
$$

- Step 9. Compute  $s_5 = \sqrt{1 r_{33}^2}$ ,  $c_5 = -r_{33}$ , and  $\theta_5 =$ Arg(*c*5,*s*5), and
- Step 10. analogously,  $s_4 = \text{sign}(\theta_5)r_{23}$ ,  $c_4 =$  $sign(\theta_5)r_{13}$ , and  $\theta_4 = Arg(c_4, s_4)$ ,
- Step 11. and finally, compute  $s_6 = \text{sign}(\theta_5)r_{31}$ ,  $c_6 =$ sign( $\theta$ <sub>5</sub>) $r_{31}$ , and  $\theta$ <sub>6</sub> = Arg( $c_6$ ,  $s_6$ ).

#### D. IMPLEMENTATION AND EXPERIMENTAL SETUP

The motion controller has been implemented in a Xilinx Spartan-3 XC3S1000 [31] device. The number of ocuppied slices is around 39% which corresponds to the logic of the position control for the six robot joints and the peripherals for data exchange, detailed above in Table [6.](#page-7-0) Special care has been taken given that this particular device has only twentyfour  $18 \times 18$  multipliers. Two multipliers are used for the computation of the position control signal and another two for the commutation, being four multipliers per axis; thus, the design fulfills the multipliers capacity of the chip.

The Digilents Spartan-3 starter kit board includes a crystal oscillator of 50 MHz. On the other hand, the timing report from Table [6](#page-7-0) suggest that the maximum clock frequency

<span id="page-7-0"></span>**TABLE 6.** Synthesis report summary for the motion controller design.

Device utilization summary				
Logic utilization	Used	Available	Utilization	
Number of slice flip-flops	2998	15360	19%	
Number of 4 input LUTs	3743	15360	24%	
Number of occupied slices	3048	7680	39%	
Total number of 4 input LUTs	3792	15360	24%	
Number of bonded IOBs	100	173	57%	
Number of MULT18X18s	24	24	100%	
Number of BUFGMUXs		8	12%	
Timing summary				
Minimum period		12.174 ns		
Maximum frequency		82.143 MHz		
Minimum input arrival time		$7.179$ ns		
Maximum output required time		$12.12$ ns		
Maximum combinational path delay		$11.299$ ns		



<span id="page-7-1"></span>**FIGURE 10.** Experimental setup.

allowed is 82.143 MHz; therefore, timing constraints are successfully met.

The experimental setup comprises an industrial Fanuc S420F robot (bottom of Fig. [10\)](#page-7-1) actuated by six PMSM; each motor has an attached optical encoder for position and commutation feedback. The power cabinet (top-left corner of Fig. [10\)](#page-7-1) has six AMC servo amplifiers with external commutation, three of them rated at 30A of continuous current for the arm joints and the other three rated at 15A for the wrist joints. The system is powered directly with a 3-phase line at 220V.



**FIGURE 11.** Main screen of user interface application software.



<span id="page-8-1"></span>**FIGURE 12.** Cartesian response of the system at a feedrate of 500mm/s.

<span id="page-8-0"></span>Given that the robot motors are rated at 150V, a transformer is used to step down the line voltage.

A Spartan-3 starter kit board is included in the control cabinet (top-right corner of Fig. [10\)](#page-7-1). Several PCBs are connected to the FPGA for conditioning the commutation signals, electrical translation of the encoder signals, activation/deactivation of servo amplifiers, deactivation motor breaks, and for coupling the master interface signals.

As mentioned earlier, a standard desktop PC with an Intel $\mathbb{R}$  Core<sup>TM</sup> i7-4770 CPU at 3.40GHz is utilized to coordinate and to execute the robot trajectories. The PC runs the LTS Ubuntu 12.04 OS with the RTAI 3.6 patch. A Qt-based application, which is depicted in Fig. [11,](#page-8-0) has been developed to serve as a human-machine interface. This application has two main operation modes: manual and automatic; both modes perform movements either with joints or Cartesian coordinates.

#### **IV. RESULTS AND DISCUSSION**

Given that the purpose of this robotic system is the arc welding application, one of the tests of interest is a straight line trajectory in the Cartesian space. Thus, as a test, a point to point displacement using a linear interpolation is programmed into the software. The initial point has the coordinates  $p_1 = \begin{bmatrix} 2000 & 500 & 1000 \end{bmatrix}^T$  while the final one is  $p_2 =$  $\begin{bmatrix} 1000 & 500 & 1000 \end{bmatrix}^T$ , and the orientation for both points is given according to the Euler angles  $\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}^T$  where the first angle represents a rotation about the *Z* axis, while the second and third ones are rotations about the *Y* and *X* axes, respectively.

The total displacement of the TCP along the *X* axis is  $\theta_f = 1000$ mm and the maximum velocity is set at  $\omega_{\text{max}} =$ 500mm/s. Thus, by following a trapezoidal velocity profile, the period of the movement is  $T = 3s$  and the acceleration rate is  $a = 500$ mm/s<sup>2</sup>.

The Cartesian response of the system is depicted in Fig. [12.](#page-8-1) The data series in this Fig. are collected from the profile generator output and from the encoder measurement every



**FIGURE 13.** Tracking error: (a) position and (b) orientation.

millisecond; thus, we obtain three thousand data points for the test. The data obtained from incremental encoders is converted to joint angles, and then the forward kinematics solution is applied to each point in order to get the current Cartesian position of the robot.

The tracking RMS error for the position is illustrated in Fig. 13a. This curve represents the distance from the commanded position to the measured one, i.e., the norm of the error vector at a given time. The RMS tracking error is 13.7827, and the maximum position error is around 18.2425mm. Likewise, the orientation error is shown in Fig. 13b, where the three curves corresponding to the error for the three Euler angles are displayed.



**FIGURE 14.** System response at a feedrate of 500 mm/s. (a) Joints 1,2, and 3; (b) Joints 4,5, and 6.

Lastly, Figs. 14a and 14b depict the individual response for joints 1, 2, 3, 4, 5, and 6, respectively. Another important feature is that the joints 2 and 3 are decoupled, i.e., the motion of joint 2 does not alter the angle of joint 3; however, for the sake of simplicity of plots, the results are given as if those joints were coupled.

#### **V. CONCLUSIONS**

An open-architecture controller is proposed in this article; moreover, experimental data demonstrates that this architecture is completely functional and may be adopted as a starting point to develop a more sophisticated solution.

Every year a great number of robotic units are disposed from factories; hence, authors believe that a low-cost solution as the one presented in this paper may represent, for small and medium-sized industries, the opportunity of incorporating this sort of machinery to their processes and thus, increment their competitiveness.

The developed system achieves almost the same performance as the original controller, but this new version has major advantages such as modularity, given that it is possible to use H-bridges instead servo amplifiers, for example. In a similar manner, the resulting system is flexible in the sense that it may be implemented for a different robot architecture for instance, or simply, it is possible to connect a feedback device with a higher resolution without any modification. Besides, the usage of reconfigurable logic permits to change the control-law if an improved system is desired. Furthermore, the proposed system is scalable because the number of servo control instances may be adjusted according to the number of DoF of the robot to be controlled.

Furthermore, authors are convinced that the future of industrial controllers is aimed at the usage of programmable SoC, i.e., the implementation of software and hardware on a single silicon device. This may bring some benefits such as reducing system complexity, reduced time delays in data transfers between PC and FPGA, greater robustness to external electrical interference, and of course, higher cost savings.

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