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A Canonical-Based NPN Boolean Matching Algorithm Utilizing Boolean Difference and Cofactor Signature

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ABSTRACT This paper presents a new compact canonical-based algorithm to solve the problem of single-output completely specified input negation and/or input permutation and/or output negation Boolean matching. We propose a new signature vector Boolean difference and cofactor signature vector. Our algorithm utilizes the Boolean difference, cofactor signature, and symmetry properties to search for canonical transformations. The use of symmetry and Boolean difference notably reduces the search space and speeds up the Boolean matching process compared with the algorithm proposed by Adbollahi and Pedram. We tested our algorithm on a large number of circuits. The experimental results showed that the average runtime of our algorithm 37% higher and its average search space 67% smaller compared with Adbollahi and Pedram, when tested on general circuits.

INDEX TERMS Boolean difference, boolean matching, NPN equivalent, symmetry.

I. INTRODUCTION

Judging whether two Boolean functions are equivalent under input negation and/or input permutation and/or output negation (NPN) is an important problem applied in integrated circuit design, logic synthesis, logic verification, and so on. In cryptography, affine equivalence is used to resolve S-box problem [2]. Technology mapping is the process of selecting logic gates from a library to implement a Boolean circuit [3]–[5]. However, every Boolean function has many NPN-equivalent Boolean functions. Technology mapping searches for an optimal combination of logic gates in terms of area, performance and power dissipation. In the cell-library binding process, some of the cells of a library are found to realize some part of a multiple-level representation of a Boolean function [6]. Two Boolean functions*f* and *g* are NPN equivalent if a transformation *T* exists that can transform *f* to *g*. In technology mapping and cell-library binding, Boolean matching is a key step.

In recent years, numerous methods have emerged to solve the NPN Boolean matching problem. The main approaches focus on the canonical-based, pairwise and SAT-based

algorithms. In the canonical-based algorithms, two Boolean functions are NPN-equivalent when they have the same canonical form. An NPN-equivalent class has a canonical representative, and all the Boolean functions in an NPN-equivalent class can be transformed to this canonical representative. The canonical representative may have a maximal truth table or a minimal truth table or a maximal signature vector $[1]$, $[6]$ – $[12]$.

A pairwise Boolean matching algorithm searches the correspondence relation of the variables of two Boolean functions by their signatures. In the pairwise Boolean matching process, the search operation terminated when it finds a transformation *T* that can transform one Boolean function into the other. The authors of [13]–[16] presented a pairwise Boolean matching algorithm.

The NPN Boolean matching problem is converted to one that involves solving Boolean satisfiability (SAT) problem. To check whether a Boolean function *f* is equivalent to a Boolean function *g*, the SAT-based algorithm first constructs a circuit with the functionality $f \oplus g$ and then generates a SAT instance (circuit CNF). Finally, it judges the Boolean

satisfiability of this circuit CNF [17]. When the circuit CNF is satisfied, f is not NPN-equivalent to g . In contrast, f is NPN-equivalent to *g* when the circuit CNF is not satisfied.

It is well known that the search space complexity of NPN Boolean matching is $n!2^{n+1}$ using the exhaustive method. Therefore, the exhaustive method is computationally infeasible when the number of inputs is large, and thus, it is important to reduce the search space regardless of what approach is used. Many properties of Boolean functions are used to reduce the search space, including positive unate, negative unate, cofactor and symmetry, and so on.

This paper studies canonical-based NPN Boolean matching for single-output completely specified Boolean functions. We exploit a new compact canonical form using a DC signature vector. The canonical representative of an NPN equivalent class has the maximal DC signature vector. The use of the symmetry class, independent class and Boolean difference signature dramatically reduce the search space of the canonical transformation. The experimental results reflect that using the Boolean difference signature is highly conductive for distinguishing variables. The proposed algorithm enhances the NPN Boolean matching speed and reduces the search space significantly.

The remainder of this paper is organized as follows. In Section II, we survey the state of the Boolean matching problem. Section III introduces related preliminaries and definitions used in our algorithm. Section IV presents our canonical-based algorithm in detail. We demonstrate the effectiveness of our algorithm by presenting experimental results in Section V, and we summarize our work and outline future work in Section VI.

II. RELATED WORKS

Numerous scholars have contributed to this problem in prior Boolean matching studies. The authors of [1], [6]–[12] studied the canonical-based Boolean matching algorithm and proposed many practical methods for computing the canonical representative. The authors of [7] studied the P-equivalence problem and proposed a formal framework that unified the spectral and canonical-based approaches. They found a linear transformation to improve the speed of P-equivalence matching. Chai and Kuehlmann of [8] designed a fast Boolean matching that used satisfied counts to assign the phases of the Boolean function and its variables, the sums of rows or columns to search permutation, and symmetry to refine transformations. In [6], a canonical-based algorithm was presented that used table look-ups and a treebased breadth-first search to find the canonical representative with the smallest value in the binary representation. Ciric and Sechen [9] proposed the minimum-cost canonical form for P-equivalence matching. In [10], the authors unified multiple canonical-based approaches and proposed a new P-equivalence matching algorithm. Generally, in the canonical-based Boolean matching algorithms, the canonical representative has the maximal or minimal truth table. Abdollahi and Pedram [1] exploited a new canonical form

Many Boolean matching approaches have emerged based on SAT. In a SAT-based Boolean matching algorithm, Boolean matching is converted to a SAT or UNSAT problem. The authors of [17]–[22] all studied Boolean matching using the SAT technique. The authors of [18] proposed PP-equivalence matching based on graphs, simulation and SAT; their approach can be applied to large-scale circuits. Matsunaga [19] presented a Boolean matching method for LUT-based circuits by using one-hot encoding and the CEGAR technique to speed up Boolean matching [19]. The authors of [20] exploited architectural symmetry in PLB and proposed a SAT-based Boolean matching algorithm to resolve FPGA technology mapping. Wang *et al.* [17] integrated the simulation and SAT technique, and proposed a P-equivalent Boolean matching approach.

A pairwise Boolean matching algorithm was presented in [13] for multiple-output Boolean functions with don't care sets and applied Boolean matching to technology mapping to optimize circuits. Abdollahi [14] proposed a signaturebased Boolean matching to resolve NPN-equivalent problem for single-output incompletely specified Boolean functions. They tested their Boolean matching approach on 4-10 inputs Boolean functions. The authors of [15] utilized a level-first strategy and a set of filters to reduce the search space in the pairwise Boolean matching process, while [16] unified the structural signature of Boolean functions and Shannon expansion to exploit a pairwise Boolean matching algorithm.

A few other methods exist for solving the Boolean matching problem. In [23], the authors studied NPN Boolean matching using a Walsh Spectral Decision Diagram, and achieved an efficiency higher than that of Luks' hypergraph method. Lai *et al.* [24] utilized a conflict-driven learning method to solve the multiple-output Boolean function matching problem.

III. PRELIMINARIES AND PROBLEM STATEMENT

Let $f(x_1, x_2, \dots, x_n)$ be a single-output completely specified Boolean function and $X = (x_1, \dots, x_n)$ be a vector of f. |f| denotes the number of minterms in *f* , which is also called the 0th-order signature.

 $X_{\pi}(x_1, \cdots, x_n) = (x_{\pi(1)}, \cdots, x_{\pi(n)})$ is an input permutation and $X_{\varphi}(x_1, \cdots, x_n) = (x_1^{\varphi(1)})$ $\chi_1^{\varphi(1)}, \ldots, \chi_n^{\varphi(n)}$ is an input negation. $x_i^{\varphi(i)} = x_i$ when $\varphi(i) = 1$, and $x_i^{\varphi(i)} = \overline{x}_i$ when $\varphi(i) = 0$. NPN equivalence is introduced by $f \equiv g \Leftrightarrow f =$ $(g \circ X_{\pi} \circ X_{\varphi})^{\phi}$, where $\varphi \in \mathcal{B}^{n}(\phi \in \mathcal{B})$ [25].

Definition 1 (NP Transformation): An NP transformation, *T*, is an onto mapping where $T = \{x_1, \dots, x_n\} \mapsto$ ${x \phi(1), \dots, x_{\pi(n)}^{\phi(n)}}$.

 $\lim_{\tau(1)}$, $\lim_{n \to \infty}$, $\lim_{n \to \infty}$ function *f* in an NPN-equivalent class with a canonical representative \mathbb{F} , there must be an NP

transformation T that can transform f to \mathbb{F} , (i.e., $f(TX) =$ \mathbb{F} or $\overline{f(TX)} = \mathbb{F}$). The T is called a canonical transformation. Boolean function f_1 is NPN-equivalent to f_2 if and only if they have the same canonical representative (i.e., $\mathbb{F}_1 = \mathbb{F}_2$).

A cofactor is a generalized signature of a Boolean function that has been used in many Boolean matching algorithms.

Definition 2: (Cofactor Signature): The cofactor signature of *f* with respect to $x_i(\overline{x}_i)$, $|f_{x_i}|(|f_{\overline{x}_i}|)$, is the onset size of $f_{x_i}(f_{\overline{x}_i})$, where $f_{x_i} = f[x_i \leftarrow 1]$ and $f_{\overline{x}_i} = f[x_i \leftarrow 0]$.

The cofactor signature of f with respect to a cube b , $|f_b|$, is the onset size of f_b , where $f_b = f[b \leftarrow 1]$. If the cube *b* has *k* variables, $|f_b|$ is denoted as the kth-order signature [1].

A Boolean function may have some independent variables; however, the value of a Boolean function is irrelevant to these independent variables. The negative cofactor and positive cofactor of a variable are used to check its independence.

Definition 3: (Boolean Difference Signature): The Boolean difference signature of *f* with respect to x_i , $|f'_x|$ x_i ['], is the onset size of f'_x $f_{x_i}^{y'}$, where $f_{x_i}^{y'} = f_{x_i} \oplus f_{\overline{x}_i}$ [26].

A variable x_i is said to be an independent variable when it satisfies $|f'_{x_i}| = 0$. The independence property of an independent variable does not changed under NP transformation.

Definition 4: (Variable Symmetry): The variables *xⁱ* and $x_i(\overline{x}_i)$ of a Boolean function *f* are symmetric if *f* is invariant after swapping x_i and $x_i(\overline{x}_i)$ [26].

Two NP transformations are equal when two or more symmetric variables are permuted. Therefore, symmetry is always used to reduce the search space for Boolean matching.

IV. CANONICAL-BASED BOOLEAN MATCHING

The goal of our algorithm is to find the canonical transformation $\mathcal T$ of a Boolean function *f*, which can transform *f* to $\mathbb F$ that has the maximal DC signature vector. During the search process, we search many candidate canonical transformations by sorting and grouping variables. Then, we compare the DC signature vectors of these candidate canonical transformations to find the target canonical transformation T .

A. THE PROPOSED CANONICAL FORM

The canonical form presented in [1] has the maximal signature vector. For some Boolean functions, many variables have the same cofactor signature and are not symmetric variables. In this case, the algorithm proposed in [1] will generate multiple splitting ways, which increases the search space of canonical transformation. Therefore, to reduce the search space, further methods to distinguish these variables are required.

Although the variables of many Boolean functions have the same cofactor signature, their Boolean difference signatures are the different. Therefore, we proposes DC signature vector that can resolve the shortcoming in [1] described above.

Definition 5: (\prec *):* Let " \prec " denote the lexicographic comparison of two vectors.

*Definition 6: (*1 *st DC Signature Value):* The 1*st* Boolean Difference and Cofactor (DC) signature value of a Boolean function *f* with respect to x_i is a two-tuple ($|f_{x_i}|$, $|f'_x|$ $\int_{x_i}^{\prime} |$.

The 1^{st} DC signature values of the two variables x_i and x_j of a Boolean function *f* are ($|f_{x_i}|$, $|f'_x|$ $(x'_{x_i}|)$ and $(|f_{x_j}|, |f'_x|)$ *xj* |), respectively. When they satisfy one of the two following cases, then $(|f_{x_i}|, |f'_x|)$ f'_{x_i} |) $\prec (|f_{x_j}|, |f'_x)$ $\left(x_i^{\prime}\right)$ and the relation of x_i and x_j is $x_i < x_j$.

$$
1) |f_{x_i}| < |f_{x_j}|
$$

2) $|f_{x_i}| = |f_{x_j}|$ and $|f'_x|$ $|f'_{x_i}| < |f'_{x_i}|$ $\int x_j$

*Definition 7: (*k*th-order DC Signature Value):* The *k*th-order DC signature value of a Boolean function *f* with respect to a cube b, $b = x_{i1} \cdots x_{ik}$, is $(|f_{x_{i1} \cdots x_{ik}}|, |f'_{x_{i1} \cdots x_{ik}}|)$.

Definition 8: (DC Signature Vector): The DC signature vector of an *n*-input Boolean function *f* is

$$
D^f = \{[f], ([f_{x_1}], [f'_{x_1}]), \cdots, ([f_{x_n}], [f'_{x_n}]), ([f_{x_1x_2}], [f'_{x_1x_2}]),\cdots, ([f_{x_{n-1}x_n}], [f'_{x_{n-1}x_n}]), \cdots, ([f_{x_1\cdots x_{n-1}}], [f'_{x_1\cdots x_{n-1}}]),([f_{x_2\cdots x_n}], [f'_{x_2\cdots x_n}]), [f_{x_1\cdots x_n}]\},
$$

which is composed of its *0*th-order signature, 1*st* DC signature values and higher order DC signature values up to the *n*th-order signature.

The authors of [1] proved that each Boolean function has a unique signature vector. Therefore, each Boolean function also has a unique DC signature vector. We need not prove that here.

Definition 9: (The Canonical Representative): The canonical representative of an NPN-equivalent class $EC =$ ${f_1, \dots, f_m}$ is the Boolean function that has the maximal DC signature vector.

Assume that two Boolean functions *f* and *g* have the DC signature vectors D^f and D^g . When comparing D^f and D^g , we compare their 0th-order signature first, then their 1st DC signature values, 2nd-order DC signature values and higher DC signature values until an inequality is encountered. $D^f \prec D^g$ when the corresponding DC signature value of D^f is less than that of D^g . If and only if $D^f \prec D^g$, the order relation of *f* and *g* is $f < g$.

Definition 10: (The Maximal Canonical Transformation): The transformation $C_f = (t_1, t_2, \dots, t_n)$ that has the maximal DC signature vector is the maximal canonical transformation.

In this paper, we use $T = (t_1, t_2, \dots, t_n)$ to denote a candidate canonical transformation searched in a canonized process where $t_i = x_{\pi(i)}^{\varphi(i)}$ $_{\pi(i)}^{\varphi(i)}$. For a candidate canonical transformation T , the DC signature vector is as follows:

$$
D^{T} = \{[f], ([f_{t_1}], [f'_{t_1}]), \cdots, ([f_{t_n}], [f'_{t_n}]), ([f_{t_1t_2}], [f'_{t_1t_2}]),\cdots, ([f_{t_{n-1}t_n}], [f'_{t_{n-1}t_n}]), \cdots, ([f_{t_1\cdots t_{n-1}}], [f'_{t_1\cdots t_{n-1}}]),([f_{t_2\cdots t_n}], [f'_{t_2\cdots t_n}]), [f_{t_1\cdots t_n}]\}
$$

In the canonized process, multiple candidate canonical transformations similar to *T* exist. The maximal canonical transformation C_f is the candidate canonical transformation that has the maximal DC signature vector. Consider two candidate canonical transformations $T1$ and $T2$: $T1 < T2$ when $D^{T1} \prec D^{T2}$.

For an n-input Boolean function *f*, $n!2ⁿ$ NP transformations exist. The canonical transformation T of a Boolean function *f* is the transformation that can transform *f* to $\mathbb F$ when $\mathbb F$ has the maximal DC signature vector in its NPN-equivalent class. We use C_f to express the maximal canonical transformation of Boolean function *f* found in the canonical process. The input phase assignment and reindexing transformation is $X =$ (x_1, \dots, x_n) to $C_f = (t_1, \dots, t_n)$, where $\mathbb{F}(C_f) = f(X)$. The relation between f and $\mathbb F$ can be expressed by an NP transformation \mathcal{T} , where $X = \mathcal{T} C_f$ [1]. Therefore, the canonical form of a Boolean function *f* is $f(\mathcal{T}X)$, and $f(\mathcal{T}X) = f(C_f^{-1}X)$.

The key step of our algorithm is sorting and grouping the variables according to their DC signature values. After each grouping, a group result is obtained. We denote *G* as the group result of *f*, and $G = \{G_1, \dots, G_m\}$, where $m \in \{1, \dots, n\}$. Every group *Gⁱ* has one or more classes, and each class has one or more variables. We use $G_i = \{C_{i1}, \dots, C_{ik}\}\$ denote a group with *k* classes.

In the process of grouping variables, each group may have many classes. We categorize these as asymmetric, symmetric and independent classes.

Definition 11: (Asymmetric Class): Every asymmetric variable in a group is an asymmetric class.

Definition 12: (Symmetric Class): A variable set C_i = ${x_{i1}, \dots, x_{ik}}$ and $k \in \{2, \dots, n\}$ is a symmetric class if any arbitrary two variables in *Cⁱ* are symmetric.

Definition 13: (Independent Class): A variable set C_i = ${x_{i1}, \dots, x_{ik}}$ and $k \in \{1, \dots, n-1\}$ is an independent class if every variable in C_i is an independent variable.

B. COMPUTE CANONICAL FORM

The maximal canonical transformation *C^f* is found after comparing all the candidate canonical transformations. We first try to use the 1*st* DC signature value to group the variables. When the 1*st* DC signature values can not resolve all the groups, we use the 2nd-order DC signature values and higher DC signature values until all the groups are resolved. Then, our algorithm generates a candidate canonical transformation.

In the matching process of two Boolean functions *f* and *g*, the algorithm first judges whether there is an output negation between them. If $|f| = |g| \wedge |f| \neq |\overline{g}|$, the phases of *f* and *g* are positive. There is no output negation and our algorithm computes the canonical form of *f* and *g*. If $|f| = |\overline{g}| \wedge |f| \neq |g|$, the phase of f is positive and the phase of g is negative. There is an output negation and our algorithm computes the canonical form of f and \overline{g} . The matching runtime will be double when $|f| = |\overline{g}| \wedge |f| = |g|$, because our algorithm must try two phases for *f* and *g*.

The phase of a variable is obtained by comparing the size of its positive and negative cofactors. The phase of x_i is positive when $|f_{x_i}| > |f_{\overline{x}_i}|$, and negative when $|f_{x_i}| < |f_{\overline{x}_i}|$. When $|f_{x_i}| = |f_{\overline{x}_i}|$, we need to use a higher signature to determine its phase. If the higher signature cannot determine its phase, we must try both positive and negative.

If variable x_i of a Boolean function f is an independent variable, the phase of x_i is assigned as positive when $|f_{x_i}| = |f_{\overline{x}_i}|$ because x_i is independent to the Boolean function. If *f* has *k* independent variables and their phases cannot be determined by comparing the positive and negative cofactors, we may try two phases; which doubles the search space. Therefore, checking independent variables is a necessary operation.

The purpose of Procedure 1 is to compute the canonical form of a Boolean function *f* . The individual tasks in Procedure 1 are as follows.

1) Compute |*f* |.

2) Compute the 1*st* DC signature values of all variables of f .

3) The 1*st* DC signature value determines the phases of all variables. Variables whose phases are not determined are handled in Procedure 2. The method used is the same as that described in [1].

4) Check the symmetry of all variables.

5) Check the independent variables. If the phases of independent variables are not determined, assign the positive phase to them.

6) Group all variables by comparing their 1*st* DC signature values.

7) Call Procedure 2 to get the maximal canonical transformation C_f .

8) Compute canonical form by the maximal canonical transformation *C^f* .

Example 1: Consider a 7-input Boolean function $f(X) =$ $x_3\overline{x}_5x_7 + \overline{x}_1\overline{x}_2x_4 + \overline{x}_1\overline{x}_2\overline{x}_4x_5\overline{x}_6 + x_3\overline{x}_4x_5\overline{x}_6 + x_2\overline{x}_3\overline{x}_4x_5\overline{x}_6 +$ $\overline{x}_2\overline{x}_3\overline{x}_4x_5\overline{x}_6$, whose phase assignment and group variables are as follows.

Procedure 2 Canonical transformation search process

Input: *f* , *map*_*list*, *group*, *m* **Output:** *Cf* **function** Search(*f* ,*CT* _*list*, *group*, *m*) **if** D_1 **then if** $Empty(C_f)$ **then** $C_f = T$ **else if** $D^{C_f} \prec D^T$ **then** $C_f = T$ **end if else for all** G_i , $i \in (1, \cdots, m)$ **do if** D_2 **then** break **end if end for if** D_3 **then** Add the variables in G_i to CT list SEARCH(*f* ,*CT* _*list*, *group*, *m*) **else for all** $C_{ij}, j \in \{1, \dots, k\}$ **do** Split *Gi* create node_list **end for for all** *node*_*j* in node_list **do** UPDATE_SEQUENCE(*node*_*j*, *group*) $m=m+1$ **if** D_1 **then if** $Empty(C_f)$ **then** $C_f = \check{T}$ **else if** $D^{C_f} \prec D^T$ **then** $C_f = T$ **end if else** Add the variables in G_i to CT_list Update_signature(*f*) $m = \text{group}(f)$ SEARCH(*f* ,*CT* _*list*, *group*, *m*) **end if end for end if end if end function**

Procedure 1 computes $|f| = 46$ and assigns a negative phase to f . Then, it computes the 1^{st} DC signature value of all the variables of f , and the results are $\{(16,28), (16,28),$ (30,28), (22,44), (24,44), (15,32), (30,28)}. The variables *x*3, *x*⁵ and *x*⁷ are positive, and the others are negative.

Check the symmetry of every variable. There are two symmetric class $\{x_1, x_2\}$ and $\{x_3, x_7\}$. Group the variables according to the 1*st* DC signature value. The grouping result is $G = \{G_1, G_2, G_3\}$, and $G_1 = \{C_{11}\} = \{\bar{x}_6\}$, $G_2 =$ ${C_{21}, C_{22} = {\{\overline{x}_1, \overline{x}_2\}, \{x_3, x_7\}\}\$ and $G_3 = {C_{31}, C_{32}} =$ $\{\{\bar{x}_4\}, \{x_5\}\}.$

C. SEARCHING THE CANONICAL TRANSFORMATIONS

After obtaining the initial group results, Procedure 2 begins to search candidate canonical transformations. Procedure 2 first addresses the first group G_1 , then group G_2 , and so on until all groups have been resolved. A group G_i is resolved when

 G_i has only one class. There are three possible cases when group G_i is resolved.

1) Group *Gⁱ* has only one asymmetric variable and the phase of this variable is determined.

2) Group G_i has multiple variables, an arbitrary two variables of G_i are symmetric, and the phases of all variables of *Gⁱ* are determined.

3) Group *Gⁱ* has one or more variables, all of which are independent variables.

Because the Boolean difference signature of an independent variable is 0 and the Boolean difference signature of dependent variable must not be 0, a group contains only independent variables when any independent variable occurs.

When searching candidate canonical formations, all the candidate canonical transformations are stored in a tree and Procedure 2 uses a depth-first search to find candidate canonical transformations. Each branch of the candidate canonical transformation tree is a candidate canonical transformation. In Procedure 2, CT_list is the candidate canonical transformation tree, which is initialized to NULL. *T* is the candidate canonical transformation found in the search process, and C_f is the maximal canonical transformation, which is initialized empty.

Let $G = \{G_1, G_2, \dots, G_m\}$ be the grouping results that Procedure 1 obtained using the 1*st* DC signature values. The first step in Procedure 2 is to check whether all groups are resolved. Here we use the condition D_1 to denote whether all groups are resolved.

When D_1 is true, Procedure 2 finds a candidate canonical transformation *T* . When this occurs, Procedure 2 assigns *T* to C_f if C_f is empty or $D^{C_f} \prec D^T$. Then, Procedure 2 continues to search for other candidate canonical transformations until all the candidate canonical transformations have been searched.

When D_1 is false, some groups could not be resolved. At this point, Procedure 2 searches the group G_i which is an unresolved group that has the minimal sequence number. Condition D_2 becomes true when an unresolved group is found.

When group *Gⁱ* (the unresolved group having the minimal sequence number) is found, it is handled in two ways.

1) Group G_i can be resolved.

The condition D_3 denotes whether group G_i can be resolved. When D_3 is true, Procedure 2 adds all the variables in G_i to the candidate canonical transformation tree and calls itself recursively.

In Example 1, the condition D_1 is false. Procedure 2 finds that group G_1 is the first unresolved group and that it can be resolved in this call. Procedure 2 adds variable \bar{x}_6 to the candidate canonical transformation tree, giving a new layer with a node. Then, Procedure 2 calls itself recursively.

2) Group G_i cannot be resolved.

When condition D_3 is false, Procedure 2 splits group G_i . Group *Gⁱ* has *k* classes; thus, there are *k* approaches for splitting it. Let group $G_i = \{C_{i1}, \dots, C_{ik}\}$, select one class as group G_i ; the other classes are in group G_{i+1} . The first

splitting approach is $P_1 = \{G_i, G_{i+1}\} = \{(C_{i1}), (C_{i2},$ \cdots , C_{ik} }, the second splitting approach is P_2 ${G_i, G_{i+1}} = {C_i_2}, {C_i_1, C_i_3, \cdots, C_{ik}}$, and the *k*th splitting approach is $P_k = \{G_i, G_{i+1}\} = \{\{C_{ik}\}, \{C_{i1}, \cdots, C_{i+1}\}\}$ $C_{i(k-1)}$ }. Note when the phases of the variables of group G_i are not determined, there are 2*k* splitting approaches, because we need to try the positive and the negative.

Suppose there are *k* splitting approaches. These *k* splitting approaches are stored in the variable node_list. Then, Procedure 2 selects one splitting approach from the variable node_list in order. Let Procedure 2 selects the *q*th splitting approach $P_q = \{G_i, G_{i+1}\}\$ $\{ \{C_{iq}\}, \{C_{i1}, \cdots, C_{i(q-1)}, C_{i(q+1)}, \cdots, C_{ik} \} \}$, Procedure 2 calls UPDATE_SEQUENCE() to update the sequence number of the groups from G_i to G_m . the group sequence number of the variables of *Ciq* remains unchanged and the group sequence number of the variables of $\{C_{i1}, \cdots, C_{i(q-1)}\}$, $C_{i(q+1)}, \cdots, C_{ik}$ is incremented by one. Finally, the group sequence number of the variables of original group G_{i+1}, \cdots, G_m is incremented by one, and the value of *m* is incremented by one.

Then, Procedure 2 checks the condition D_1 . When condition D_1 is true, Procedure 2 creates a candidate canonical transformation *T* and compares the DC signature vector of *T* and C_f . When condition D_1 is false, the *q*th branch is added to the $|C_{iq}|$ layers as one layer with one node. In other words, $|C_{iq}|$ expresses the number of variables in C_{iq} . Procedure 2 calls Update_signature() to update the DC signature value of *f* and regroups the variables of G_{i+1}, \cdots, G_m , and calls itself recursively.

Procedure 2 starts with the first splitting approach and then processes the next splitting approach until all splitting approaches have been processed.

In Example 1, after handling group G_1 , group G_2 is the next unresolved group with the minimal sequence number. Group *G*² has two symmetric classes; therefore, it can not be resolved. There are two ways to split group G_2 : P_1 = ${G_2, G_3} = {\{\overline{x}_1, \overline{x}_2\}, \{x_3, x_7\}}$ and $P_2 = {G_2, G_3} =$ $\{\{x_3, x_7\}, \{\overline{x}_1, \overline{x}_2\}\}\$. The first branch added to the candidate canonical transformation tree has two layers with the nodes \bar{x}_1 and \bar{x}_2 . The second branch added to the candidate canonical transformation tree has two layers with the nodes x_3 and x_7 .

Procedure 2 selects one splitting approach in sequence and updates the DC signature values. The method for updating the DC signature is similar to that used in [1] to update signatures. In Example 1, Procedure 2 uses variable \bar{x}_6 to compute the 2nd-order DC signature value for the other variables. Thus, there are new DC signature values, and Procedure 2 regroups all the unresolved groups and calls itself recursively.

Our algorithm uses three strategies to reduce the search space.

1) A symmetric class has multiple variables. When we group and sort variables, all the variables of a symmetric class are regarded as a single operational object.

2) Take advantage of the independence of independent variable. When the phase of independent variables cannot

be determined, we assign positive phase to them. If we do not make use of independent variable, a independent class C_{il} is a symmetric class. When the phases of the variables in *Cil* are not determined, the number of candidate canonical transformations is doubled. When there are other symmetric classes having the same signatures to C_{il} , the number of candidate canonical transformations is grow even more.

3) The combination of the cofactor signature and Boolean difference signature can better distinguish variables.

In Example 1, after updating the DC signature using variable \bar{x}_1 , variables \bar{x}_4 and x_5 are also resolved. There are two candidate canonical transformations $T1 = {\overline{x}_6, \overline{x}_1, \overline{x}_2}$ \overline{x}_2 , x_3 , x_7 , \overline{x}_4 , x_5 } and $T2 = {\overline{x}_6, x_3, x_7, \overline{x}_1, \overline{x}_2, \overline{x}_4, x_5}$. The candidate canonical transformation tree of Example 1 is shown in Fig. 1.

FIGURE 1. The candidate canonical transformation tree of Example 1.

For a 7-input Boolean function, there are $7!2^7$ NP transformations, while only two candidate canonical transformations exist after using our algorithm in Example 1. Through the DC signature vector comparison, the maximal canonical transformation C_f is $\{\bar{x}_6, \bar{x}_1, \bar{x}_2, x_3, x_7, \bar{x}_4, x_5\}.$

To demonstrate that our algorithm is superior to the algorithm proposed in [1], we use both algorithms to search the maximal candidate canonical transformations in Example 2.

Example 2: Consider a 6-input Boolean function $f(X) =$ $\overline{x_1}$ *x*₂*x*₃*x*₄ + *x*₁*x*₂*x*₃*x*₄ + *x*₁*x*₂*x*₃*x*₄ + *x*₁*x*₂*x*₃*x*₆ + *x*₁*x*₂*x*₄*x*₅*x*₆ + $x_1\overline{x}_2x_3\overline{x}_6 + x_1x_2\overline{x}_3x_6 + \overline{x}_1\overline{x}_2\overline{x}_3\overline{x}_4\overline{x}_5 + \overline{x}_1\overline{x}_2x_3\overline{x}_4x_6 +$ $\overline{x_1 x_2}$ *x*₃*x*₅*x*₆ + $\overline{x_1 x_2}$ *x*₃*x*₅*x*₆ + $\overline{x_1 x_2}$ *x*₃*x*₄*x*₅*x*₆ + *x*₁*x*₂*x*₃*x*₄*x*₅*x*₆ + $x_1\overline{x}_2\overline{x}_3\overline{x}_4x_5x_6$. We use both the signature vector from [1] and the DC signature vector proposed in this paper to search the candidate canonical transformations.

1) Using the algorithm of [1], the candidate canonical transformation search process is as follows.

The algorithm from $[1]$ computes the 1^{st} signature, checks the symmetry and groups variables. The Boolean function *f* has 32 minterms, i.e., $|f| = 32$. The results of the 1st signature are $|f_{x_1}| = 13$ and $|f_{x_2}| = |f_{x_3}| = |f_{x_4}| = |f_{x_5}| = |f_{x_6}| = 16$. There are no symmetric classes. The phase of variable *x*¹ is negative and the phases of the other variables are not determined. According to the 1*st* signatures, the grouping result is $G = \{G_1, G_2\} = \{\{x_1\}, \{x_2, x_3, x_4, x_5, x_6\}\}.$

The first group G_1 can be resolved, and the variable \overline{x}_1 is used to compute the 2nd-order signature of the variables in G_2 . The results are $|f_{\overline{x}_1 x_2}| = 9$ and $|f_{\overline{x}_1 x_3}| =$ $|f_{\overline{x}_1 x_4}| |f_{\overline{x}_1 x_5}| |f_{\overline{x}_1 x_6}| = 10$. We know that $|f_{\overline{x}_1}| = |f| |f_{x_1}| = 19$; therefore, we can determine the phases of these 5 variables. Variable x_2 is negative and the others are positive. But G_2 cannot be resolved: these 5 variables have the same 2nd-order signature. Group *G*² has *5* asymmetric classes, the algorithm of $[1]$ splits G_2 and there are five ways to split it.

- $P_1 = \{G_2, G_3\} = \{\{x_2\}, \{x_3, x_4, x_5, x_6\}\}\$
- $P_2 = \{G_2, G_3\} = \{\{x_3\}, \{x_2, x_4, x_5, x_6\}\}\$
- $P_3 = \{G_2, G_3\} = \{\{x_4\}, \{x_2, x_3, x_5, x_6\}\}\$
- $P_4 = \{G_2, G_3\} = \{\{x_5\}, \{x_2, x_3, x_4, x_6\}\}\$
- $P_5 = \{G_2, G_3\} = \{\{x_6\}, \{x_2, x_3, x_4, x_5\}\}\$

The algorithm of [1] traverses each possible split shown above. We select the first split approach as an example. The updated groups are $G_1 = \{x_1\}$, $G_2 = \{x_2\}$ and $G_3 =$ {*x*3, *x*4, *x*5, *x*6}.

Group G_2 can be resolved; therefore, we use variable \bar{x}_2 to compute 2nd-order signature and group variables. The 2nd-order signatures of the unresolved variables are $|f_{\overline{x}_2 x_3}|$ = $|f_{\overline{x}_2 x_4}| = |f_{\overline{x}_2 x_5}| = |f_{\overline{x}_2 x_6}| = 8$. From these results, we also know that group G_3 also cannot be resolved. There are four ways to split it as follows.

- $P_1 = \{G_3, G_4\} = \{\{x_3\}, \{x_4, x_5, x_6\}\}\$
- $P_2 = \{G_3, G_4\} = \{\{x_4\}, \{x_3, x_5, x_6\}\}\$
- $P_3 = \{G_3, G_4\} = \{\{x_5\}, \{x_3, x_4, x_6\}\}\$
- $P_4 = \{G_3, G_4\} = \{\{x_6\}, \{x_3, x_4, x_5\}\}\$

Continuing, the algorithm of [1] computes its signature and splits the group. Because variables x_2 , x_3 , x_4 , x_5 and x_6 always have the same 2nd-order signature, the following computations will generate a total of $5 \times 4 \times 3 \times 2$ candidate canonical transformations.

2) Using the algorithm proposed by this paper, the candidate canonical transformation search process is as follows.

Our algorithm computes the 1*st* DC signature value of each variable, checks the symmetry, and searches the independent variables and groups variables by the 1*st* DC signature value.

Boolean function *f* has no symmetry classes or independent classes. The 1st DC signature values are $(|f_{x_1}|, |f'_x|)$ $\binom{y'}{x_1}$ $\big) =$ $(13, 64), (|f_{x_2}|, |f'_x)$ y'_{x_2} |) = (16, 36), ($|f_{x_3}|$, $|f'_x|$ (x'_{x_3}) = (16, 52), $(|f_{x_4}|, |f'_x)|$ $\binom{x'}{x_4}$ |) = (16, 20), ($|f_{x_5}|$, $|f'_x|$ $\int_{x_5}^{x'} |$ = (16, 12) and $(|f_{x_6}|, |f'_x)|$ $x'_{(x_6)}$ |) = (16, 28).

Because $|f| = 32$, we can determine that the phase of variable x_1 is negative and the phases of others are not

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determined. Our algorithm groups the variables and generates 6 groups: $G_1 = \{x_1\}, G_2 = \{x_3\}, G_3 = \{x_2\}, G_4 = \{x_6\},$ $G_5 = \{x_4\}$ and $G_6 = \{x_5\}.$

Group *G*¹ can be resolved. The other groups are not resolved because the phases of the variables in these groups have not been determined. Our algorithm uses the variable \bar{x}_1 to compute the 2nd-order DC signature values, which are: $(|f_{\overline{x}_1x_2}|, |f_{\overline{x}_1x_2}|') = (9, 18), (|f_{\overline{x}_1x_3}|, |f_{\overline{x}_1x_3}|') = (10, 26),$ $(|f_{\overline{x}_1x_4}|, |f_{\overline{x}_1x_4}|) = (10, 10), (|f_{\overline{x}_1x_5}|, |f_{\overline{x}_1x_5}|) = (10, 6),$ and $(|f_{\overline{x}_1x_6}|, |f_{\overline{x}_1x_6}|') = (10, 14).$

From the above 2nd-order DC signature values, the phases of all variables are determined. Therefore, groups *G*2, *G*3, *G*4, *G*⁵ and *G*⁶ are all resolved. One candidate canonical transformation is generated: $\{\overline{x}_1, x_3, \overline{x}_2, x_6, x_4, x_5\}.$

Because $|f| = |\overline{f}|$, the algorithm of [1] and ours must both perform the same search for *f* . However, the Boolean function *f* results in 240 candidate canonical transformations when computed by the algorithm of [1] but only 2 when computed by the algorithm proposed in this paper.

Using our algorithm, the NPN Boolean matching algorithm is denoted as follows: Given two Boolean function $f(X)$ and $g(X)$, Boolean function *f* is NPN-equivalent to *g* if the canonical form $\mathbb F$ of f is equal to that of g.

V. EXPERIMENTAL RESULTS

We reimplemented the algorithm of [1] to compare it with the algorithm of this paper and obtain supporting experimental evidence. The two algorithms were tested on a set of randomly generated circuits and a set of MCNC benchmark circuits. The two circuit sets include a number of NPN-equivalent circuits. We tested the NPN matching runtime and the number of candidate canonical transformations generated when searching for the canonical transformation of a Boolean function. The runtime is measured in seconds. The following experimental results were obtained on a computer equipped with 3.3 GHz CPU and 4GB RAM.

TABLE 1. Boolean matching results on the random circuits.

#I	#A.T of $[1]$	# A.C.N of [1]	# A.T of ours	$\overline{\text{# A.C.N}}$ of ours
7	0.0007	13.2	0.0005	7.3
8	0.0009	22.1	0.0004	5.3
9	0.0010	16.8	0.0007	6.1
10	0.0013	19.6	0.0004	3.6
11	0.0032	19.1	0.0020	4.3
12	0.0068	12.5	0.0048	4.5
13	0.0099	17.2	0.0043	3.7
14	0.0097	14.8	0.0062	4.2
15	0.0078	16.5	0.0044	4.2
16	0.0249	13.9	0.0135	5.0
17	0.0326	23.4	0.0208	5.9
18	0.0423	13.3	0.0261	5.0
19	0.1565	15.8	0.1070	5.8
20	0.1195	12.5	0.0974	5.6
21	0.0859	13.3	0.0708	4.9
22	0.1349	18.3	0.1186	6.8

Tables 1 and 2 show the experimental results on the random circuit and MCNC benchmark circuit sets, respectively.

¹A.T: Average runtime, A.C.N: Average number of candidate canonical transformations

TABLE 2. Boolean matching results on the MCNC benchmark circuits.

#I	#A.T of $[1]$	# A.C.N of [1]	# A.T of ours	# A.C.N of ours
7	0.00215	66.7	0.00037	2.4
8	0.00989	27.1	0.00939	26.3
9	0.00163	3.0	0.00169	2.8
10	0.00098	2.1	0.00103	2.1
11	0.00283	3.2	0.00211	2.3
12	0.00177	2.7	0.00173	2.2
13	0.04242	7.8	0.02971	6.5
14	0.00713	2.1	0.00784	2.0
15	0.02730	2.4	0.02818	2.4
16	0.00238	2.9	0.00246	2.4
17	0.18872	2.8	0.19304	2.5
18	0.21707	4.4	0.21725	2.4
19	1.13960	3.4	1.04902	2.1
20	1.90159	3.8	2.07058	3.7
21	7.1248	2.0	6.6848	1.8
22	13.9547	4.5	14.3204	4.0

FIGURE 2. The comparison results on the equivalent random circuits.

In these two tables, the first column shows the number of inputs (#I), the second and third columns show the average matching runtime (#A.T) and the average number of candidate canonical transformations (#A.C.T) of the algorithm in [1], respectively, and the forth and fifth columns show the average matching runtime and the average number of candidate canonical transformations of our algorithm, respectively.

The experimental results listed in Table 1 show that our algorithm improves the speed of the Boolean matching process and reduces the search space effectively. In the best case, our algorithm's runtime is 70% faster and its search space is 82% smaller than that of the algorithm from [1]. On average, our algorithm improves the runtime by 37% and reduces the search space by 67%. Fig. 2 shows the results on the equivalent random circuits.

Form Table 2, we can see that the average runtime improves by 83% and the search space is reduced by 96%

compared with [1] when the number of input variables is 7. However, neither the runtime nor the search space are drastically improved in the other input cases. In majority of the circuits of the MCNC benchmark, using only cofactor signature serves to quickly identify the variables. Therefore, the average runtime of the algorithm of [1] when tested on 22 inputs is slightly faster than ours. Despite this, our algorithm improves the average runtime by 6% and reduces the average search space by 19% compared with that of [1] when tested on the MCNC benchmark circuits.

VI. CONCLUSIONS

In this paper, we propose a canonical-based Boolean matching algorithm. The proposed DC signature vector is more effective in computing canonical form than the algorithm of [1]. We test the runtime and the canonical transformation search space of Boolean matching algorithms with 7-22 inputs. The experimental results show that the average runtime of our algorithm is 37% faster and its search space is 67% smaller than those of the algorithm proposed in [1]. The algorithm proposed in this paper is highly effective at reducing the search space and enhancing the Boolean matching speed. In future work, we plan to extend this Boolean matching approach to Boolean matching with don't care set and multiple-output Boolean matching.

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