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# APPLIED RESEARCH

# A GPU-Based Ising Machine With a Multi-Spin-Flip Capability for Constrained Combinatorial Optimization

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**ABSTRACT** Ising machines are domain-specific computers that solve combinatorial optimization problems (COPs). They utilize an Ising model to represent a COP and search for the optimal spin configuration of the Ising model to solve the COP. Most Ising machines are based on simulated annealing (SA) and update the spin configuration according to single-spin-flip Markov Chain Monte Carlo methods. However, supporting a multi-spin flip is important to enhance the search performance of SA-based Ising machines when constrained COPs are solved. In this paper, we extend the merge method, which was introduced to provide a multi-spin-flip capability for SA-based Ising machines, and formulate it as a series of matrix multiplications that can be executed on a graphics processing unit (GPU) efficiently. We then construct a GPU-based Ising machine that implements the GPU-oriented merge method together with an extended SA algorithm. We finally demonstrate its superiority over the state-of-the-art GPU-based Ising machine for quadratic knapsack problems.

**INDEX TERMS** Constrained combinatorial optimization, graphics processing unit (GPU), Ising machine, Ising model, multi-spin flip, simulated annealing (SA).

# I. INTRODUCTION

Ising machines [1], [2], [3], [4], [5], [6], [7], [8], [9], [10], [11], [12], [13], [14] have attracted attention in various industry fields as efficient solvers for combinatorial optimization problems (COPs). They utilize an Ising model to represent COPs [15], [16], [17]. The Ising model has a system energy called Hamiltonian as a function of binary variables called spins; each spin takes either +1 or -1. Solving a COP corresponds to finding the ground state (i.e., the optimal spin configuration that minimizes the system energy).

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Various Ising machines [3], [4], [5], [6], [7], [8], [9], [10], [11] have been developed so far. Most of them are based on simulated annealing (SA) that updates the spin configuration according to single-spin-flip Markov Chain Monte Carlo (MCMC) methods; i.e, a next candidate spin configuration is generated by a single-spin-flip operation that changes a spin value from +1 to -1 or from -1 to +1. SA-based Ising machines implement the single-spin-flip operations on application specific integrated circuit (ASIC) [3], [9] or graphics processing unit (GPU) [11] to accelerate the groundstate search.

However, the single-spin-flip operations are not suitable for solving constrained COPs such as knapsack problem, traveling salesman problem, etc. When a constrained COP is converted into an Ising model, the Hamiltonian is constructed such that the feasible solutions (FSs), i.e., the solutions satisfying the constraints, have lower energy than the infeasible solutions (IFSs). Since a multi-spin flip is always necessary to make transitions among FSs, passing IFSs in the search process is inevitable as long as singlespin-flip MCMC methods are adopted. Nevertheless, making a transition to an IFS causes significant energy increase, and hence is difficult to be accepted in the latter half of annealing. Therefore, enabling direct transitions among FSs without passing IFSs should be a mandatory requirement to enhance the search performance of SA-based Ising machines.

Some studies have introduced a multi-spin-flip operation into Ising-machine hardware; such Ising machines are classified into two types. The first type [10] updates a specified set of spins simultaneously and induces a transition within the subspace of FSs. However, introducing this mechanism into Ising-machine hardware makes it specialized for some COPs, and hence limits the applicability. The second type [4], [6] updates the spin configuration according to multi-spinflip MCMC methods. However, this type of Ising machines is difficult to induce a direct transition between two FSs since the methods decide to flip each spin according to the energy difference caused by its single flip. For these reasons, it is challenging and important to develop an Ising machine with a multi-spin-flip capability that meets the three requirements below:

- (i) The Ising machine does not require any operations specialized for limited COPs.
- (ii) The Ising machine decides to flip multiple spins according to the energy difference caused by the multispin flip.
- (iii) The Ising machine is efficiently realized on parallel computing platforms such as ASIC or GPU.

In recent years, the merge method [18] was proposed to provide a multi-spin-flip capability for SA-based Ising machines. It can engineer a multi-spin flip using single-spinflip MCMC methods by temporarily generating Hamiltonian H' that is deformed from the original Hamiltonian H. For example, when a single spin i in H' describes multiple spins 1, 2, and 3 in H, a single-spin flip of spin i is equivalent to the three-spin flip; i.e., their spin values  $\sigma_1$ ,  $\sigma_2$ , and  $\sigma_3$  are simultaneously changed to  $-\sigma_1$ ,  $-\sigma_2$ , and  $-\sigma_3$ , respectively. Note that the merge method meets requirements (i) and (ii) since H' keeps the Ising-model format and the single-spin flip in H' is evaluated by the energy difference of the corresponding multi-spin flip in H. However, requirement (iii) is not met since it sequentially constructs H', and thus is unsuitable for the implementation onto parallel computing platforms. Furthermore, the previous work implemented this method on a CPU outside an Ising machine, which incurs a large computation cost for data transfer between the CPU and the Ising machine. For truly evaluating the benefit of the merge method, integrating it with Ising-machine hardware is necessary.

This paper proposes a GPU-based Ising machine with a GPU-oriented merge method that meets all the requirements (i), (ii), and (iii). GPU is a powerful computing platform to realize Ising machines [6], [11], [12], [13]. The main contributions of this paper are summarized as follows:

- We propose a GPU-oriented merge method. Its main body is a series of matrix multiplications, which can be executed on a GPU efficiently. In the proposed merge method, we newly introduce a merging matrix that gives the relation between *H* and *H'*.
- We extend the proposed merge method for replica parallel execution. The merging matrix is decomposed into two matrices: one is shared among all replicas and the other is a diagonal matrix (i.e., unnecessary to store the off-diagonal elements). As a result, we can save the memory to store multiple merging matrices.
- We develop a GPU-based Ising machine that integrates the proposed merge method into an extended SA algorithm. The experimental results for quadratic knapsack problems (QKPs) demonstrate its superiority over the state-of-the-art GPU-based Ising machine.

The rest of this paper is organized as follows. In Section II, we review the merge method. In Section III, we first show an obstacle to implement the merge method on a GPU and then propose a GPU-oriented merge method. The proposed merge method is further extended in Section IV for replica parallel execution on a GPU. In Section V, we show the experimental results for QKPs and verify the effectiveness of our GPU-based Ising machine with the proposed merge method. This paper is finally wrapped up in Section VI.

#### II. BACKGROUND KNOWLEDGE

This section introduces SA-based Ising machines utilized for solving COPs. Then, we review the merge method to improve the search performance of SA-based Ising machines.

#### A. SA-BASED ISING MACHINE

When a COP is solved on an Ising machine, the Ising model provides a unified way to represent the problem. The Hamiltonian *H* for an *N*-spin Ising model is defined as a function of the spin configuration  $\boldsymbol{\sigma} = \{\sigma_i | \sigma_i \in \{+1, -1\}\}_{i=1}^N$ :

$$H(\boldsymbol{\sigma}) = -\frac{1}{2} \sum_{i \neq j} J_{ij} \sigma_i \sigma_j - \sum_i h_i \sigma_i, \qquad (1)$$

where  $J_{ij} \in \mathbb{R}$  and  $h_i \in \mathbb{R}$  are the interaction between spins *i* and *j* and the bias on spin *i*, respectively. The interactions are symmetric (i.e.,  $J_{ij} = J_{ji}$ ), and the diagonal elements are set to 0 (i.e.,  $J_{ii} = 0$ ). A set of the interactions  $J \in \mathbb{R}^{N \times N}$  and the biases  $h \in \mathbb{R}^N$  represents a COP. The spin configuration  $\sigma$  that makes the system energy (i.e., the value of *H*) minimum corresponds to the optimal solution of the COP. Efficient Ising-model formulations of COPs have been discussed in [15] and [16].

SA-based Ising machines [3], [9], [11] iteratively update the spin configuration according to single-spin-flip MCMC methods such that the next configuration is generated by flipping one spin. In the search process, the spin-flip probability is controlled with a hyperparameter T (> 0) called pseudo temperature; the value of T is initially set to be large for aggressively flipping spins and is gradually decreased for finally fixing the spin configuration. The finite temperature helps the spin configuration escape from local optimal solutions. However, a multi-spin flip capability is required to efficiently solve constrained COPs on SA-based Ising machines, as explained in Section I.

# **B. MERGE METHOD**

# 1) OVERVIEW

The merge method [18] engineers a multi-spin flip on SA-based Ising machines by deforming the Hamiltonian H. In this method, one or more spins are merged into another spin to describe multiple spins using a single spin. When spin *i* is merged into spin *j*, a flip of spin *i* is synchronized with a flip of spin *j*, resulting in the two-spin flip. Here, spin *i* is called a merged spin, and spin *j* is called the destination of spin *i*; a spin not merged into another spin is called an unmerged spin. The deformed Hamiltonian H' keeps the Ising-model format (i.e., (1)) and has different interactions J' and biases h' from the original Hamiltonian H (see the specific form of J' and h' in (3) and (4)):

$$H'(\boldsymbol{\sigma}') = -\frac{1}{2} \sum_{i \neq j} J'_{ij} \sigma_i \sigma_j - \sum_i h'_i \sigma_i, \qquad (2)$$

where  $\sigma'$  is a subset of  $\sigma$  and consists of unmerged spins only. Utilizing the mechanism where a single-spin flip in H'is equivalent to a multi-spin flip in H, the merge method potentially enables direct transitions among FSs.

Fig. 1 shows the annealing computation flow with the merge method. In the main loop on an Ising machine, each spin is sequentially and iteratively updated according to the flip probability calculated from the local field and the pseudo temperature. The merge method integrates merge process and demerge process into this computation flow as a sub-loop. Here, the number of iterations of the sub-loop is denoted by merge interval  $I_{\rm M}$ . First, in the merge process, a deformed Hamiltonian H' is generated from H and  $\sigma$ . Then, spins in  $\sigma'$  are updated on the Ising machine using the deformed Hamiltonian H'. Finally, in the demerge process, the complete spin configuration  $\sigma$  is recovered from the output spin configuration  $\sigma'$ .

The merge process generates two vectors  $\mathbf{m} = \{m_i\}_{i=1}^N$  and  $\mathbf{s} = \{s_i\}_{i=1}^N$  to construct the deformed Hamiltonian H'.  $m_i$  takes a value from the set of integers  $\{1, 2, \dots, N\}$  so that  $m_{m_i} = m_i$  is satisfied. Spin *i* is an unmerged spin if  $m_i = i$  and is a merged spin if  $m_i = j$  ( $j \neq i$ ); in the latter case, spin *j* is the destination of spin *i*.  $s_i$  takes a value of +1 or -1 and is given by  $s_i = \sigma_i \sigma_{m_i}$ . When spin *i* is merged into spin *j*,  $s_i$  takes +1 if  $\sigma_i = \sigma_j$ , otherwise  $s_i$  takes -1. On the other hand,





**FIGURE 1.** Annealing computation flow w/ and w/o the merge method [18]. Up arrow in open circle and down arrow in filled circle indicate the spin values of +1 and -1, respectively.

Algorithm 1 Merge Process **Input:**  $\sigma$ , J, h,  $P_{merge}$ Output:  $m, s, \sigma', J', h'$ 1:  $\sigma' \leftarrow \sigma, J' \leftarrow J$ , and  $h' \leftarrow h$ 2:  $m_i \leftarrow i$  and  $s_i \leftarrow 1$  for 1 < i < N3: for i = 1 to N do 4: Generate a random number rand  $\in [0, 1)$ if rand  $< P_{merge}$  then 5: Randomly select an unmerged spin j ( $j \neq i$ ) 6: for 1 < j < N $m_i \leftarrow j \text{ and } s_i \leftarrow \sigma_i \sigma_j$ 7:  $\boldsymbol{\sigma}' \leftarrow \boldsymbol{\sigma}' \setminus \{\sigma_i\}$ 8:  $J' \leftarrow$  update\_interactions $(J', i, j, s_i)$ 9:  $\mathbf{h}' \leftarrow \text{update\_biases}(\mathbf{h}', i, j, s_i)$ 10: 11: end if end for 12: for i = 1 to N do 13: while  $m_{m_i} \neq m_i$  do 14: 15:  $s_i \leftarrow s_{m_i} s_i$  $m_i \leftarrow m_{m_i}$ 16: 17: end while 18: end for

when spin *i* is an unmerged spin,  $s_i$  always takes +1. Then, the interactions and the biases of H' are respectively given as:

$$J'_{ij} = \begin{cases} \sum_{k=1}^{N} \sum_{l=1}^{N} s_k s_l J_{kl} \delta_{m_k, i} \delta_{m_l, j} : & i \neq j \\ 0 : & i = j, \end{cases}$$
(3)

$$h_i' = \sum_{k=1}^N s_k h_k \delta_{m_k,i},\tag{4}$$

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**FIGURE 2.** A demonstration of the merge process using a 5-spin Ising model with a spin configuration  $\{\sigma_1, \sigma_2, \sigma_3, \sigma_4, \sigma_5\} = \{-1, -1, -1, +1, +1\}$ . In this example, spin 1 is merged into spin 4, and then spin 4 is merged into spin 5.

where  $\delta_{i,j}$  denotes the Kronecker delta. The demerge process recovers  $\boldsymbol{\sigma}$  by  $\sigma_i = s_i \sigma_{m_i}$  for  $1 \le i \le N$ .

# 2) ALGORITHM OF MERGE PROCESS

Algorithm 1 shows a merge process to assign  $m_i$  and  $s_i$  from the current spin configuration  $\sigma$ . It sequentially attempts to merge spin *i* into a randomly selected unmerged spin *j* with probability  $P_{\text{merge}}$  (lines 3–12). Here,  $P_{\text{merge}}$  is a hyperparameter called merge probability to control the number of merged spins. The interactions connected to spin *j* and the bias on spin *j* are updated when spin *i* is merged into spin *j* (lines 9–10). After completing the sequential merge attempt for all spins,  $s_i$  and  $m_i$  are updated for merged spins to satisfy  $m_{m_i} = m_i$  (lines 13–18).

Algorithm 2 shows the update functions of J' and h' in Algorithm 1. For a given merged spin *i* and its destination spin *j*, the interactions connected to spin *j* and the bias on spin *j* are updated in lines 4–5 and 12, respectively. Then, the interactions connected to spin *i* and the bias on spin *i* are set to 0 in lines 7 and 13, respectively, since they do not exist in the deformed Hamiltonian H'.

Fig. 2 demonstrates **Algorithm 1** when a 5-spin Ising model with  $\{\sigma_1, \sigma_2, \sigma_3, \sigma_4, \sigma_5\} = \{-1, -1, -1, +1, +1\}$  is given. In this example, spin 1 is merged into spin 4, and then spin 4 is merged into spin 5. When spin 1 is merged into spin 4, we obtain  $m_1 = 4$  and  $s_1 = \sigma_1 \sigma_4 = -1$ . After that, the interactions connected to spin 4 are updated using those connected to spin 1, and the bias on spin 4 is updated using that on spin 1. Spin 4 is merged into spin 5 in the same way;

<b>Algorithm 2</b> Functions to Update $J'$ and $h'$				
: <b>function</b> update_interactions( $J'$ , $i$ , $j$ , $s_i$ )				
2: <b>for</b> $k = 1$ to $N$ <b>do</b>				
3: <b>if</b> $k \neq j$ <b>then</b>				
4: $J'_{ki} \leftarrow J'_{ki} + s_i J'_{ki}$				
5: $J_{ik}^{i'} \leftarrow J_{ik}^{i'} + s_i J_{ik}^{i'}$				
6: end if				
7: $J'_{ki} \leftarrow 0 \text{ and } J'_{ik} \leftarrow 0$				
8: end for				
9: <b>return J</b> '				
10: end function				
11: <b>function</b> update_biases( $\boldsymbol{h}', i, j, s_i$ )				
12: $h'_i \leftarrow h'_i + s_i h'_i$				
13: $\vec{h}_i \leftarrow \vec{0}$				
14: <b>return</b> <i>h</i> ′				
15: end function				

we obtain  $m_4 = 5$  and  $s_4 = \sigma_4 \sigma_5 = +1$ . Finally,  $s_1$  and  $m_1$  are updated to  $s_1 = -1$  and  $m_1 = 5$  to satisfy  $m_{m_i} = m_i$ .

We also demonstrate using Fig. 2 that a single-spin flip in H' is equivalent to a multi-spin flip in H. In this example, the original Hamiltonian H is a function of  $\{\sigma_1, \sigma_2, \sigma_3, \sigma_4, \sigma_5\}$ , and the spin configuration is  $\{-1, -1, -1, +1, +1\}$  (see the leftmost panel). On the other hand, the deformed Hamiltonian H' is a function of  $\{\sigma_2, \sigma_3, \sigma_5\}$ , and the spin configuration is  $\{-1, -1, +1\}$  (see the rightmost panel). When spin 5 in H' is flipped (i.e., the spin value changes from +1 to -1), the system energy changes from  $H'(\{-1, -1, +1\}) = 12$  to

 $H'(\{-1, -1, -1\}) = -12$ ; thus, the energy decreases by -24 as a result of the single-spin flip. In the original Hamiltonian, the energy changes from  $H(\{-1, -1, -1, +1, +1\}) = 18$  to  $H(\{+1, -1, -1, -1, -1\}) = -6$  when spins 1, 4, and 5 are flipped simultaneously; i.e., the single-spin flip in H' decreases the same energy as the three-spin flip in H. In this way, the merge method captures the energy difference caused by a multi-spin flip on SA-based Ising machines.

# **III. GPU-ORIENTED MERGE METHOD**

The previous work [18] has demonstrated that the merge method improves the search performance of SA-based Ising machines when constrained COPs are solved. However, it is necessary to integrate the merge method with Ising-machine hardware for truly evaluating its benefit. In this section, we first discuss the obstacle to implement the merge method onto parallel computing platforms. Then, we propose an extended merge method that can be executed on a GPU efficiently.

#### A. PROBLEM AND MOTIVATION

We need to integrate the merge method with an Ising machine so that its processing time can be comparable to the annealing computation time. Ising machines accelerate the annealing computation by exploring parallel operations. However, the merge method is difficult to accelerate on parallel computing platforms since the merge process (i.e., **Algorithm 1**) is a sequential process; interactions updated in a single merge attempt are dependent on the subsequent attempt, as shown in Fig. 2.

In Algorithm 1, assigning  $m_i$  and  $s_i$  can be separated from updating interactions and biases; i.e., the algorithm can be rewritten as follows:

- 1) Assign  $m_i$  and  $s_i$  for all spins using the spin configuration  $\sigma$  and merge probability  $P_{\text{merge}}$ .
- Generate the interactions J' and the biases h' from the original Hamiltonian (i.e., J and h) using m and s obtained in step 1.

It should be noted that we can execute step 1 in parallel for all spins by first arranging spins into two groups, merged spins and unmerged spins, and then assigning the destination of each merged spin.

However, it is challenging to eliminate the sequential process in step 2. Modern GPUs process matrix multiplications quite efficiently since they have mixed-precision matrix multiplication units called Tensor Cores. Therefore, in the next section, we formulate step 2 as a series of matrix multiplications to execute it on a GPU efficiently.

# **B. FORMULATION**

This section newly presents a theorem to generate a deformed Hamiltonian (i.e., J' and h') with matrix multiplications.

Theorem 1: Set a merging matrix M by (5) such that the k-th column keeps  $m_k$  and  $s_k$  by storing the value  $s_k$  at the



**FIGURE 3.** Change in the interaction  $J'_{ij}$  between two unmerged spins i and j through the merge process. Interactions finally merged into  $J_{ij}$  are classified into three groups: (a)  $\{J_{kj} | k \in X_i\}$  (vellow edges), (b)  $\{J_{ij} | l \in X_j\}$  (blue edges), and (c)  $\{J_{kl} | k \in X_i, l \in X_j\}$  (red edges). The interactions  $\{J_{kk'} | k, k' \in (X_i \cup \{i\}), k \neq k'\}$  and  $\{J_{ll'} | l, l' \in (X_j \cup \{j\}), l \neq l'\}$  (gray edges) are left out in the merge process.

 $m_k$ -th row and padding 0 for the remaining rows.

$$\boldsymbol{M} = \{M_{ik}\}^{N \times N}, \ M_{ik} = \begin{cases} s_k : & m_k = i \\ 0 : & Otherwise. \end{cases}$$
(5)

Then,  $J'_{ij} = (\boldsymbol{M} \boldsymbol{J} \boldsymbol{M}^{\mathrm{T}})_{ij}$  for  $i \neq j$  and  $h'_i = (\boldsymbol{h} \boldsymbol{M}^{\mathrm{T}})_i$ .

*Proof 1:* The interaction  $J'_{ij}$  between two unmerged spins *i* and *j* after completing the merge process is first explored. Let  $X_i$  be the set of spins merged into spin *i*; i.e., a spin  $k \in X_i$  satisfies  $m_k = i$ . As illustrated in Fig. 3, interactions merged into  $J_{ij}$  are classified into three groups: (a) *j* and  $k \in X_i$ , (b) *i* and  $l \in X_j$ , and (c)  $k \in X_i$  and  $l \in X_j$ . On the basis of this grouping, the interaction  $J'_{ij}$  in (3) is calculated as follows:

$$J'_{ij} = J_{ij} + \sum_{k \in X_i} s_k J_{kj} + \sum_{l \in X_j} s_l J_{il} + \sum_{k \in X_i} \sum_{l \in X_j} s_k s_l J_{kl}, \quad (6)$$

where the second, third, and fourth terms in the right-hand side correspond to groups (a), (b), and (c), respectively. Also, the bias  $h'_i$  after completing the merge process (i.e., (4)) is calculated as follows:

$$h'_i = h_i + \sum_{k \in X_i} s_k h_k.$$
<sup>(7)</sup>

Then,  $\tilde{J}' := MJM^{T}$  and  $\tilde{h}' := hM^{T}$  must satisfy the following conditions:

- (i)  $\tilde{J}'$  is a symmetric matrix.
- (ii) J̃'<sub>ij</sub> = J'<sub>ij</sub> in (6) holds for unmerged spins *i* and *j*, and J̃'<sub>kj</sub> = 0, J̃'<sub>il</sub> = 0, and J̃'<sub>kl</sub> = 0 hold for merged spins k ∈ X<sub>i</sub> and l ∈ X<sub>j</sub>.
- (iii)  $\tilde{h}'_i = h'_i$  in (7) holds for an unmerged spin *i*, and  $\tilde{h}'_k = 0$  holds for a merged spin  $k \in X_i$ .

As for condition (i), an off-diagonal element  $\tilde{J}'_{ij}$  is transformed into  $\tilde{J}'_{ii}$  as follows:

$$\tilde{J}'_{ij} = (\boldsymbol{M}\boldsymbol{J}\boldsymbol{M}^{\mathrm{T}})_{ij} = \sum_{b=1}^{N} \left(\sum_{a=1}^{N} M_{ia}J_{ab}\right) M_{jb}$$
$$= \sum_{a=1}^{N} \left(\sum_{b=1}^{N} M_{jb}J_{ba}\right) M_{ia} = (\boldsymbol{M}\boldsymbol{J}\boldsymbol{M}^{\mathrm{T}})_{ji} = \tilde{J}'_{ji}.$$
 (8)

The above transformation uses the symmetric property of the original interactions (i.e.,  $J_{ab} = J_{ba}$ ).

As for conditions (ii) and (iii), it is necessary to consider each row of the merging matrix M. The *i*-th row has at least one non-zero element and is given by:

$$M_{ik} = \begin{cases} s_k : k \in X_i \\ 1 : k = i \\ 0 : \text{Otherwise.} \end{cases}$$
(9)

Similarly, the *j*-th row is given by:

$$M_{jl} = \begin{cases} s_l : l \in X_j \\ 1 : l = j \\ 0 : \text{Otherwise.} \end{cases}$$
(10)

From (9) and (10),  $\tilde{J}'_{ij} = (MJM^{T})_{ij}$  and  $\tilde{h}'_{i} = (hM^{T})_{i}$  are respectively transformed as follows:

$$\begin{split} \tilde{J}'_{ij} &= \sum_{b=1}^{N} \left( \sum_{a=1}^{N} M_{ia} J_{ab} \right) M_{jb} \\ &= \sum_{b=1}^{N} \left( J_{ib} + \sum_{k \in X_i} s_k J_{kb} \right) M_{jb} \\ &= \left( J_{ij} + \sum_{k \in X_i} s_k J_{kj} \right) \qquad (11) \\ &+ \sum_{l \in X_j} \left( J_{il} + \sum_{k \in X_i} s_k J_{kl} \right) s_l \\ &= J_{ij} + \sum_{k \in X_i} s_k J_{kj} + \sum_{l \in X_j} s_l J_{il} \\ &+ \sum_{k \in X_i} \sum_{l \in X_j} s_k s_l J_{kl}, \\ \tilde{h}'_i &= \sum_{a=1}^{N} h_a M_{ia} = h_i + \sum_{k \in X_i} s_k h_k. \end{split}$$

On the other hand, all the elements in the k and l-th rows are 0 since  $m_k = i \ (\neq k)$  and  $m_l = j \ (\neq l)$  indicate that no spins are merged into spins  $k \in X_i$  and  $l \in X_j$ .

$$\tilde{J}'_{kj} = \sum_{b=1}^{N} \left( \sum_{a=1}^{N} \underline{M}_{ka} J_{ab} \right) M_{jb}, \qquad (13)$$

$$\tilde{J}'_{il} = \sum_{b=1}^{N} \left( \sum_{a=1}^{N} M_{ia} J_{ab} \right) \underline{M}_{lb}, \tag{14}$$

$$\tilde{J}_{kl}' = \sum_{b=1}^{N} \left( \sum_{a=1}^{N} \underline{M}_{ka} J_{ab} \right) \underline{M}_{lb}, \tag{15}$$

$$\tilde{h}'_k = \sum_{a=1}^N \underline{M_{ka}} h_a. \tag{16}$$

Equations from (13) to (16) show that  $\tilde{J}'_{kj} = 0$ ,  $\tilde{J}'_{ll} = 0$ ,  $\tilde{J}'_{kl} = 0$ , and  $\tilde{h}'_{k} = 0$  hold since all the underlined values are 0. Thus, conditions (ii) and (iii) are satisfied.

Since all the conditions are satisfied,  $J'_{ij} = (\boldsymbol{M} \boldsymbol{J} \boldsymbol{M}^{\mathrm{T}})_{ij}$  for  $i \neq j$  and  $h'_i = (\boldsymbol{h} \boldsymbol{M}^{\mathrm{T}})_i$ .

**Theorem 1** enables generating the interactions J' and the biases h' of the deformed Hamiltonian with matrix multiplications only. In other words, we can efficiently execute step 2 of the two-stage merge process on Tensor Cores.

Now, we revisit the example shown in Fig. 2 and confirm that  $MJM^{T}$  and  $hM^{T}$  give J' and h', respectively. Since spins 1 and 4 are merged into spin 5 in this example, m and s are given by:

$$m = \begin{bmatrix} 5 & 2 & 3 & 5 & 5 \end{bmatrix}, \\ s = \begin{bmatrix} -1 & +1 & +1 & +1 \end{bmatrix}$$

Thus, the merging matrix M is constructed as follows:

$$M = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 \\ 0 & +1 & 0 & 0 & 0 \\ 0 & 0 & +1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 \\ -1 & 0 & 0 & +1 & +1 \end{bmatrix}$$

Note that, as mentioned earlier, the row vectors for unmerged spins (i.e., 2, 3, and 5) have one or more non-zero elements. Using the merging matrix M, we obtain

$$\boldsymbol{M}\boldsymbol{J}\boldsymbol{M}^{\mathrm{T}} = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 2 & 0 & 4 \\ 0 & 2 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & 0 \\ 0 & 4 & 1 & 0 & -12 \end{bmatrix}$$
$$\boldsymbol{h}\boldsymbol{M}^{\mathrm{T}} = \begin{bmatrix} 0 & -3 & 5 & 0 & -7 \end{bmatrix}.$$

The interaction matrix  $MJM^{T}$  is the same as J' in Fig. 2 except for a diagonal element (i.e.,  $(MJM^{T})_{55}$ ). Since the deformed Hamiltonian H' only depends on the off-diagonal elements of J' (see (2)),  $MJM^{T}$  is equivalent to J'. Also, the bias vector  $hM^{T}$  is the same as h' in Fig. 2.

Moreover, a minor modification to **Theorem 1** can correct the diagonal elements to zeros as follows:

Corollary 2:

$$\boldsymbol{J}' = \boldsymbol{M} \boldsymbol{J}^* \boldsymbol{M}^{\mathrm{T}}, \qquad (17)$$

where

$$\boldsymbol{J}^{*} = \{J_{ij}^{*}\}^{N \times N}, \ J_{ij}^{*} = \begin{cases} 0 : & m_{i} = m_{j} \\ J_{ij} : & Otherwise. \end{cases}$$
(18)

*Proof 2:* Equation (11) shows that non-zero values of a diagonal element  $J'_{ii}$  arise due to the two types of interactions; one is those between spins *i* and  $k \in X_i$ , and the other is those between two different spins in  $X_i$ . Note that  $m_k = i$  is only satisfied for spins k = i or  $k \in X_i$ . Thus, we can make  $J'_{ii} = 0$  by preparing a masked interaction matrix  $J^*$  instead of J. More explicitly,

$$J'_{ii} = (MJ^*M^{\mathrm{T}})_{ii}$$
  
=  $\sum_{b=1}^{N} \left( \sum_{a=1}^{N} M_{ia} J^*_{ab} \right) M_{ib}$   
=  $2 \sum_{k \in X_i} s_k J^*_{ki} + \sum_{k \in X_i} \sum_{k' \in X_i} s_k s_{k'} J^*_{kk'}$   
= 0. (19)

As demonstrated in Fig. 3, the above two types of interactions are left out in the merge process. This means that the masked interactions in (18) do not affect the off-diagonal elements of J' calculated by (11), and thus  $J' = MJ^*M^T$ .

Also, the demerge process, i.e.,  $\sigma_i = s_i \sigma_{m_i}$   $(1 \le i \le N)$ , is formulated using the merging matrix M as follows:

*Corollary 3:* Set a spin configuration  $\tilde{\sigma}$  by:

$$\tilde{\boldsymbol{\sigma}} = \{\tilde{\sigma}_i\}^N, \ \tilde{\sigma}_i = \begin{cases} \sigma_i : & m_i = i \\ X : & Otherwise, \end{cases}$$
(20)

where X shows a don't care value. Then, the spin configuration  $\sigma$  is recovered by  $\tilde{\sigma}M$ , i.e.,  $\sigma = \tilde{\sigma}M$ 

*Proof 3*: The *i*-th element of  $\tilde{\sigma}M$  is given by:

$$(\tilde{\boldsymbol{\sigma}}\boldsymbol{M})_i = \sum_{a=1}^N \tilde{\sigma}_a M_{ai}.$$
 (21)

From the definition of M in (5), the *i*-th column of M has only one non-zero element  $s_i$  at the  $m_i$ -th row; i.e.,  $M_{ai} = s_i$  if  $a = m_i$ , otherwise  $M_{ai} = 0$ . Then,

$$(\tilde{\boldsymbol{\sigma}}\boldsymbol{M})_i = s_i \tilde{\sigma}_{m_i}.$$
 (22)

Moreover,  $\tilde{\sigma}_{m_i} = \sigma_{m_i}$  holds because  $m_{m_i} = m_i$ . Thus,  $(\tilde{\sigma}M)_i = s_i \sigma_{m_i} = \sigma_i$ .

# **IV. GPU-BASED ISING MACHINE WITH MERGE METHOD**

This section constructs a GPU-based Ising machine with the merge method proposed in Section III. When an annealing algorithm is implemented on a GPU, preparing replicas is an effective way to enhance the execution efficiency. However, integrating the proposed merge method with the replica parallel execution results in large memory consumption. In this section, we first discuss the memory issue, and then propose a decomposition method for saving the memory.

# A. MEMORY ISSUE

When a GPU-based Ising machine is developed, preparing multiple replicas that search for multiple solutions in parallel is a major strategy to fully utilize computing resources on a GPU [12], [13]. This replica parallel execution reduces the

required time per a single solution. Let *R* be the number of replicas. The spin vector  $\sigma$  is extended to an  $R \times N$  matrix represented by:

$$\boldsymbol{\sigma}^* = \begin{bmatrix} \boldsymbol{\sigma}^{(1)} \\ \boldsymbol{\sigma}^{(2)} \\ \vdots \\ \boldsymbol{\sigma}^{(R)} \end{bmatrix}$$

where  $\sigma^{(r)}$  (r = 1, 2, ..., R) shows the spin configuration of the *r*-th replica.

In this work, we focus on parallel-trial SA (PSA) [9], one of the SA-based annealing algorithms. It inspects all spins in parallel, and then selects one spin from spin-flip candidates. In the replica parallel execution of PSA, the calculation of local fields on  $R \times N$  spins at every Monte Carlo (MC) step is the most computationally expensive part. The local fields  $\hat{h}^{(r)}$  of the *r*-th replica are given by:

$$\hat{\boldsymbol{h}}^{(r)} = \boldsymbol{\sigma}^{(r)} \boldsymbol{J} + \boldsymbol{h}. \tag{23}$$

When the proposed merge method is integrated into PSA with multiple replicas, an individual merging matrix  $M^{(r)}$  is required for every replica; this is because the merging matrix is constructed using the current spin configuration. In other words,  $O(RN^2)$  memory space is required to store merging matrices. As a result, the number of replicas is strictly limited due to the memory capacity of a GPU, making it difficult to enhance the execution efficiency. Therefore, we should take measures to reduce memory consumption.

#### **B. MERGING MATRIX DECOMPOSITION**

One approach for reducing memory consumption is to share some information among replicas. If replicas share the combination of merged spins and their destinations, we can make the vector m common in all replicas. However, even in this case, the vector s must be kept in each replica individually, preventing the merging matrix M from being shared among replicas. In order to resolve this situation, we propose a new theorem to decompose the merging matrix M:

Theorem 4: Set two  $N \times N$  matrices L and D by (24) and (25), respectively.

$$\boldsymbol{L} = \{L_{ik}\}^{N \times N}, \ L_{ik} = \begin{cases} 1 : & m_k = i \\ 0 : & Otherwise, \end{cases}$$
(24)

$$\boldsymbol{D} = \operatorname{diag}(\boldsymbol{s}). \tag{25}$$

Then, M = LD.

*Proof 4:* From (5) and (24),  $M_{ik} = L_{ik}s_k$  holds. Then,

$$(LD)_{ik} = \sum_{a=1}^{N} L_{ia} D_{ak} = L_{ik} D_{kk} = L_{ik} s_k.$$
 (26)

Thus,  $M_{ik} = (LD)_{ik}$ .

**Theorem 4** separates m and s by L and D, respectively. As a result, the matrix L can be shared among all replicas by

introducing the assumption that the combination of merged spins and their destinations is shared among replicas. Here, spin *i* is a merged spin when  $L_{ii} = 0$ , whereas an unmerged spin when  $L_{ii} = 1$ . Although the matrix **D** remains required for individual replicas, we need to store the diagonal elements only. Therefore, the memory space is reduced to  $O(N^2) + O(RN)$  by this matrix decomposition.

# C. MERGE PSA

In this section, we propose an algorithm, Merge PSA, where the merge method is integrated into PSA with multiple replicas. After that, we explain how to implement this algorithm on a GPU.

#### 1) ALGORITHM

In Merge PSA, the local-field calculation of the r-th replica is extended to:

$$\hat{\boldsymbol{h}}^{(r)} = \tilde{\boldsymbol{\sigma}}^{(r)} \boldsymbol{J}^{\prime(r)} + \boldsymbol{h}^{\prime(r)}, \qquad (27)$$

where  $J'^{(r)}$  and  $h'^{(r)}$  are the deformed interaction matrix and bias vector of the *r*-th replica, respectively, and  $\tilde{\sigma}^{(r)}$  is a spin configuration given by (20). Here, it is necessary to use (17) to obtain  $J'^{(r)}$  since the diagonal elements of  $J'^{(r)}$ affect the values in  $\hat{h}^{(r)}$ . This means that we need to calculate the masked interaction matrix  $J^*$  given by (18) in the merge process. Note that  $J^*$  can be calculated as follows:

Corollary 5:

$$\boldsymbol{J}^* = \boldsymbol{J} - \boldsymbol{J} \odot (\boldsymbol{L}^{\mathrm{T}} \boldsymbol{L}), \qquad (28)$$

where  $\odot$  is Hadamard product.

*Proof 5:* An element  $(L^T L)_{ij}$  gives the inner product of the *i*-th and the *j*-th column vectors in L. It becomes 1 only when  $m_i = m_j$ , otherwise it becomes 0:

$$(\boldsymbol{L}^{\mathrm{T}}\boldsymbol{L})_{ij} = \sum_{a=1}^{N} L_{ai}L_{aj} = \begin{cases} 1: & m_i = m_j \\ 0: & \text{Otherwise} \end{cases}$$
(29)

since the *i*-th (*j*-th) column of L has only one element with value 1 at the  $m_i$ -th ( $m_j$ -th) row. Comparing (29) with (18) implies that  $J^* = J - J \odot (L^T L)$ .

As a result, (27) is rewritten as follows:

$$\hat{\boldsymbol{h}}^{(r)} = \tilde{\boldsymbol{\sigma}}^{(r)} \boldsymbol{M}^{(r)} \boldsymbol{J}^* \left( \boldsymbol{M}^{(r)} \right)^{\mathrm{T}} + \boldsymbol{h} \left( \boldsymbol{M}^{(r)} \right)^{\mathrm{T}} = \boldsymbol{\sigma}^{(r)} \boldsymbol{J}^* \left( \boldsymbol{L} \boldsymbol{D}^{(r)} \right)^{\mathrm{T}} + \boldsymbol{h} \left( \boldsymbol{L} \boldsymbol{D}^{(r)} \right)^{\mathrm{T}} = \boldsymbol{\sigma}^{(r)} \boldsymbol{J}^* \boldsymbol{D}^{(r)} \boldsymbol{L}^{\mathrm{T}} + \boldsymbol{h} \boldsymbol{D}^{(r)} \boldsymbol{L}^{\mathrm{T}},$$
(30)

where we have used  $\sigma^{(r)} = \tilde{\sigma}^{(r)} M^{(r)}$  in Corollary 3.

Algorithm 3 shows Merge PSA; the two-stage merge process in Section III-A and the demerge process are introduced into PSA. The merge process first generates L and  $D^{(r)}$  in the first stage (line 3). Then, in the second stage, the merge process generates  $J'^{(r)}$  and  $h'^{(r)}$  to calculate the local fields in (30), which is explicitly given as follows:

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Algorithm 5 Merge PSA for a Single Replica (r)				
<b>Input:</b> $\sigma^{(r)}, J, h, P_{\text{merge}}, S, I_{\text{M}}, T(s)$				
Output: $\sigma^{(r)}$				
1: <b>for</b> $s = 1$ to <i>S</i> <b>do</b>				
2: <b>if</b> $s \mod I_M = 1$ <b>then</b>				
3: $(\boldsymbol{L}, \boldsymbol{D}^{(r)}) \leftarrow \text{generate}(\boldsymbol{\sigma}^{(r)}, P_{\text{merge}})$				
4: $\mathbf{h}^{\prime(r)} \leftarrow \mathbf{h} \mathbf{D}^{(r)} \mathbf{L}^{\mathrm{T}}$				
5: $J^* \leftarrow J - J \odot (L^T L)$				
6: end if				
7: $\hat{\boldsymbol{h}}^{(r)} \leftarrow \boldsymbol{\sigma}^{(r)} \boldsymbol{J}^* \boldsymbol{D}^{(r)} \boldsymbol{L}^{\mathrm{T}} + \boldsymbol{h}^{\prime(r)}$				
8: <b>for</b> $i = 1$ to $N$ <b>do</b>				
9: $p_i \leftarrow \frac{1}{1}$				
$1 + \exp\left(2\hat{h}_i^{(r)}\sigma_i^{(r)}/T(s)\right)$				
10: Generate a random number $rand \in [0, 1)$				
11: $f_i \leftarrow (p_i > \text{rand}) \& (L_{ii} = 1)$				
12: <b>end for</b>				
13: <b>if</b> $\exists i, f_i =$ <b>True then</b>				
14: Randomly select a spin $j$ with $f_j = \text{True}$				
15: <b>for</b> $k = 1$ to $N$ <b>do</b>				
16: $\sigma_k^{(r)} \leftarrow (-2 \times L_{jk} + 1) \times \sigma_k^{(r)}$				
17: end for				
18: end if				
19: end for				

- The second term of (30) is calculated in advance (line 4) since *R* bias vectors (i.e., *h*<sup>'(r)</sup> for 1 ≤ r ≤ *R*) consume only *O*(*RN*) memory space.
- The masked interaction matrix  $J^*$  is calculated in advance (line 5).  $J^*$  is shared among all replicas, and thus consumes only  $O(N^2)$  memory space.

The demerge process is executed with a single-spin-flip operation in PSA (lines 13–18); in lines 15–17, all spins with  $L_{jk} = 1$  (i.e.,  $m_k = j$ ) are flipped, meaning that merged spins  $k \in X_j$  are flipped simultaneously with an unmerged spin *j*. Note that we need to remove merged spins (i.e., spins *k* with  $L_{kk} = 0$ ) from spin-flip candidates in PSA, as in line 11.

Algorithm 4 shows the function to generate L and  $D^{(r)}$  in Algorithm 3. This algorithm arranges all N spins into two groups, merged spins and unmerged spins, in lines 3–8.  $L_{ii}$  is set to 1 for an unmerged spin i (line 6), whereas it remains 0 if spin i is selected as a merged spin. For an unmerged spin i,  $D_{ii}^{(r)} = s_i^{(r)} = +1$ . Then, the destination of each merged spin is assigned in lines 9–14. For a merged spin k (i.e., spin k with  $L_{kk} = 0$ ), an unmerged spin i (i.e., spin i with  $L_{ii} = 1$ ) is randomly selected (line 11) and is set as the destination by  $L_{ik} = 1$  and  $D_{kk}^{(r)} = s_k^{(r)} = \sigma_k^{(r)} \sigma_i^{(r)}$  (line 12).

# 2) GPU IMPLEMENTATION

Algorithm 3 is implemented on a GPU to execute *R* replicas in parallel. This implementation stores the matrix  $D^{(r)}$  in the form of:

$$\boldsymbol{D}^* = \{D_{ri}^*\}^{R \times N}, \ D_{ri}^* = D_{ii}^{(r)}.$$
(31)

#### Algorithm 4 A Function to Generate L and D

1:	<b>function</b> generate( $\sigma^{(r)}, P_{merge}$ )		
2:	$L_{ik} \leftarrow 0 \text{ and } D_{ik}^{(r)} \leftarrow 0 \text{ for } 1 \le i, k \le N$		
3:	for $i = 1$ to N do		
4:	Generate a random number rand $\in [0, 1)$		
5:	if rand $\geq P_{\text{merge}}$ then		
6:	$L_{ii} \leftarrow 1, D_{ii}^{(r)} \leftarrow +1$		
7:	end if		
8:	end for		
9:	for $k = 1$ to N do		
10:	if $L_{kk} = 0$ then		
11:	Randomly select a spin <i>i</i> with $L_{ii} = 1$		
12:	$L_{ik} \leftarrow 1, D_{kk}^{(r)} \leftarrow \sigma_k^{(r)} \sigma_i^{(r)}$		
13:	end if		
14:	end for		
15:	return $L, D^{(r)}$		
16:	end function		



FIGURE 4. Matrix operations in (32).



FIGURE 5. Matrix operations in (33).

Then, as functions of  $\sigma^*$  and  $D^*$ ,  $h'^{(r)}$  in line 4 and  $\hat{h}^{(r)}$  in line 7 are calculated across all replicas as follows:

$$h_i^{\prime(r)} = \left( \left( \boldsymbol{h} \odot \boldsymbol{D}^* \right) \boldsymbol{L}^{\mathrm{T}} \right)_{ri}, \qquad (32)$$

$$\hat{h}_{i}^{(r)} = \left( \left( \left( \boldsymbol{\sigma}^{*} \boldsymbol{J}^{*} \right) \odot \boldsymbol{D}^{*} \right) \boldsymbol{L}^{\mathrm{T}} \right)_{ri} + h_{i}^{\prime(r)}.$$
(33)

Figs. 4 and 5 illustrate the matrix operations in (32) and (33), respectively. In (32), the bias vector  $\boldsymbol{h}$  is broadcast to the  $R \times N$  matrix, and then is multiplied by the matrix  $\boldsymbol{D}^*$  element-wise; this element-wise multiplication corresponds to  $\boldsymbol{h}\boldsymbol{D}^{(r)}$  for  $1 \leq r \leq R$ . After that, the resulting matrix  $\boldsymbol{\sigma}^*$  is multiplied by the matrix  $\boldsymbol{J}^*$ , and then is multiplied by the matrix  $\boldsymbol{\sigma}^*$ 

#### TABLE 1. Specification of eight QKP instances [19] used in this paper.

Instance	# of items	Capacity	The rate of non-zero values
	K	c	$\inf \{p_{ij}   1 \le i < j \le K\}$
r_200_25_1	200	4027	0.25
r_200_50_1	200	3547	0.50
r_200_75_1	200	2866	0.75
r_200_100_1	200	4785	1.0
r_300_25_1	300	376	0.25
r_300_50_1	300	3550	0.50
r_300_50_7	300	328	0.50
r_300_50_10	300	7040	0.50

matrix  $D^*$  element-wise to obtain  $\sigma^{(r)}J^*D^{(r)}$  for  $1 \le r \le R$ . Finally, the resulting matrix is multiplied by the matrix  $L^T$ , and then the deformed bias matrix given by (32) is added to it.

The matrix operations in (32) and (33) are classified into two types; one is matrix multiplication between a  $R \times N$ matrix and a  $N \times N$  matrix, and the other is element-wise multiplication or addition between two  $R \times N$  matrices. The former type has  $O(RN^2)$  computational complexity, being processed on Tensor Cores. On the other hand, the latter type is executed on general-purpose parallel computing units, and its computational complexity is O(RN). Furthermore, line 3 and lines 8–12 in **Algorithm 3** have O(RN) computational complexity, and thus are classified into the latter type.

#### **V. EXPERIMENTAL RESULTS**

In order to evaluate the performance of Merge PSA, we developed GPU-implemented Merge PSA (GiMP) and GPU-implemented PSA (GiP), and solved several QKP instances provided in [19]. In this evaluation, we used CUDA<sup>®</sup> and cuBLAS on NVIDIA<sup>®</sup> TITAN RTX<sup>TM</sup> as a GPU, AMD EPYC<sup>TM</sup>7542 as a CPU, Ubuntu 18.04 LTS as an operating system. Moreover, we solved the same QKP instances using a state-of-the-art GPU-based Ising machine [11] to compare the results with GiMP; in this section, this GPU-based Ising machine is labeled as "Ising machine" for simplicity.

# A. SETUP

#### 1) ISING-MODEL FORMULATION OF QKP

QKP is one of the COPs, which packs items into a knapsack with a capacity c and maximizes the total profit P brought by the packed items. In the K items given in this problem, item i has weight  $w_i$  and profit  $p_{ii}$ . Also, unlike the basic knapsack problem, QKP gives profit  $p_{ij}$  ( $i \neq j$ ) that is added to the total profit P when items i and j are both packed into the knapsack. In this experiment, we selected eight instances in a QKP benchmark suite [19] considering the distribution of three factors (the number of items K, capacity c, and the rate of non-zero values in  $\{p_{ij}|1 \leq i < j \leq K\}$ ) that characterize QKP; their specification is summarized in Table 1.

A QKP is formulated as a quadratic unconstrained binary optimization (QUBO) problem [18]. In this formulation, *K* binary variables  $\mathbf{x} = \{x_i | x_i \in \{1, 0\}\}_{i=1}^{K}$  are used, where  $x_i$ 

shows whether item i is packed into the knapsack ("1") or not ("0"). Solving the QKP is finding a configuration x that maximizes the total profit P given by:

$$P = \sum_{i=1}^{K} \sum_{j=i}^{K} p_{ij} x_i x_j,$$
 (34)

while satisfying the capacity constraint given by:

$$W = \sum_{i=1}^{K} w_i x_i \le c.$$
(35)

Based on the objective function (34) and the constraint (35), the energy function  $H_{\text{OUBO}}$  is defined by:

$$H_{\text{QUBO}} = -P + A \left\{ W - \left( c - \sum_{d=1}^{w_{\text{max}}} y_d \right) \right\}^2, \qquad (36)$$

where *A* is a constraint coefficient and  $w_{\text{max}}$  is the maximum weight of all *K* items. Equation (36) introduces auxiliary binary variables  $\{y_d\}_{d=1}^{w_{\text{max}}}$  to represent the inequality (35) in the energy function. As a result,  $K + w_{\text{max}}$  binary variables are required to formulate a QKP as a QUBO problem. Note that (36) is transformed into (1) when a QKP is solved on GiMP, GiP, or Ising machine.

#### 2) EXPERIMENTAL CONDITIONS

In order to compare GiMP, GiP, and Ising machine, we evaluate their performance from the perspective of solution quality and execution time. Since they search for solutions according to stochastic processes, the performance evaluation is based on  $k (\gg 1)$  solutions and time to obtain the k solutions as follows:

For k solutions obtained in  $t_{\text{total}}$  seconds, FSs that satisfy the capacity constraint (35) are extracted; here,  $k_{\text{FS}}$  denotes the number of extracted solutions. Then, the performance is evaluated using the effective time t' and the average residual energy  $\overline{\Delta H}$  in (37) and (38), respectively:

$$t' = \frac{t_{\text{total}}}{k_{\text{FS}}} [s], \tag{37}$$

$$\overline{\Delta H} = \frac{\sum_{r=1}^{k_{\rm FS}} (H^{(r)} - H_{\rm opt})}{k_{\rm FS}},\tag{38}$$

where  $H^{(r)}$  is equivalent to the total profit *P* derived from the *r*-th replica, and  $H_{opt}$  is the optimal total profit recorded in [19]. By utilizing (37) and (38), we can study the correlation between the time to obtain a FS and its average quality. Now, how to obtain *k* solutions by GiMP/GiP and Ising machine is explained in the following:

GiMP and GiP can specify the number of replicas R and the number of MC steps S. Then, we obtained k solutions by selecting the top-k ( $k \le R$ ). In this experiment, we varied Sas shown in Table 2, whereas we fixed R to 128. Referring to the previous work [20], [21], we used the pseudo temperature scheduling given by:

$$T(s) = T_{\text{init}} \times \left(\frac{T_{\text{fin}}}{T_{\text{init}}}\right)^{\frac{s-1}{s-1}},$$
(39)

#### TABLE 2. Parameter settings used in GiMP and GiP.

Natation	Description	Value
Notation	Description	value
S	# of MC steps	$10^4, 10^5, 10^6$
R	# of replicas	128
$T_{1}$ .	Pseudo temperature at	$0.01 N \times \max \int  I_{\cdots} $
1 init	the first MC step	$0.0117 \times \max\{ J_{ij} \}$
$T_{c}$	Pseudo temperature at	$0.1 \times \min_{I \to I} \int  I_{II} $
1 fin	the last MC step	$0.1 \times \min_{j \neq 0} \{ j_{ij} \}$

#### TABLE 3. Merge interval I<sub>M</sub> used in GiMP.

# of MC steps $S$	Merge interval $I_{\rm M}$
$10^{4}$	5, 10, 100
$10^{5}$	5, 10, 50, 100
$10^{6}$	5, 10, 50, 100, 500, 1000

and set  $T_{\text{init}}$  and  $T_{\text{fin}}$  as shown in Table 2. In GiMP, we also need to specify the merge probability  $P_{\text{merge}}$  and the merge interval  $I_{\text{M}}$ . We varied  $I_{\text{M}}$  for each S as in Table 3, whereas we set  $P_{\text{merge}} = 0.7$ . For every QKP instance and every parameter set, we conducted the following procedure:

- 1) Initialize the constraint coefficient A in (36) to 5.
- 2) Run GiMP (or GiP) and obtain R = 128 solutions.
- 3) Extract FSs and calculate the FS rate.
- 4) Increase *A* by 5 and return to step 2 if the FS rate is below 0.9; otherwise, exit.

As for Ising machine, on the other hand, we only specify the approximate annealing time  $t_{app}$  and the maximum number of solutions  $R_{max}$  output from it. In this experiment, we assumed three settings for  $t_{app}$ : 0.1, 1, and 10 seconds. Note that we can also run Ising machine without specifying  $R_{max}$ . Given a QKP instance and a setting of  $t_{app}$ , we conducted the following procedure to obtain *k* solutions:

- 1) Initialize the constraint coefficient A in (36) to 5.
- 2) Run Ising machine 20 times without specifying  $R_{\text{max}}$  and estimate the number of solutions  $R_{\text{est}}$  expected to obtain in a single run. When we have obtained  $R_{\text{total}}$  solutions in this step,  $R_{\text{est}}$  is calculated by  $\lfloor R_{\text{total}}/20 \rfloor$ .
- 3) Calculate the FS rate based on  $R_{\text{total}}$  solutions.
- 4) Specify  $R_{\text{max}}$  to 1.
- 5) Run Ising machine 20 times with specifying  $R_{\text{max}}$  and obtain  $k \ (\approx 20 \times R_{\text{max}})$  solutions.
- 6) Increase  $R_{\text{max}}$  by  $\Delta_R$  and repeat step 5 as long as  $R_{\text{max}} < R_{\text{est}}$  is satisfied.  $\Delta_R = 1$  if  $R_{\text{max}} \le 10$ ; otherwise,  $\Delta_R = 5$ .
- Increase A by 5 and return to step 2 if the FS rate is below 0.9; otherwise, exit.

#### **B.** RESULTS

Experimental results when eight QKP instances are solved by GiMP ( $\Box$ ), GiP ( $\circ$ ), and Ising machine ( $\Delta$ ) are shown in Fig. 6. The horizontal and the vertical axes of each figure are the effective time t' calculated by (37) and the average residual energy  $\overline{\Delta H}$  calculated by (38), respectively. Small t' means that a large number of FSs are obtained in a short time,



FIGURE 6. Experimental results when eight QKP instances [19] are solved by GiMP (
), GiP (
), and Ising machine (
).

whereas small  $\overline{\Delta H}$  indicates that many high-quality solutions are obtained. We expect that increasing the effective time improves the average residual energy. The results in Fig. 6 demonstrate that GiMP, GiP, and Ising machine follow this trend regardless of the three factors in QKP. Also, GiMP outperforms the others in all the instances. In particular, the superiority is observed in the region where the effective time is small; i.e., GiMP can find relatively high-quality FSs quickly. Furthermore, GiMP finds the optimal solutions for some problem instances (see Figs. 6a, 6b, and 6c).

In Fig. 7, the dependence of the merge interval  $I_M$  on the performance of GiMP is evaluated. As mentioned earlier, we should evaluate the performance based on the effective time and the average residual energy. We solved the instance r\_300\_50\_1 by GiMP while varying  $I_M \in \{5, 10, 100\}$ . It is apparent that using a small  $I_M$  tends to increase the execution



**FIGURE 7.** Relationship between the effective time and the average residual energy while varying the merge interval  $I_{\rm M}$  of GiMP. The QKP instance used in this evaluation is r\_300\_50\_1.

time because the frequency of the merge process increases. However, the results of  $I_{\rm M} = 5$  and 10 show a similar trend with respect to the performance. This means that GiMP with  $I_{\rm M} = 5$  obtains high-quality FSs with fewer MC steps than that with  $I_{\rm M} = 10$ . Also, the result of  $I_{\rm M} = 100$  is inferior to the others. Since the merge fixes the combination of merged spins and their destinations, we need to use a relatively small  $I_{\rm M}$  and try various combinations.

# C. DISCUSSION

In this section, we discuss the performance differences between GiMP and the others.

First, we compare the results of GiMP and GiP in Fig. 6. We can find that the merge method has a significant effect on the performance. The Ising-model formulation of QKP implies that GiP tends to avoid packing more items than the knapsack capacity since the energy is always increased by this operation. On the other hand, GiMP can keep searching for solutions without violating the capacity constraint. This is because GiMP can flip multiple spins to pack and unpack several items simultaneously.

Next, we compare the results of GiMP and Ising machine in Fig. 6. We can observe large differences in the region where the effective time is small. This means that GiMP can find relatively high-quality FSs even in a short time. As the effective time increases, the solution quality is improved both in GiMP and Ising machine, making the differences small.

#### D. SCALABILITY OF GIMP

In this section, we study the impact of the number of spins N and the number of replicas R on the execution time of GiMP.

First, we evaluate the dependence of the execution time per replica on the number of replicas *R* at each *N*. We prepared three Ising models with N = 800, N = 2000, and N = 8000, and run GiMP while varying *R* in the range of  $2^4$  to  $2^9$ . We set the number of MC steps *S* and the merge interval



**FIGURE 8.** Relationship between the number of replicas and the execution time per replica. In this experiment, we ran GiMP with the number of MC steps  $S = 10^5$  for Ising models with 800, 2000, and 8000 spins.



**FIGURE 9.** Relationship between the number of spins and the execution time.

 $I_{\rm M}$  to 10<sup>5</sup> and 10, respectively. Fig. 8 shows the result. In all three models with 800 spins ( $\Box$ ), 2000 spins ( $\circ$ ), and 8000 spins ( $\Delta$ ), the required time per a single solution decreases as the number of replicas *R* increases. More specifically, the execution efficiency is improved about 10 times by increasing *R* from 2<sup>4</sup> to 2<sup>9</sup>. When solving a constrained COP, we usually need to obtain a lot of solutions beyond the required number since we must remove infeasible solutions. The scalability of GiMP offers a great advantage in such a situation.

Next, we evaluate the dependence of the execution time on the number of spins N. We added seven Ising models with N = 128, 256, 512, 1024, 3000, 5000, 7000 and ran GiMP for ten Ising models. We assumed  $R = 512, S = 10^5$ , and  $I_M = 10$  in this evaluation. The result is shown in Fig. 9 with the blue dots (•). In the range of N < 1000, the execution time varies proportionally to N (see the dotted line in Fig. 9), whereas it follows a proportion to the square of N for N >1000 (see the dashed line in Fig. 9). We can explain why the scaling changes beyond a border (i.e., beyond N = 1000 in this case) as follows. As discussed in Section IV-C2, GiMP has O(RN) and  $O(RN^2)$  computation processes. For small N, the O(RN) process dominantly contributes the execution time. However, the weight of the  $O(RN^2)$  process grows as *N* increases. Note that the border depends on both *R* and the specification of GPU used to implement GiMP.

# **VI. CONCLUSION**

The recently-proposed merge method is an important technique to enhance the search performance of SA-based Ising machines. However, its sequential process was an obstacle to integrate it into existing Ising-machine hardware. In order to overcome this obstacle, this paper newly proposed the GPU-oriented merge method by formulating the merge method as a series of matrix multiplications using the merging matrix. Then, we developed Merge PSA by integrating the proposed merge method into replica parallel execution of PSA. Finally, Merge PSA was implemented on a GPU and evaluated for QKP instances. Compared to the state-of-theart GPU-based Ising machine, our proposed Ising machine demonstrated the superiority because high-quality FSs could be obtained in a shorter time. Moreover, we confirmed that the replica parallel execution effectively reduces the time per a single solution.

In this paper, we constructed the merging matrix such that merged spins and their destinations are selected at random. On the other hand, the previous work [18] has demonstrated that putting restrictions on the selection further enhances the search performance for some types of constrained COPs. In the future, we will introduce such restrictions into our GPU-oriented merge method by modifying the generation process of the matrix L, and verify our method for a wider range of constrained COPs.

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# IEEE Access



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