

Received 14 December 2023, accepted 7 January 2024, date of publication 10 January 2024, date of current version 19 January 2024.

Digital Object Identifier 10.1109/ACCESS.2024.3352157

RESEARCH ARTICLE

High- and Half-Degree Quantum Multiplication for Post-Quantum Security Evaluation

RINI WISNU WARDHANI¹, (Graduate Student Member, IEEE),
DEDY SEPTONO CATUR PUTRANTO^{1,2,3}, (Member, IEEE),
AND HOWON KIM¹, (Member, IEEE)

¹School of Computer Science and Engineering, Pusan National University, Busan 609735, South Korea

²IoT Research Center, Pusan National University, Busan 609735, South Korea

³Blockchain Platform Research Center, Pusan National University, Busan 609735, South Korea

Corresponding author: Howon Kim (howonkim@pusan.ac.kr)

This work was supported in part by the Institute for Information and Communications Technology Planning and Evaluation (IITP) funded by the Korea Government Ministry of Science and Information and Communication Technology/ICT (MSIT), South Korea (Study on Quantum Security Evaluation of Cryptography Based on Computational Quantum Complexity, 50%) under Grant 2019-0-00033; and in part by MSIT under the Convergence Security Core Talent Training Business (Pusan National University) Support Program Supervised by IITP (50%) under Grant IITP-2023-2022-0-01201.

ABSTRACT This paper provides a thorough examination of strategies related to the design of the polynomial multiplication approach, which is essential to resolving large number operations, including multiplication in the post-quantum cryptography algorithm. Our research specifically centers on the implementation of Toom-Cook-based multiplication algorithms for high- and half-degree quantum multiplication, up to 20.5-way degrees, considering that existing Toom-Cook lower degrees are combined with a lattice-based approach like the Saber algorithm. In particular, we propose the quantum multiplication design, conduct computation step experiments, and implement the division-free method. In addition to examining the Toom-Cook 20.5-way algorithm, this study also provides an overview of the results obtained from the Toom-Cook 8.5-way and Toom-Cook 10.5-way algorithms. The Toom-Cook 20.5-way design exhibits significant performance improvement over its predecessor, as evidenced by its lower value of asymptotic complexity and lower cost of quantum implementation, with a qubit count of $n^{1.186}$, approximately $522n^{\log_{21} 41} - 540n$ Toffoli count, and $n^{1.033}$ Toffoli depth. Concerning the asymptotic performance analysis and quantum cost compared to alternative Toom-Cook-based multiplication, the results indicate the development of a more efficient multiplication polynomial that may be utilized in the evaluation of post-quantum security.

INDEX TERMS Toom-cook, high- and half-degree quantum multiplication, asymptotic performance analysis, quantum cost.

I. INTRODUCTION

The Toom-Cook algorithm, which is based on work from [1], [2], is well recognized as a highly efficient strategy for resolving complex multiplication problems involving large numbers. The concept can often be referred to as a universal multiplication [3]. In general, the approach entails decomposing the multiplication procedure into smaller multiplications and additions, resulting in a reduction of the overall computational complexity. In numerous fields,

The associate editor coordinating the review of this manuscript and approving it for publication was Jiafeng Xie.

including computer algebra systems and cryptography, this method is utilized extensively to enhance the performance of polynomial multiplication operations, including the multiplication portion of the post-quantum cryptography (PQC) algorithm.

There has been a significant surge in the adoption of Toom-Cook and number theoretic transform (NTT)-based polynomial multiplication as a result of their incorporation as essential elements in the post-quantum standardization initiative [3], [4]. In addition to classical and quantum multiplication designs, i.e., [5], [6], [7], [8], several studies [3], [4], [9] have put forth an interesting investigation concerning

Toom-Cook multiplication. The current investigation, which reveals the vulnerabilities of the Toom-Cook multiplication algorithm, was carried out by [4] and [10]. Hence, the research described in [11] gives an insight into increasing the number of iterations to mitigate the vulnerability to peak analysis attacks in the correlation power analysis (CPA) technique to lower degree Toom-Cook, as noted by [4].

In their preliminary investigation, Bodrato et al. [6] proposed a multiplication method known as Toom'n'half, which integrates high- and half-degree multiplication to enhance the efficacy strategy and outperforms Toom-Cook in classical computing. In this work, we design the Toom-Cook-based multiplication algorithms for high- and half-degree quantum multiplication, up to 20.5-way degrees, and investigate asymptotic performance analysis and quantum resources for Toom-Cook-based multiplication in terms of qubit count, Toffoli count, and Toffoli depth.

This work presents a novel design that significantly improves the efficiency of the multiplication algorithm based on the substantial strategy for Toom-Cook high-degree algorithm from several prior works (e.g., [4], [5], [6], [7], [8], [11], [12], and [13]). To the best of our understanding, this is the first work to integrate high- and half-degree quantum multiplication in classical and quantum design while maintaining an architecture degree of up to 20.5-way for Toom-Cook-based multiplication. Achieving the highest possible asymptotic performance result while utilizing fewer quantum resources is the principal aim.

The contributions of the paper can be summed up as follows:

- 1) We conduct a thorough examination and synthesis of literature pertaining to multiplication techniques and works based on the Toom-Cook algorithm, focusing on strategies for polynomial multiplication in classical and quantum designs and also attacks based on Toom-Cook multiplication. In addition, we examine the application of the multiplication operation in the construction of a post-quantum algorithm, such as the lattice-based methodology, i.e., Saber and Kyber. In addition, we capture relevant research on multiplication-based attack techniques that can potentially be performed with a lower degree of Toom-Cook-based multiplication, specifically CPA.
- 2) We elaborate on several approaches and strategies from prior related works ([5], [6], [7], [8], [11]) to reduce computational resources, i.e., opposite points, inverses, and cost-exploiting symmetries. Our focus lies on the implementation of high- and half-degree quantum multiplication techniques, specifically addressing the design strategies employed in the Toom-Cook 8.5-way, 10.5-way, and 20.5-way methodologies.
- 3) We design the proposed multiplication and experiment computation steps such as splitting, evaluation, recursive multiplication, interpolation, and recomposition in a sequential manner and derive the quantum circuit for

performing high- and half-degree quantum multiplication. As a result, we present the Toom-Cook 8.5-way, 10.5-way, and 20.5-way quantum design architectures, wherein the highest degree of Toom-Cook 20.5-way exhibits the lowest asymptotic performance and minimal utilization of quantum resources.

- 4) We analyze the asymptotic performance and quantum resource utilization of various multiplication algorithms, with a particular focus on the Toom-Cook-based multiplication algorithm for degrees up to 20.5-way degrees. Our investigation leads to the conclusion that the Toom-Cook 20.5-way architecture demonstrates the lowest quantum resource utilization, requiring qubit count of $n \left(\frac{41}{21}\right)^{\frac{\log 41}{(2 \log 41 - \log 21)} \log_{21} n} \approx n^{1.186}$, $522n^{\log_{21} 41} - 540n$ Toffoli count, and $n \left(\frac{41}{21}\right)^{1 - \frac{\log 41}{(2 \log 41 - \log 21)} \log_{21} n} \approx n^{1.033}$ Toffoli depth.

The paper is structured in the following manner: Section I gives an overview related to the research. Section II presents a thorough examination of the existing literature, specifically focusing on the Toom-Cook algorithm and its associated works. In Section III, we present a succinct summary of the utilization and multiplication-based attacks. In Sections IV and V, the strategies and detailed procedures for designing the proposed high- and half-degree quantum multiplication are outlined, specifically the Toom-Cook 20.5-way designs. Section VI presents a comprehensive analysis of the space and time complexity, along with a comparative evaluation of multiplication techniques utilizing the Toom-Cook algorithm. Section VII encompasses the discussion and future work directions, while Section VIII presents the conclusions of the current study.

II. RELATED WORKS

The Toom-Cook algorithm, particularly the Toom-Cook k -way approach for multiplication, has superior time complexity when compared to the naive Schoolbook multiplication algorithm, which has a time complexity of $\mathcal{O}(n^2)$. Furthermore, this approach also achieves a lower asymptotic performance analysis compared to the Karatsuba algorithm, which is regarded as equivalent to the Toom 2-way algorithm with a complexity of $(n^{\log(3)/\log(2)}) \equiv \mathcal{O}(n^{1.58})$. In Table 1, a comprehensive analysis and synthesis of works related to multiplication methodologies and investigations centered around the Toom-Cook algorithm are presented. The discussion primarily revolves around approaches employed for polynomial multiplication, both in classical and quantum environments, as well as potential vulnerabilities stemming from Toom-Cook multiplication.

Alberto Zanoni [5] presents a computational implementation of a balanced Toom-Cook 8-way algorithm in a classical environment, specifically designed for integer multiplication and squaring. The Toom-8 classical method employs a technique that divides components into eight distinct portions. Previously, Bodrato and Zanoni have made advancements in the original Toom approach family, as discussed in

TABLE 1. Toom-Cook-based multiplication related research. We extracted information from [4], [5], [6], [7], [8], [11], [12], and [13] to elucidate on their particular Toom-Cook-based-related task in a classical-quantum environment.

No	References Paper	Year	Arithmetic Algorithm	Research Field Focus	Specific Step	Strategies	Results
1	Marco Bodrato [6], [12]	2007-2011	High-degree Toom'n'half (in general)	High degree Toom'n'half for balanced and unbalance multiplication (Classical)	Splitting, Evaluation, Recursive Multiplication, Interpolation, (Early)Recomposition	Estimation cost Toom'n'half, First comparison to Karatsuba	Memory optimization and arithmetic optimization in classical environment
2	Zanoni et al. [5]	2009	Toom-Cook 8-way - degree 7	Toom-Cook 8-way implementation for Long Integers Multiplication (Classical)	Splitting, Evaluation, Recursive Multiplication, Interpolation, Recomposition	Toom-Cook 8-way implementation for Long Integers (multiplication and squaring)	Balance Toom-8 and Unbalanced Toom-8 Multiplication and comparing code with GMP 4.3.0 library
3	Dutta et al. [7]	2018	Toom-Cook 2.5 Multiplication	Toom-Cook Multiplication (Quantum Design)	Splitting, Evaluation, Recursive Multiplication, Interpolation, Recomposition	Implementation in k=2.5, tree structured method, and gate count analysis	Quantum design on Toom 2.5 and Comparison on asymptotic multiplication method (naive, naive improves, Karatsuba, Toom 2.5 and Const.Mult)
4	Larasati et al. [8]	2021	Toom-Cook 3 Multiplication	Toom-Cook Multiplication (Quantum Design)	Splitting, Evaluation, Recursive Multiplication, Interpolation, Recomposition	Implementation in k=3 and gate count analysis	Quantum design on Toom 3 and Comparison on asymptotic multiplication method (naive, naive improves, Karatsuba, Toom 2.5, Toom 3, and Const.Mult)
5	Mera et al. [3]	2020	Toom-Cook-based multiplication	Time-memory trade off in Toom-Cook multiplication (Classical)	Pre- and post-processing steps (Evaluation step and Interpolation)	Evaluation step is reduced by a factor of 2 (precomputation) and lazy-interpolation	Toom-Cook design that increase efficiency
6	Ghosh et al. [9]	2020	Toom-Cook	Design efficient polynomial multiplication with Toom-Cook (Classical)	Memory efficient striding Toom-Cook with lazy interpolation	Low-latency striding Toom-Cook Multiplication	Optimized Toom-Cook Multiplication implementation in Saber post-quantum accelerator
7	Gu et al. [13] [3]	2021	DT3 (Division-free Toom-3)	Proposed Montgomery Modular Multiplication Based on DT3 (Classical)	Division-Free Toom-Cook Multiplication	Employing Montgomery Modular Multiplication with a Division-Free Toom-Cook Multiplication	Achieve ASIC high-performance hardware implementation
8	Li et al. [4]	2022	Toom Cook Multiplication	Single Trace Side Channel attack on the Toom-Cook (Classical)	Side Channel Attack on the Toom Cook with case study Saber post-quantum Algorithm	Elaborate on Toom-Cook based multiplication on Saber PQC Algorithm and its correlation to power usage (correlation power analysis)	Side Channel Attack result on the Toom Cook with case study Saber post-quantum Algorithm (for lower degree ; Toom 4)
9	Putranto et al. [14]	2023	Toom-Cook 2, 4 and 8 Multiplication	Toom-Cook Multiplication (Quantum Design)	Splitting, Evaluation, Recursive Multiplication, Interpolation, Recomposition	Quantum division free implementation in Toom-Cook k=2,4,8	Comparison on asymptotic multiplication method (naive, naive improves, Karatsuba, Toom 2, Toom 2.5, Toom 3, Toom 4, Toom 8, and Const.Mult)
10	Ours	2023	Toom-Cook 8.5, 10.5 and 20.5 Multiplication	High- and half-Degree Quantum Multiplication (Quantum Design)	Splitting, Evaluation, Recursive Multiplication, Interpolation, Recomposition	Quantum design division free Implementation in high-degree Toom-Cook k=8.5,10.5,20.5 with division free and arithmetic optimization (i.e., opposite points, inverses, symmetries) and gate count analysis	First method in quantum for high- and half-degree quantum multiplication with division free and arithmetic optimization (i.e., tree structured method, opposite points, inverses, symmetries)

their work [15], by incorporating unbalanced operands, which refer to polynomials with varying degrees. This has been achieved by the utilization of the Toom– $(k + 1/2)$ approaches. Later, in Marco Bodrato's advanced research in [6], several strategies and techniques for achieving a high degree of precision in the implementation of Toom-Cook algorithms, specifically for both balanced and unbalanced scenarios, are clarified in classical hardware implementation.

Dutta et al. [7] conducted a thorough investigation of the utilization of the Toom-Cook 2.5-way technique within the quantum circuit for multiplication, offering a detailed explanation of its workings. A bound on the count of Toffoli gates and qubits was found by analysis of the recursive tree structure of the method. The proposed quantum circuit

exhibits superior asymptotic bounds for several performance metrics compared to previous implementations of multiplier circuits utilizing schoolbook and Karatsuba methods. Later, the research continued by Larasati et al. [8] shows findings that demonstrate the possibility of the k –way Toom-Cook method, which employs higher-degree polynomial interpolation, to exhibit lower asymptotic complexity in comparison to alternative approaches such as Toom-Cook 2.5-way.

In their study, Larasati et al. [8] expound upon the Toom-Cook 3-way algorithm by incorporating the division gate. This research specifically devised the quantum circuit that aligns with Bodrato's suggested sequence, aiming to minimize the number of operations required, particularly

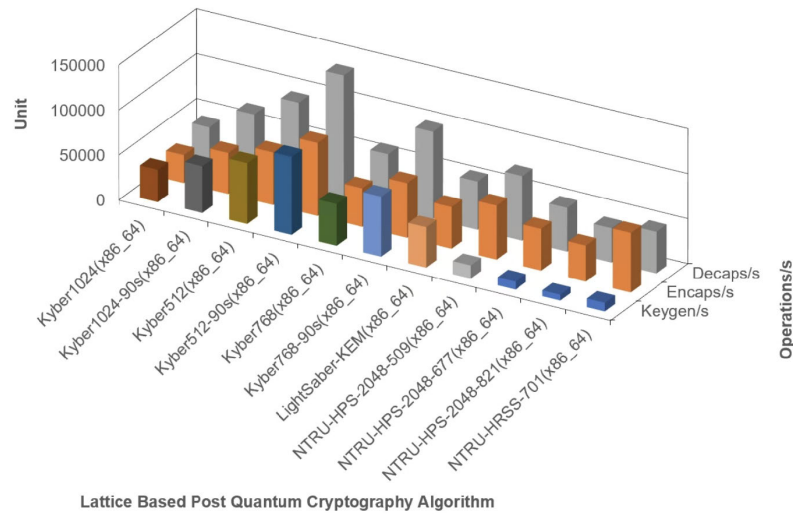


FIGURE 1. Runtime analysis of Open Quantum Safe Lattice-based Cryptographic algorithms (Key Encapsulation Mechanisms).

in relation to nontrivial division. This approach effectively reduces the need for nontrivial divisions, as only one exact division by 3 circuit is utilized per iteration. Furthermore, in order to further reduce the expenses associated with the remaining division, the researchers employ the distinctive characteristics of the specific division circuit. Based on the result of the numerical analysis, it can be observed that the circuit exhibits a reduced asymptotic complexity in terms of Toffoli depth and qubit count when compared to Toom-Cook 2.5-way [7]. However, it is worth noting that a significant number of Toffoli gates are required primarily for the implementation of the division operation.

The research on [6], [8], and [11] highlights the existence of a challenge in designing high degree Toom-Cook-based multiplication. However, it also presents an opportunity to achieve superior performance in asymptotic performance analysis, both in classical or quantum environments.

III. LOWER-DEGREE USAGE AND ATTACK VULNERABILITY OF TOOM-COOK IN THE PQC ERA

Numerous investigations have been conducted on the enhancement of public-key cryptosystems, aiming to protect against potential attacks deriving from both classical and quantum computing paradigms. The period characterized by the need for quantum-resistant encryption is commonly denoted as the PQC era, as elucidated in [16]. In this subsection, we highlight a brief example of the usage and implementation of Toom-Cook-based multiplication in the Saber and Kyber PQC algorithm, as well as the potential vulnerability that arises from the utilization of lower-degree multiplication.

According to the NIST PQC standardization process, the two main algorithms that are suggested for a range of applications, including digital signatures, are Crystals-Kyber [17] for public-key setup and Crystals-Dilithium [18] Lattice-based encryption is expected to exhibit optimal

efficiency and resilience against quantum attacks, rendering it a feasible solution within the domain of PQC and appears to be the most rapid implementation as in [19], [20], [21], and [22]. Dilithium, Falcon, FrodoKEM, Kyber, NTRU, NTRU Prime, and Saber are seven of the fifteen candidates in the NIST third round that use lattice-based cryptography [16].

Polynomial multiplications, such as Toom-Cook and NTT, play a crucial role in lattice-based post-quantum encryption by serving as the essential constituents. Lattice-based cryptographic systems commonly employ either the NTT with time complexity of $(\mathcal{O}(n \log n))$ [23] or the Toom-Cook/Karatsuba algorithm with time complexity of $(\mathcal{O}(n^{1+\epsilon}))$, where $0 < \epsilon < 1$, [1], [2], [24], to achieve efficient polynomial multiplication involving n coefficients [10]. These multiplications facilitate the division of the resultant sub-polynomial, as highlighted in [10]. The results of the runtime analysis for a post-quantum lattice-based cryptographic algorithm, specifically a key encapsulation mechanism, are displayed in Figure 1. In this figure, our focus is solely on the Kyber algorithm. The analysis is conducted by comparing the algorithm's runtime behavior and memory consumption statistics, as documented in the work by Mujdei et al. [10].

While the majority of other research concentrates on optimizing NTT-based multiplications, research [9] optimizes a Toom-Cook-based multiplier to an exceptional degree. A memory-efficient striding Toom-Cook with delayed interpolation yields a highly compact, low-power implementation that allows for a very regular memory access scheme. Besides the usage of Toom-Cook multiplication, they demonstrate the multiplier's effectiveness and integrate it into one of the four NIST finalists, the Saber post-quantum accelerator. The Saber algorithm employs an additional division of the resultant sub-polynomials into two Karatsuba layers, followed by the execution of a 16-coefficient schoolbook operation [10]. Figure 2 displays an image that portrays an occurrence of Toom-Cook-based multiplication executed

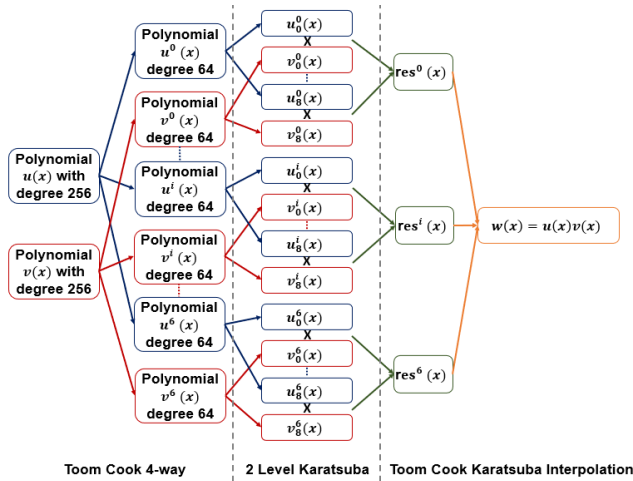


FIGURE 2. The Toom-Cook 4-way and Karatsuba Multiplication used in Saber Post-Quantum Cryptography Algorithm.

within the Saber structures. We redraw to demonstrate the application of the Toom-Cook 4-way method in the implementation of the Saber post-quantum cryptography algorithm from the work of Mera et al. [3].

Regarding attacks based on multiplication, the utilization of side-channel information can be targeted towards the multiplication process, as demonstrated in recent studies [4], [10]. The exploitation of side-channel information, such as power consumption, electromagnetic radiation, and execution time, has been shown to be a method for gaining unauthorized access to sensitive data [25]. CPA is widely recognized as a very effective technique that leverages the correlation between a device’s power consumption and the data it is processing. This approach exploits power fluctuations that are caused by mathematical processes such as multiplication. Hence, the evaluation of potential risks associated with multiplication exploitation in side-channel analysis attacks, particularly when utilizing the CPA approach, is crucial during the construction of cryptographic algorithms. This concern arises due to the frequent use of arithmetic multiplication as a sub-operation multiplier in real implementations.

Mujdei et al. conducted an experimental analysis to investigate the potential occurrence of CPA peaks when employing the schoolbook sub-operation in the processing of 3-way and 4-way Toom-Cook within the lattice-based PQC algorithm. The post-quantum algorithm NTRU–HPS–4096821 elaborated in [10], can be subjected to a multiplication-based attack utilizing side-channel measurements. Mujdei et al. study encompasses an examination of the variance plot of 500 instances of schoolbook multiplication, wherein a comprehensive analysis reveals the identification of a total of 72 apparent peaks. Proficiency in mathematical approaches is essential for the development of PQC algorithms that can effectively withstand SCA. Furthermore, the utilization of effective mathematical techniques is imperative in the construction of quantum circuits, which can be employed for the creation of cryptanalysis circuits. The primary function

of these cryptanalysis circuits is to evaluate the resilience of a method.

Efficient arithmetic operations, particularly multiplication, play a vital role in conducting comprehensive investigations within the domain of quantum-based cryptanalysis. According to Roche [26], Parent et al. [27], Gidney [28], Banegas et al. [29], and Putranto et al. [11], [14], the development of a fundamental arithmetic constructor that demonstrates efficiency in terms of space use and time consumption is crucial for expediting the cryptanalysis process. The primary objective of these investigations is to reduce the complexity that is typically encountered during the execution of quantum cryptanalysis. The efficacy of basic mathematical operations, particularly multiplication, can significantly impact the predictive analysis of the utilization of multiplication inside the lattice-based PQC algorithm, as well as the quantum computer’s ability to solve conventional public key cryptography through cryptanalysis, which further leads to post-quantum security evaluation.

IV. TOOM-COOK MULTIPLICATION COMPUTATIONAL STEPS AND STRATEGIES

A. TOOM-COOK COMPUTATION STEPS

In the present subsection, we employ the identical overarching parameter as Marco Bodrato’s prior study on classical computation [6] to establish a direct correlation. Given two polynomials, u and v , belonging to the ring of polynomials over an integral domain $\mathbb{R}[x]$, we aim to calculate the product $\mathbb{R}[x]$. The algorithm can be delineated into five distinct steps, denoted as splitting, evaluation, recursive multiplication, interpolation, and recombination.

1) SPLITTING

Let $Y = x^b$ be selected as the fundamental value. Subsequently, u and v can be represented by two homogeneous polynomials, namely $u(y, z) = \sum_{i=0}^{n-1} u_i z^{n-1-i} y^i$ and $v(y, z) = \sum_{i=0}^{m-1} v_i z^{m-1-i} y^i$. These polynomials consist of n and m coefficients, respectively, and their degrees, denoted as $\deg(u) = n - 1$ and $\deg(v) = m - 1$, align with the aforementioned coefficients and degrees. Such that $u(x^b, 1)$ equals the variable u and $v(x^b, 1)$ equals the variable v under the specified circumstances. The coefficients of $u_i, v_i \in \mathbb{R}[x]$ can be represented as polynomial functions and can be chosen to have a specified degree, denoted as $\forall i, \deg(u_i) < b, \deg(v_i) < b$.

The typical way of implementing the Toom k -way algorithm requires the operands to be evenly sized, with one operand m being the same size as the other operand n . In contrast to the same-size operand value, we expand our approach to elaborate on unbalanced or not-the-same-size operand value circumstances. This approach refers to [5] and [6] that labels the methodology as Toom- $\frac{n+m}{2}$ for Toom’n’half terminology. In this research, we specifically refer to it as Toom-Cook high- and half-degree quantum multiplication.

2) EVALUATION

In order to calculate the product $w = u \cdot v$, where the degree of the polynomial is denoted as $d = n + m - 2$, it is necessary to have $d + 1 = n + m - 1$ evaluation points $P_d = (\alpha_0, \beta_0), \dots, (\alpha_d, \beta_d)$, where $(\alpha_i, \beta_i) \in \mathbb{R}[x]$. The process of computing the evaluation of a single polynomial, for example u , at specific positions (α_i, β_i) , can be achieved by the utilization of matrix by vector multiplication.

3) RECURSIVE MULTIPLICATION

During this stage, the computation of $\forall i, w(\alpha_i, \beta_i) = u(\alpha_i, \beta_i)v(\alpha_i, \beta_i)$ will be performed, which will involve the multiplication of $d + 1$ polynomials with degrees that are either less than or equal to the degree of $Y = x^b$.

4) INTERPOLATION

The outcome of this phase is solely determined by the expected degree of the result d as well as the selection of $d + 1$ points (α_i, β_i) . It is not influenced by n and m individually. The coefficients of the polynomial $w(y, z) = \sum_{i=0}^d w_i z^{d-i} y^i$ are currently required. Given that we possess the values of w assessed at $d + 1$ distinct points, we are met with a classical interpolation problem. We apply the inverse of the A_d , as also described in the [6] paper, often referred to as a $(d + 1) \times (d + 1)$ Vandermonde-like matrix.

5) RECOMPOSITION

Calculating the required output in an effective manner is made possible by performing an additional evaluation using $w = w(x^b, 1)$. This particular stage requires at most d shifts and additions to be implemented.

B. DESIGN STRATEGIES

Within the classical context strategies for Toom-Cook multiplication, prior work [6] notes the presence of two critical stages, notably evaluation and interpolation. The rationale provided is that both evaluation and interpolation necessitate the utilization of matrix by vector multiplication. Therefore, both of these stages necessitate numerous operations such as additions, subtractions, shifts, and even little multiplications or precise divisions (that is, interpolation) involving small elements in $\mathbb{R}[x]$. Furthermore, within the [6] works, the process of splitting apart is executed by a mix of all phases and interlaced operations, with the objective of reducing both memory consumption and the overall count of operations in \mathbb{R} . This section provides a concise explanation of the methods employed in classical Toom'n'half by Bodrato [6]. These strategies include the utilization of opposite points, the implementation of inverse matrix strategies during the interpolation step, and the incorporation of cost-exploiting symmetries.

1) OPPOSITE POINTS

The interpolation can be achieved by performing a series of operations on the rows of the matrix during the interpolation

step. By eliminating more entries in each step, it is possible to shorten the sequence [15]. The lines produced by two contrasting evaluations exhibit identical absolute values, with all values being positive on one line and alternating on the other. By performing the operations of addition and subtraction on the two lines and subsequently dividing the result by two, we are able to derive the following lines:

$$\begin{pmatrix} \beta^d & \alpha\beta^{d-1} & \alpha^2\beta^{d-2} & \dots & \alpha^d \\ \beta^d & -\alpha\beta^{d-1} & \alpha^2\beta^{d-2} & \dots & (-\alpha)^d \end{pmatrix} \quad (1)$$

In this procedure, half of the entries are adjusted to zero.

$$\begin{pmatrix} 0 & \alpha\beta^{d-1} & 0 & \alpha^3\beta^{d-3} & \dots \\ \beta^d & 0 & \alpha^2\beta^{d-2} & 0 & \dots \end{pmatrix} \quad (2)$$

In this work, we employ opposite points, which implies the necessity of consistently employing pairs of points. This implies that in the presence of the two unusual points (1,0) and (0,1), commonly referred to as zero and infinity, it is desirable to have an even number of points. In the case of the conventional balanced Toom-Cook k -way algorithm, the number of points needed is determined by the equation $d + 1 = 2n - 1$, which always results in an odd value. On the other hand, we have advantages that serve as the primary motivation for investigating the unbalanced Toom'n'half algorithm which necessitates the use of $2n$ points.

2) INVERSES

In the interpolation matrix, if we evaluate (α, β) and its inverse (β, α) , it reveals the presence of two symmetric lines in the matrix. These lines exhibit identical values and signs but are arranged in opposite orders, as described in Equation 3. By performing the operations of addition and subtraction on the two lines, we are able to derive two distinct lines. One of these lines exhibits symmetry, while the other does not possess this characteristic (asymmetric), as shown in Equation 4.

$$\begin{pmatrix} \beta^d & \alpha\beta^{d-1} & \dots & \alpha\beta^{d-1} & \alpha^d \\ \alpha^d & \alpha^{d-1}\beta & \dots & \alpha^{d-1}\beta & \beta^d \end{pmatrix} \quad (3)$$

$$\begin{pmatrix} \alpha^d + \beta^d & \alpha^{d-1}\beta + \alpha\beta^{d-1} & \dots & \beta^d + \alpha^d \\ \alpha^d - \beta^d & \alpha^{d-1}\beta - \alpha\beta^{d-1} & \dots & \beta^d - \alpha^d \end{pmatrix} \quad (4)$$

If each line in the interpolation matrix possesses its own inverse, it is possible to derive a new matrix that can be represented in a block-wise manner [6], as presented in Equation 5.

$$\begin{pmatrix} c|cA & A' \\ -B & B' \end{pmatrix} \quad (5)$$

The matrices A' and B' represent the mirrored versions of matrices A and B , respectively. The two exceptional points, (1, 0) and (0, 1), were disregarded, as they can be handled independently. The two regular patterns, (1, 1) and (-1, 1), exhibit symmetrical lines and should be included in A, A' without the requirement of precomputations. In the following

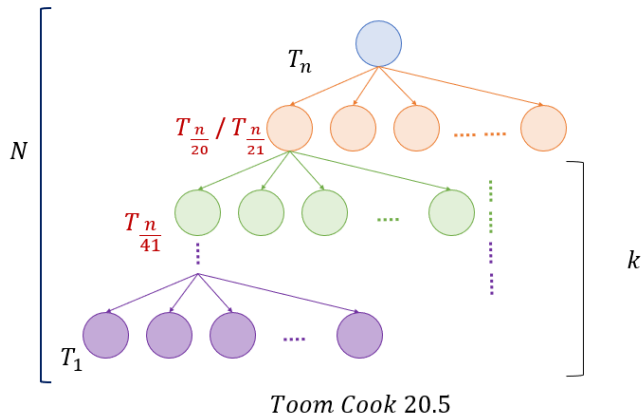


FIGURE 3. The Toom-Cook 20.5-way Multiplication Recursion Tree Structure, where T represents the Toom-Cook k -way Multiplication and n and N represent the bit length for each level and the overall depth of the tree, respectively.

steps, when operating lines in A , any entry that is zeroed in A will have a corresponding entry zeroed through the same action in A' . A similar phenomenon occurs with both B and B' . In this manner, the nullifying impact of the following operations is doubled.

3) COST EXPLOITING SYMMETRIES

It is observed from the studies of [6] that, in order to encompass all symmetries, it is necessary to consider quartets of points denoted as $\pm\alpha$, $\pm\alpha^{-1}$, along with the four “standard” points 0 , ± 1 , and ∞ . To fully exploit this advantage, we aim to minimize computational costs in our quantum design. It is crucial to carefully determine the appropriate selection of the $4k$ evaluation points. The extremal points 0 and ∞ do not necessitate any operations for evaluation. To satisfy the condition of $n + m - 1 = 4k$, the two operands must be denoted by two polynomials, each possessing a degree of n and m , respectively. This implies that the evaluation of each pair of $\pm\alpha$ incurs a cost $(n + 1) + (m + 1) = (4k + 1)$. There are a total of $2k - 1$ couples. Hence, evaluation necessitates the consideration of $(4k + 1)(2k - 1)$ combinations. To address a wide range of unbalancements, Toom balanced addresses the issue of operand ratios that are unbalanced and cannot be handled well [6]. One such approach is to disregard the evaluation at ∞ and substitute it with a value of zero. A potentially more efficient approach might involve disregarding the assessment in 0 , as the desired product from the evaluation in ∞ could potentially be shorter.

V. HIGH- AND HALF-DEGREE QUANTUM MULTIPLICATION DESIGN

Figure 3 depicts the visual interpretation of the recursive tree structures associated with Toom-Cook 20.5-way. Further, Figure 4 draws quantum circuits pertaining to the Toom-Cook 20.5-way multiplications design. The function block boxes are utilized as visual representations of the discrete stages encompassed in the construction process of the Toom-Cook quantum circuit. The multiplication algorithm employs a

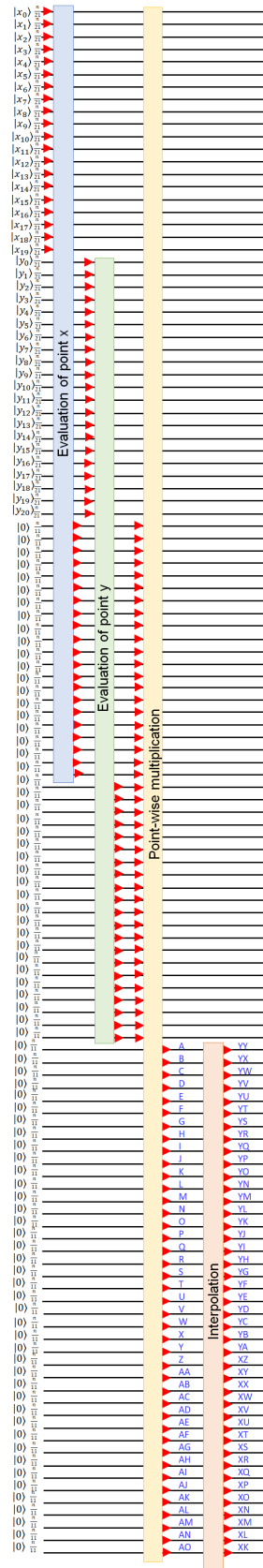


FIGURE 4. Quantum Design for the Toom-Cook 20.5-way Multiplication Algorithms.

quantum circuit wherein red triangles are employed to represent the input and output of each operation within the function blocks. A notation symbol is utilized to represent the quantum state of the input, where each line corresponds to a necessary register in the quantum circuit. The inclusion of triangles situated on the left side of a block serves to emphasize the positioning of its input entry point. The symbolization of the output location on the right side is represented by triangles. In order to preserve a sense of simplicity, the ancilla registers have been excluded from the visual representation.

The computational steps encompass the following steps: splitting, evaluation, recursive multiplication, interpolation, and recombination, as expounded upon in prior literary investigations [5], [6], [8], [11]. The input operands, denoted as variables x and y , represent the quantities to be multiplied. The variable x is employed as a symbol to denote the entirety of the numerical input. The subscripts $x_0, x_1, x_{-1}, x_{-2}, \dots$ are employed to denote the individual constituents of the input. Conversely, the symbols $x(0), x(1), x(-1), x(-2), \dots$ are utilized to denote the outcomes derived from assessing the variable x at certain places.

In the splitting computation stage of Toom-Cook 20.5-way, the two given homogeneous polynomial inputs, represented by Equations 6 and 7 as x and y correspondingly, are divided into 20 and 21 smaller pieces of length $\frac{n}{21}$ each. These polynomials consist of n and m operands, respectively, 20 and 21, which align with the aforementioned coefficients and degrees.

$$\begin{aligned} x = & x_{19}s^{19j} + x_{18}s^{18j} + x_{17}s^{17j} + x_{16}s^{16j} + x_{15}s^{15j} + x_{14}s^{14j} \\ & + x_{13}s^{13j} + x_{12}s^{12j} + x_{11}s^{11j} + x_{10}s^{10j} + x_9s^{9j} + x_8s^{8j} \\ & + x_7s^{7j} + x_6s^{6j} + x_5s^{5j} + x_4s^{4j} + x_3s^{3j} + x_2s^{2j} \\ & + x_1s^j + x_0 \end{aligned} \quad (6)$$

$$\begin{aligned} y = & y_{20}s^{20j} + y_{19}s^{19j} + y_{18}s^{18j} + y_{17}s^{17j} + y_{16}s^{16j} + y_{15}s^{15j} \\ & + y_{14}s^{14j} + y_{13}s^{13j} + y_{12}s^{12j} + y_{11}s^{11j} + y_{10}s^{10j} + y_9s^{9j} \\ & + y_8s^{8j} + y_7s^{7j} + y_6s^{6j} + y_5s^{5j} + y_4s^{4j} + y_3s^{3j} + y_2s^{2j} \\ & + y_1s^j + y_0 \end{aligned} \quad (7)$$

In the beginning stages of Toom-Cook's splitting steps, when employing Toom-Cook's k -way algorithm to partition a given quantity into k segments, it is imperative to select a base $Y = x^b$ and compute the radix j value in the equations, which can be predetermined by utilizing Equation 8.

$$j = \max \left\{ \left\lfloor \frac{\lceil \log_2 x \rceil}{21} \right\rfloor, \left\lfloor \frac{\lceil \log_2 y \rceil}{20} \right\rfloor \right\} + 1 \quad (8)$$

In the evaluation computation step, Values in Equation 9 are utilized to evaluate the points x and y , specifically, all 41 predefined evaluation points. In order to achieve evaluation as described in equation 10, 41 predefined evaluation points in Equation 9 are utilized. The utilization of the infinity value is intentionally avoided, as it aligns with the strategies outlined in the research conducted by [6].

One possible strategy involves disregarding the evaluation at infinity and replacing it with a value of zero.

$$\begin{aligned} x_1 = 0, x_2 = 1, x_3 = -1, x_4 = 2, x_5 = -2, x_6 = 4, \\ x_7 = -4, \\ x_8 = 0.5, x_9 = -0.5, x_{10} = 0.25, x_{11} = -0.25, \\ x_{12} = 0.125, x_{13} = -0.125, x_{14} = 0.0625, \\ x_{15} = -0.0625, x_{16} = 0.03125, x_{17} = -0.03125, \\ x_{18} = 0.015625, x_{19} = -0.015625, x_{20} = 0.0078125, \\ x_{21} = -0.0078125, x_{22} = 0.00390625, \\ x_{23} = -0.00390625, x_{24} = 0.001953125, \\ x_{25} = -0.001953125, x_{26} = 0.000976565, \\ x_{27} = -0.000976565, x_{28} = 0.0004882825, \\ x_{29} = -0.0004882825, x_{30} = 0.0002441415, \\ x_{31} = -0.0002441415, x_{32} = 0.00012206, \\ x_{33} = -0.00012206, x_{34} = 0.00006103, \\ x_{35} = -0.00006103, x_{36} = 0.000030515, \\ x_{37} = -0.000030515, x_{38} = 0.0000152575, \\ x_{39} = -0.0000152575, x_{40} = 0.00000762875, \\ x_{41} = -0.00000762875 \end{aligned} \quad (9)$$

$$\begin{aligned} x(0), x(1), x(-1), x(2), x(-2), x(4), x(-4), x(0.5), \\ x(-0.5), x(0.25), x(-0.25), x(0.125), x(-0.125), \\ x(0.0625), x(-0.0625), x(0.03125), x(-0.03125), \\ x(0.015625), x(-0.015625), x(0.0078125), \\ x(-0.0078125), x(0.00390625), x(-0.00390625), \\ x(0.001953125), x(-0.001953125), x(0.000976565), \\ x(-0.000976565), x(0.0004882825), x(-0.0004882825), \\ x(0.0002441415), x(-0.0002441415), x(0.00012206), \\ x(-0.00012206), x(0.00006103), x(-0.00006103), \\ x(0.000030515), x(-0.000030515), x(0.0000152575), \\ x(-0.0000152575), x(0.00000762875), \\ x(-0.00000762875) \end{aligned} \quad (10)$$

Note that, in the interest of clarity, the exact equation used to calculate the evaluation points has been excluded. However, it can be inferred from the evaluation multiplication equation, Tables 3 and 4 in the Appendix. The evaluation points, represented as x and y in the evaluation step of the Toom-Cook 20.5-way multiplications design, are illustrated in the Appendix in Figure 5 and Figure 6, respectively. It is important to note that we propose the utilization of the free-division design as a means to mitigate the complexity associated with multiplication operations, particularly when dealing with high-degree multiplications, such as polynomial multiplication.

After the completion of the splitting and evaluation computation steps, the subsequent computational step for Toom-Cook 20.5-way involves recursive multiplication as the third step of computation in Toom-Cook. A single iteration of non-recursive point-wise multiplication for Toom-Cook

TABLE 2. Asymptotic Performance and Quantum Implementation Cost Multipliers Comparison.

No	Reference	Arithmetic Algorithm	Asymptotic Performance Analysis			Cost of Quantum Implementation of Multiplication			
			Qubit Count	Toffoli Count	Toffoli Depth	Qubit Count	Toffoli Count	Toffoli Depth	CNOT
1	Kepley and Steinwandt (2015, [33])	Karatsuba	$\mathcal{O}(n^{\log_2 3})$	$\mathcal{O}(n^{\log_2 3})$	-	-	-	-	$\mathcal{O}(n^{\log_2 3})$
2	Parent et al. (2017, [27])	Karatsuba	$\mathcal{O}(n^{1.427})$	$\mathcal{O}(n^{\log_2 3})$	$\mathcal{O}(n^{1.158})$	$n(\frac{3}{2})^{\frac{\log 2}{(2 \log 3 - \log 2)}} \log_2 n \approx n^{1.427}$	$42n^{\log_2 3}$	$n(\frac{3}{2})^{1 - \frac{\log 3}{(2 \log 3 - \log 2)}} \log_2 n \approx n^{1.158}$	-
3	Dutta et al. (2018, [7])	Toom-Cook-2.5	$\mathcal{O}(n^{1.404})$	$\mathcal{O}(n^{\log_6 16})$	$\mathcal{O}(n^{1.143})$	$n(\frac{8}{3})^{\frac{\log 16}{(6 \log 16 - \log 3)}} \log_6 n \approx n^{1.404}$	$49n^{\log_6 16}$	$n(\frac{8}{3})^{1 - \frac{\log 16}{(2 \log 16 - \log 3)}} \log_6 n \approx n^{1.143}$	-
4	Larasati et al.(2021, [8])	Toom-Cook 3	$\mathcal{O}(n^{1.35})$	$\mathcal{O}(n^2)$	$\mathcal{O}(n^{1.112})$	$n(\frac{5}{3})^{\frac{\log 5}{(2 \log 3 - \log 3)}} \log_3 n \approx n^{1.353}$	$8n^2 + 66n^{\log_3 5} - 72$	$n(\frac{5}{3})^{1 - \frac{\log 5}{(2 \log 3 - \log 3)}} \log_3 n \approx n^{1.112}$	-
5	Van Hoof (2020, [34])	Karatsuba	$3n$	$\mathcal{O}(n^{\log_2 3})$	-	-	-	-	$\mathcal{O}(n^2)$
6	Putranto et al. (2023, [14])	Karatsuba	$3n$	$\mathcal{O}(n^{\log_2 3})$	-	-	-	-	$\mathcal{O}(n^{\log_2 3})$
7	Putranto et al. (2023, [11])	Toom-Cook 2-way	$\mathcal{O}(n^{1.589})$	$\mathcal{O}(n^{\log_2 3})$	$\mathcal{O}(n^{1.217})$	$n(\frac{3}{2})^{\frac{\log 3}{(2 \log 3 - \log 2)}} \log_2 n \approx n^{1.589}$	$34n^{\log_2 3} - 32n$	$n(\frac{3}{2})^{1 - \frac{\log 3}{(2 \log 3 - \log 2)}} \log_2 n \approx n^{1.217}$	-
8	Putranto et al. (2023, [11])	Toom-Cook 4-way	$\mathcal{O}(n^{1.313})$	$\mathcal{O}(n^{\log_4 7})$	$\mathcal{O}(n^{1.09})$	$n(\frac{7}{4})^{\frac{\log 7}{(2 \log 7 - \log 4)}} \log_4 n \approx n^{1.313}$	$122n^{\log_4 7} - 160n$	$n(\frac{7}{4})^{1 - \frac{\log 7}{(2 \log 7 - \log 4)}} \log_4 n \approx n^{1.09}$	-
9	Putranto et al. (2023, [11])	Toom-Cook 8-way	$\mathcal{O}(n^{1.245})$	$\mathcal{O}(n^{\log_8 15})$	$\mathcal{O}(n^{1.0569})$	$n(\frac{15}{8})^{\frac{\log 15}{(2 \log 15 - \log 8)}} \log_8 n \approx n^{1.245}$	$112n^{\log_8 15} - 128n$	$n(\frac{15}{8})^{1 - \frac{\log 15}{(2 \log 15 - \log 8)}} \log_8 n \approx n^{1.0569}$	-
10	our	Toom-Cook 8.5-way	$\mathcal{O}(n^{1.236})$	$\mathcal{O}(n^{\log_8 17})$	$\mathcal{O}(n^{1.053})$	$n(\frac{17}{8})^{\frac{\log 17}{(2 \log 17 - \log 8)}} \log_8 n \approx n^{1.236}$	$186n^{\log_8 17} - 202n$	$n(\frac{17}{8})^{1 - \frac{\log 17}{(2 \log 17 - \log 8)}} \log_8 n \approx n^{1.053}$	-
11	our	Toom-Cook 10.5-way	$\mathcal{O}(n^{1.222})$	$\mathcal{O}(n^{\log_{11} 21})$	$\mathcal{O}(n^{1.047})$	$n(\frac{21}{11})^{\frac{\log 21}{(2 \log 21 - \log 11)}} \log_{11} n \approx n^{1.222}$	$206n^{\log_{11} 21} - 132n$	$n(\frac{21}{11})^{1 - \frac{\log 21}{(2 \log 21 - \log 11)}} \log_{11} n \approx n^{1.047}$	-
12	our	Toom-Cook 20.5-way	$\mathcal{O}(n^{1.186})$	$\mathcal{O}(n^{\log_{21} 41})$	$\mathcal{O}(n^{1.033})$	$n(\frac{41}{21})^{\frac{\log 41}{(2 \log 41 - \log 21)}} \log_{21} n \approx n^{1.186}$	$522n^{\log_{21} 41} - 540n$	$n(\frac{41}{21})^{1 - \frac{\log 41}{(2 \log 41 - \log 21)}} \log_{21} n \approx n^{1.033}$	-

$$T_n = 41^{\log_{21} n} + \sum_{i=0}^{\log_{21}(n)-1} \left[260n \left(\frac{41}{21} \right)^i \right] \quad (15)$$

The Toffoli cost of a recursive implementation can be determined by utilizing the geometric series calculation $\sum_{i=0}^{m-1} r^i = \frac{1-r^m}{1-r}$, as denoted by Equation 16. However, the result obtained from Equation 16 does not incorporate the conventional practice of uncomputation carried out in a quantum environment. In Equation 17, we incorporated the concept of an uncomputed operation as a strategy to reduce a significant increase in the previously determined cost. It is of utmost importance to acknowledge that the term ‘‘clean cost’’ utilized in the following equation aligns with the definitions put forth by Larasati et al. [8] and Putranto et al. [11].

$$\begin{aligned} T_n &= 41^{\log_{21} n} + 2604n \left(\frac{1 - (\frac{41}{21})^{\log_{21} n}}{1 - (\frac{41}{21})} \right) \\ &= n^{\log_{21} 41} + 260n \left(\frac{1 - n^{\log_{21}(\frac{41}{21})}}{1 - (\frac{41}{21})} \right) \end{aligned} \quad (16)$$

$$m = 261n^{\log_{21} 41} - 270n$$

$$T_{n(clean)} = 522n^{\log_{21} 41} - 540n \quad (17)$$

B. SPACE-TIME COMPLEXITY ANALYSIS

A prior work from Bennett in [30] introduced the technique of reversible pebble games as a means to measure asymptotic improvements in performance, specifically in relation to space consumption within the framework of space-time complexity analysis. The utilization of this technique is widespread in the field of reversible computing, allowing for the analysis of time and space complexity analysis possible and enabling time-efficient finite-space computing [31]. This approach will enable us to assess the difference in the cost of the efficiently optimized multiplication and compare it with the findings of prior investigations. The optimal cost of multiplication was determined by

implementing the methodologies described in the works of Parent et al. [27], Dutta et al. [7], Larasati et al. [8], and Putranto et al. [11].

The Toom-Cook 20.5-way algorithm involved the execution of 41 concurrent multiplications through a recursive process, resulting in the formation of a 21 structure. There are 41^l nodes of size $21^{-l}n$ for an input of size n at level l , and this input has a total circuit cost of $n(\frac{41}{21})^l$. The equation presented as Equation 18 represents the aggregate cost associated with the quinary tree. To ascertain the most suitable height k of a tree for achieving optimal performance, Equation 20 should be employed.

$$n \sum_{i=0}^N \left(\frac{41}{21} \right)^i, \quad N = \log_{21} n \quad (18)$$

$$n \sum_{i=0}^{N-k-1} \left(\frac{41}{21} \right)^i = \frac{1}{21^{N-k}} \sum_{i=0}^{k-1} \left(\frac{41}{21} \right)^i \quad (19)$$

In a manner identical to Equation 17, the identity of the geometric series enables us to locate the boundaries specified by Equation 20. Therefore, it is possible to decrease the amount of space required, as demonstrated by the qubit count equation presented in Equation 21. The result obtained from Equation 21, which is approximately equal to $\mathcal{O}(n^{1.245})$, is found to be smaller than the initially estimated space requirement determined by Equation 22, which is limited to the value $\mathcal{O}(n^{\log_8 15}) \approx \mathcal{O}(n^{1.30229})$.

$$k \leq \frac{N}{2 - \frac{\log 21}{\log 41}} \approx 0.847 \quad (20)$$

$$QC = \mathcal{O} \left(n \left(\frac{41}{21} \right)^{\left(\frac{\log 41}{2 \log 41 - 2 \log 21} \right) \log_{21} n} \right) \approx \mathcal{O}(n^{1.186}) \quad (21)$$

$$n \sum_{k=0}^{\log_{41} n - 1} \left(\frac{41}{21} \right)^k$$

TABLE 3. The Evaluation Multiplication.

Table with 2 columns: A and B. Each row contains a list of mathematical expressions representing multiplication results for different values of A and B. The expressions are complex polynomials involving variables x1 through x17 and various numerical coefficients.

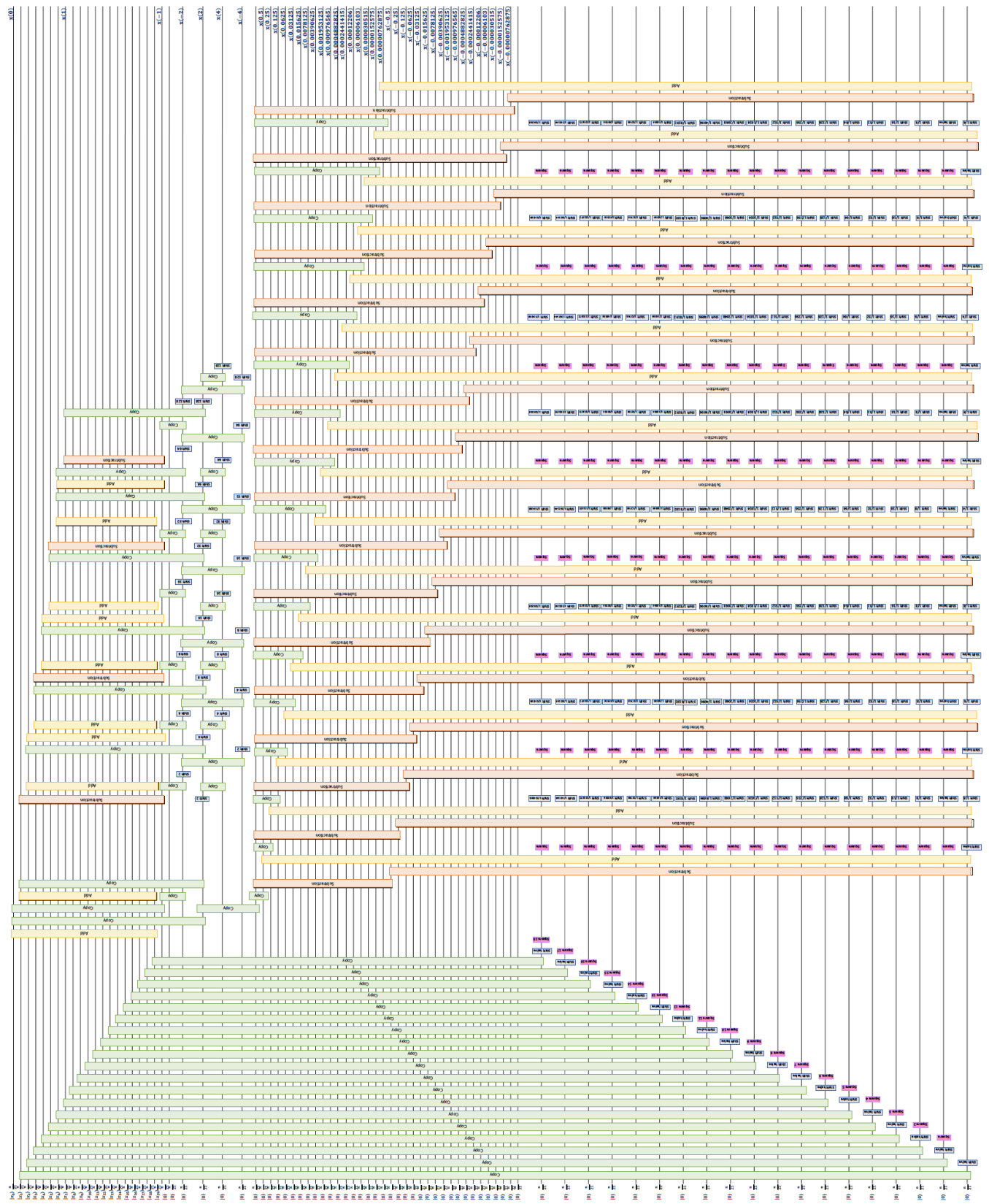


FIGURE 5. Evaluation point x.

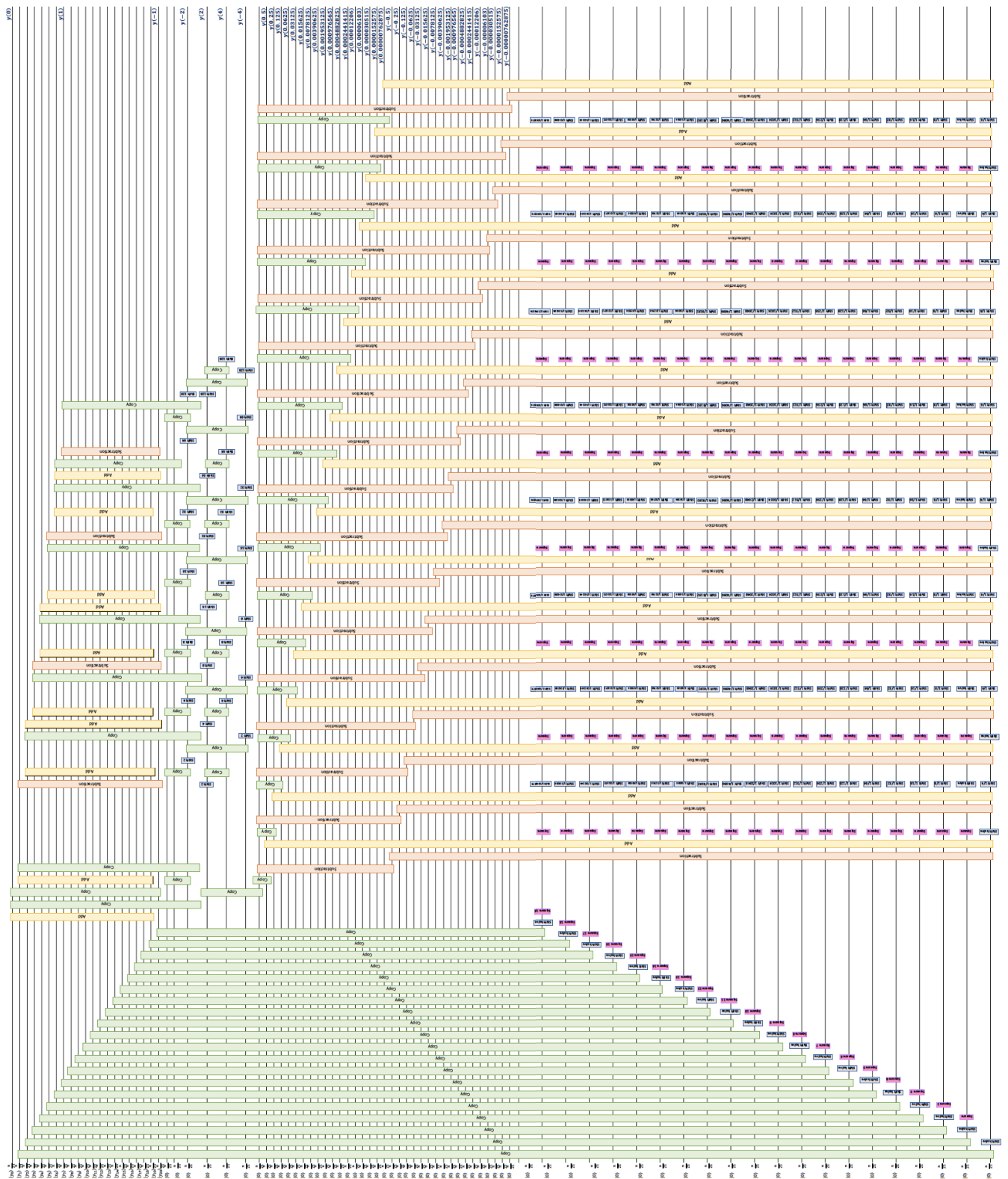


FIGURE 6. Evaluation point y.

a Toffoli count of $\mathcal{O}(n^{\log_9 17})$, and a Toffoli depth count of $\mathcal{O}(n^{1.053})$. The quantum implementation costs an identifiable cost, resulting in a certain number of $n(\frac{17}{9})^{\frac{\log 17}{(2\log 17 - \log 9)}} \log_9 n \approx n^{1.236}$ qubits, $186n^{\log_9 17} - 202n$ Toffoli count operations, and Toffoli depth with $n(\frac{17}{9})^{1 - \frac{\log 17}{(2\log 17 - \log 9)}} \log_9 n \approx n^{1.053}$.

Additionally, the analysis of the performance of the Toom-Cook 10.5 method can be reported that it demonstrates an asymptotic qubit count of $\mathcal{O}(n^{1.222})$, a Toffoli count of $\mathcal{O}(n^{\log_{11} 21})$, and a Toffoli depth count of $\mathcal{O}(n^{1.047})$. The cost of implementation in quantum is resulting in a quantity of $n(\frac{21}{11})^{\frac{\log 21}{(2\log 21 - \log 11)}} \log_{11} n \approx n^{1.222}$ qubits. Additionally, the computational Toffoli count overhead is given by $206n^{\log_{11} 21} - 132n$. The Toffoli depth can be approximated by the expression $n(\frac{21}{11})^{1 - \frac{\log 21}{(2\log 21 - \log 11)}} \log_{11} n \approx n^{1.047}$.

The optimal outcome of this endeavor is the computation result of high- and half-degree quantum multiplication, particularly utilizing the Toom-Cook 20.5-way method, necessitates a qubit count with a complexity of $\mathcal{O}(n^{1.186})$, a toffoli count of $\mathcal{O}(n^{\log_{21} 41})$, and a toffoli depth count with a complexity of $\mathcal{O}(n^{1.033})$. The cost of quantum implementation for this multiplication is $n(\frac{41}{21})^{\frac{\log 41}{(2\log 41 - \log 21)}} \log_{21} n \approx n^{1.186}$ qubits, $522n^{\log_{21} 41} - 540n$ Toffoli count operations, and Toffoli depth with $n(\frac{41}{21})^{1 - \frac{\log 41}{(2\log 41 - \log 21)}} \log_{21} n \approx n^{1.033}$.

VII. DISCUSSION

An extensive analysis from different multiplication techniques revealed that, despite a high degree, the Toom-Cook 20.5-way multiplier achieves reduced resource utilization. It is crucial to recognize that the effectiveness of the recently developed Toom-Cook 20.5-way algorithm exceeds that of the currently employed multiplication methods, particularly the Toom-Cook 4-way technique utilized in algorithms based on lattice algorithms during the post-quantum cryptography era.

The role of quantum cryptanalysis is significant in identifying potential weaknesses in classical cryptographic systems when confronted with quantum attacks. Additionally, it plays a crucial role in evaluating the security of post-quantum cryptographic algorithms to determine their capacity to provide a robust defense against quantum adversaries. For future work, the design multiplication needs to be incorporated into the real implementation of the PQC algorithm or notable cryptanalysis circuit, for example, utilizing the Shor algorithm technique. Moreover, it is crucial to improve the implementation by offering a more thorough explanation of multiplication-based attacks using SCA or CPA techniques, as well as measuring the potential for errors in quantum design for high-degree multiplication.

VIII. CONCLUSION

This work extensively analyzed the Toom-Cook algorithm and employed advanced techniques, including division-free approaches, tree-structured methods, opposite points, inverses, and cost-exploiting symmetries, to devise efficient

strategies for implementing high- and half-degree quantum multiplication. The specific focus was on the Toom-Cook 8.5-way, 10.5-way, and 20.5-way degree multiplication approaches. The implementation outcome demonstrates exceptional performance in comparison to the existing cutting-edge outcomes, as determined by analyzing the asymptotic performance and the cost associated with quantum implementation, particularly in terms of qubit counts, Toffoli counts, and Toffoli depth. Our analysis indicates that the Toom-Cook 20.5-way architecture demonstrates the highest level of efficiency in utilizing quantum resources. This is observable from the qubit counts of the $n^{1.186}$, $522n^{\log_{21} 41} - 540n$ Toffoli counts, and the $n^{1.033}$ Toffoli depth required. This outcome represents a significant advancement when compared to the previous approach of employing classical schoolbook multiplication with a complexity of $\mathcal{O}(n^2)$ or utilizing quantum design for Karatsuba multiplication with a complexity of $\mathcal{O}(n^{\log_2(3)})$. Moreover, the result demonstrates a significant improvement in comparison to the presently employed Toom-k-way, Toom-Cook 2.5-way, Toom-Cook 8.5-way, and 10.5-way approaches.

APPENDICES

See Tables 3 and 4 and Figures 5 and 6.

REFERENCES

- [1] A. L. Toom, "The complexity of a scheme of functional elements realizing the multiplication of integers," *Soviet Math. Doklady*, vol. 3, no. 4, pp. 714–716, 1963.
- [2] S. A. Cook and S. O. Aanderaa, "On the minimum computation time of functions," *Trans. Amer. Math. Soc.*, vol. 142, pp. 291–314, Aug. 1969.
- [3] J. M. Bermudo Mera, A. Karmakar, and I. Verbaauwhede, "Time-memory trade-off in Toom–Cook multiplication: An application to module-lattice based cryptography," *IACR Trans. Cryptograph. Hardw. Embedded Syst.*, vol. 2020, no. 2, pp. 222–244, 2020.
- [4] Y. Li, J. Zhu, Y. Huang, Z. Liu, and M. Tang, "Single-trace side-channel attacks on the Toom–Cook: The case study of saber," *IACR Trans. Cryptograph. Hardw. Embedded Syst.*, vol. 2022, no. 4, pp. 285–310, 2022. [Online]. Available: <https://doi.org/10.46586/tches.v2022.i4.285-310>
- [5] A. Zaroni, "Toom–Cook 8-way for long integers multiplication," in *Proc. 11th Int. Symp. Symbolic Numeric Algorithms Scientific Comput.*, Sep. 2009, pp. 54–57.
- [6] M. Bodrato, "High degree Toom'n'half for balanced and unbalanced multiplication," in *Proc. IEEE 20th Symp. Comput. Arithmetic*, Jul. 2011, pp. 15–22.
- [7] S. Dutta, D. Bhattacharjee, and A. Chattopadhyay, "Quantum circuits for Toom–Cook multiplication," *Phys. Rev. A, Gen. Phys.*, vol. 98, no. 1, Jul. 2018, Art. no. 012311.
- [8] H. T. Larasati, A. M. Awaludin, J. Ji, and H. Kim, "Quantum circuit design of Toom 3-way multiplication," *Appl. Sci.*, vol. 11, no. 9, p. 3752, Apr. 2021.
- [9] A. Ghosh, J. M. B. Mera, A. Karmakar, D. Das, S. Ghosh, I. Verbaauwhede, and S. Sen, "A 334 μW 0.158 mm² ASIC for post-quantum key-encapsulation mechanism saber with low-latency striding Toom–Cook multiplication authors version," 2023, *arXiv:2305.10368*.
- [10] C. Mujdei, L. Wouters, A. Karmakar, A. Beckers, J. M. B. Mera, and I. Verbaauwhede, "Side-channel analysis of lattice-based post-quantum cryptography: Exploiting polynomial multiplication," *ACM Trans. Embedded Comput. Syst.*, vol. 2022, pp. 1–26, Nov. 2022.
- [11] D. S. C. Putranto, R. W. Wardhani, H. T. Larasati, and H. Kim, "Space and time-efficient quantum multiplier in post quantum cryptography era," *IEEE Access*, vol. 11, pp. 21848–21862, 2023.
- [12] M. Bodrato, "Towards optimal Toom–Cook multiplication for univariate and multivariate polynomials in characteristic 2 and 0," in *Proc. Int. Workshop Arithmetic Finite Fields*. Berlin, Germany: Springer, 2007, pp. 116–133.

- [13] Z. Gu and S. Li, "A division-free Toom–Cook multiplication-based Montgomery modular multiplication," *IEEE Trans. Circuits Syst. II, Exp. Briefs*, vol. 66, no. 8, pp. 1401–1405, Aug. 2019.
- [14] D. S. C. Putranto, R. W. Wardhani, H. T. Larasati, J. Ji, and H. Kim, "Depth-optimization of quantum cryptanalysis on binary elliptic curves," *IEEE Access*, vol. 11, pp. 45083–45097, 2023.
- [15] M. Bodrato and A. Zanzi, "Integer and polynomial multiplication: Towards optimal Toom–Cook matrices," in *Proc. Int. Symp. Symbolic Algebr. Comput.*, Jul. 2007, pp. 17–24.
- [16] G. Alagic, D. Apon, D. Cooper, Q. Dang, T. Dang, J. Kelsey, J. Lichtinger, C. Miller, D. Moody, R. Peralta, R. Perlner, A. Robinson, D. Smith-Tone, and Y.-K. Liu, "Status report on the third round of the NIST post-quantum cryptography standardization process," U.S. Dept. Commerce, NIST, Gaithersburg, MD, USA, Tech. Rep. NIST IR 8413, 2022.
- [17] J. Bos, L. Ducas, E. Kiltz, T. Lepoint, V. Lyubashevsky, J. M. Schanck, P. Schwabe, G. Seiler, and D. Stehle, "CRYSTALS-Kyber: A CCA-secure module-lattice-based KEM," in *Proc. IEEE Eur. Symp. Secur. Privacy (EuroS&P)*, Apr. 2018, pp. 353–367.
- [18] V. Lyubashevsky, "Fiat–Shamir with aborts: Applications to lattice and factoring-based signatures," in *Proc. Int. Conf. Theory Appl. Cryptol. Inf. Secur.* Berlin, Germany: Springer, 2009, pp. 598–616.
- [19] D. Micciancio and O. Regev, "Post-quantum cryptography, chapter lattice-based cryptography," *Computing*, vol. 85, nos. 1–2, pp. 105–125, 2008.
- [20] Z. Liu, K. R. Choo, and J. Grossschadl, "Securing edge devices in the post-quantum Internet of Things using lattice-based cryptography," *IEEE Commun. Mag.*, vol. 56, no. 2, pp. 158–162, Feb. 2018.
- [21] M. Bisheh-Niasar, R. Azarderakhsh, and M. Mozaffari-Kermani, "Instruction-set accelerated implementation of CRYSTALS-Kyber," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 68, no. 11, pp. 4648–4659, Nov. 2021.
- [22] M. Bisheh-Niasar, R. Azarderakhsh, and M. Mozaffari-Kermani, "High-speed NTT-based polynomial multiplication accelerator for post-quantum cryptography," in *Proc. IEEE 28th Symp. Comput. Arithmetic (ARITH)*, Jun. 2021, pp. 94–101.
- [23] J. M. Pollard, "The fast Fourier transform in a finite field," *Math. Comput.*, vol. 25, no. 114, pp. 365–374, 1971.
- [24] A. A. Karatsuba and Y. P. Ofman, "Multiplication of many-digital numbers by automatic computers," in *Doklady Akademii Nauk*, vol. 145, no. 2. Moscow, Russia: Russian Academy of Sciences, 1962, pp. 293–294.
- [25] P. Koehler, J. Jaffe, and B. Jun, "Differential power analysis," in *Proc. Annu. Int. Cryptol. Conf.* Berlin, Germany: Springer, 1999, pp. 388–397.
- [26] D. S. Roche, "Space- and time-efficient polynomial multiplication," in *Proc. Int. Symp. Symbolic Algebr. Comput.*, Jul. 2009, pp. 295–302.
- [27] A. Parent, M. Roetteler, and M. Mosca, "Improved reversible and quantum circuits for Karatsuba-based integer multiplication," in *Proc. 12th Conf. Theory Quantum Comput., Commun., Cryptogr. (TQC)*, Cham, Switzerland: Springer, 2017, pp. 7:1–7:15.
- [28] C. Gidney, "Asymptotically efficient quantum Karatsuba multiplication," 2019, *arXiv:1904.07356*.
- [29] G. Banegas, D. J. Bernstein, I. van Hoof, and T. Lange, "Concrete quantum cryptanalysis of binary elliptic curves," *IACR Trans. Cryptograph. Hardw. Embedded Syst.*, vol. 2021, no. 1, pp. 451–472, 2020. [Online]. Available: <https://doi.org/10.46586/tches.v2021.i1.451-472>
- [30] C. H. Bennett, "Time/space trade-offs for reversible computation," *SIAM J. Comput.*, vol. 18, no. 4, pp. 766–776, Aug. 1989.
- [31] R. Král'ovič, "Time and space complexity of reversible pebbling," in *Proc. Int. Conf. Current Trends Theory Pract. Comput. Sci.* Berlin, Germany: Springer, 2001, pp. 292–303.
- [32] M. Amy, "Algorithms for the optimization of quantum circuits," M.S. thesis, Dept. Comput. Sci., Quantum Inf., Univ. Waterloo, Waterloo, ON, Canada, 2013.
- [33] S. Kepley and R. Steinwandt, "Quantum circuits for \mathbb{F}_{2^n} -multiplication with subquadratic gate count," *Quantum Inf. Process.*, vol. 14, no. 7, pp. 2373–2386, May 2015.
- [34] I. van Hoof, "Space-efficient quantum multiplication polynomials for binary finite fields with sub-quadratic Toffoli gate count," *Quantum Inf. Comput.*, vol. 20, nos. 9–10, pp. 721–735, Aug. 2020, doi: [10.26421/qic20.9-10-1](https://doi.org/10.26421/qic20.9-10-1).



RINI WISNU WARDHANI (Graduate Student Member, IEEE) received the Diploma degree in cryptography from the National Cryptography Institute, the bachelor's degree in electrical engineering from Pancasila University, and the master's degree in electrical engineering from the University of Indonesia. She is currently pursuing the Ph.D. degree with the Department of Computer Science and Engineering, Pusan National University, South Korea. She was a Researcher with the National Cyber and Crypto Agency (BSSN), Indonesia. She was a Lecturer with the Hardware Security Program, National Cyber and Crypto Polytechnic (Poltek SSN), Indonesia. Her research interests include hardware security, information security, cryptography, and quantum computing.



DEDY SEPTONO CATUR PUTRANTO (Member, IEEE) received the Diploma degree in cryptography from the National Cryptography Institute, the bachelor's degree in electrical engineering from Pancasila University, Indonesia, the master's degree in electrical engineering from the University of Indonesia, and the Ph.D. degree in nanovision technology from Shizuoka University, Hamamatsu, Japan. He is currently a Postdoctoral Researcher with Pusan National University. He has experience as a Researcher and a Lecturer with the National Cyber and Crypto Agency (BSSN), Indonesia, and the National Cyber and Crypto Polytechnic (Poltek SSN), Indonesia. His research interests include hardware security, information security, cryptography, nanotechnology, and quantum computing.



HOWON KIM (Member, IEEE) received the bachelor's degree from the Kyungpook National University (KNU), and the Ph.D. degree from the Pohang University of Science and Technology (POSTECH). He is currently a Professor with the Department of Computer Science and Engineering, the Chief of Energy Internet of Things (IoT) with the IT Research Center (ITRC), and the Chief of the Information Security Education Center (ISEC), Pusan National University (PNU). Before joining PNU, he was with the Electronics and Telecommunications Research Institute (ETRI) as a Team Leader for ten years beginning, in December 1998. He was a Visiting Postdoctoral Researcher with the Communication Security Group (COSY), Ruhr-University Bochum, Germany, from July 2003 to June 2004.

...