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RESEARCH ARTICLE

Fault Current Improved Grey Slope Relational Analysis Based Unit Protection Scheme for DC Distribution Lines

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ABSTRACT Multi-terminal flexible DC distribution line faults develop rapidly, and power electronic equipment has a poor ability to withstand overcurrent. To solve the aforementioned problem, a DC line unit protection scheme based on improved Grey Slope Relational Analysis (GSRA) of fault currents is proposed to address the issue of achieving a balance between the reliability and speed of relay protection for DC distribution lines. This scheme uses the fault current measured at both ends of the line under various operating conditions, and calculates the correlation between the current at both ends of the positive and negative lines using the slope distance and polarity of the current increments. Based on this, GSRA is used to analyze the correlation of fault signals so that line faults can be eliminated quickly. This protection scheme allows the selectivity of traditional unit protection to be improved. Simulations using PSCAD/EMTDC have shown that this method can achieve fault discrimination and fault pole selection by simultaneously measuring the current at both ends of the line. This fast protection action is not affected by the line distribution capacitance and converter blocking, strong resistance to transition resistance and noise interference, and reliable identification under heavy loads and high-resistance ground faults.

INDEX TERMS DC distribution system, current increment, slope relational analysis, unit protection, fault detection.

N negative line.

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- *IXy* measured current at the y-pole of the X-end of the line.
- PPG positive pole-to-ground.
- NPG negative pole-to-ground.
- PP pole-to-pole.
- ΔI current increment sequence.
- σ the slope distance of positive and negative pole lines.
- *r* the slope correlation sequence.
- γ the slope relation.

I. INTRODUCTION

In recent years, there has been an increasing demand for renewable energy worldwide, as the use of traditional fossil fuels has contributed to global warming, causing serious

environmental pollution [\[1\],](#page-10-0) [\[2\]. A](#page-10-1) flexible multi-terminal DC (MTDC) distribution system with low line losses, high transmission capacity, and efficient access to distributed energy sources has become the key to solving current power distribution problems and the development of power systems in the immediate future [\[3\],](#page-10-2) [\[4\]. H](#page-10-3)owever, the damping of the flexible DC distribution network is low, and the overcurrent generated by the fault may destroy the power electronics within milliseconds, and the traditional protection scheme with low speed is no longer applicable, which puts higher requirements on relay protection [\[5\],](#page-10-4) [\[6\].](#page-10-5) In addition, line faults account for the vast majority of DC system faults, and the fast and reliable identification of faulty lines is a key and difficult point in the development of relay protection technology for DC distribution systems [\[7\],](#page-10-6) [\[8\].](#page-11-0)

At the present stage, the protection scheme for DC lines is mainly divided into two categories: non-unit protection and unit protection. Non-unit protection measures electrical quantities at the one end of the DC line without transferring the measured values to the other end of the line and determines faults directly based on the measured values at the single end, which has a high speed. In [\[9\], a](#page-11-1) fast fault recognition scheme based on measured current was proposed, but this scheme could not withstand high-intensity noise. References [\[10\]](#page-11-2) and [\[11\]](#page-11-3) constructed protection schemes used the traveling wave of the initial current and the voltage drop of the initial current limiting reactor (CLR), respectively, and such methods require high sampling frequency. References [\[12\]](#page-11-4) and [\[13\]](#page-11-5) used the current fault component of the CLR and the voltage integral of the CLR to discriminate line faults and achieve fast fault isolation, respectively, such methods require additional measurement points and are limited by the current limiting effect of the CLR, which cannot withstand higher transition resistance. References[\[14\]](#page-11-6) and [\[15\]](#page-11-7) analyzed aspects such as line frequency parameter models, fault characteristics, and quantify the sensitivity of the flexible DC fault characteristics for different influencing factors, which have complicated threshold rectification of such protection schemes. In general, non-unit protection has the disadvantages of complex threshold rectification, reliance on simulation, inability to tolerate high transition resistance and noise interference, and large influence by system operation modes.

Unit protection treats the DC line as a unit, measures electrical quantities at both ends of the line, and compares the data at each end through the communication channel to decide whether to operate for protection, unit protection has a very high selectivity and the simpler threshold adjustment makes the reliability of unit protection relatively higher. There is a relatively large amount of literature on unit protection of DC lines. For example, in [\[16\], t](#page-11-8)he Bhattacharyya distance method was used to determine the fault location based on the conclusion that the measured current waveform characteristics were different on both sides of the line in the case

of internal and external faults. In [\[17\], t](#page-11-9)he Pearson correlation coefficients were used to realize the recognition of faulty lines based on the difference in voltage waveforms on both sides of the line at different fault locations. In [\[18\],](#page-11-10) the protection achieved fault recognition by calculating the transient voltage ratio of the CLR on both sides of the line. In [\[12\], a](#page-11-4)n Event-based protection scheme that requires fewer transmission data and higher speed was proposed. Protection schemes using the difference of fault components of line voltage and current under different location faults were proposed in [\[19\]](#page-11-11) and [\[20\], r](#page-11-12)espectively, such methods have high communication requirements and poor interference immunity. The current differential protection proposed in [\[21\]](#page-11-13) and [\[22\]](#page-11-14) can recognize faults quickly and accurately, however, they are susceptible to interference by transition resistance and line-distributed capacitance. References [\[23\]](#page-11-15) and [\[24\]](#page-11-16) used traveling waves for fault recognition and pole selection, such methods require extremely high sampling frequency, and short DC distribution lines make the cost of sampling equipment increase significantly, whether traveling waves sampling equipment for transmission system are suitable for distribution system is still open to discussion. Unit protection has the problems of dependence on communication and lower speed, how to improve the speed as much as possible while ensuring the reliability and selectivity of unit protection has become the focus of research on unit protection.

In response to the above problems, this paper proposes a protection scheme based on an improved Grey Slope Relational Analysis (GSRA) of fault currents. First, we calculated the current increments on either side of the positive and negative DC lines. Then, the correlation of the currents at both ends of the positive and negative lines was calculated by extracting their slope distances and polarities. Finally, fault recognition and fault pole selection were realized using GSRA. The protection is lower for communication requirements, the principle is simple, it is unaffected by the transition resistance and loads current, and it has better tolerance to changes in system operation modes and disturbances such as noise. The protection scheme without initiation criteria is not affected by converter blocking and has higher speed and reliability. The feasibility of the protection scheme was verified by simulating and analyzing different working conditions using the PSCAD/EMTDC platform.

II. FLEXIBLE DC DISTRIBUTION SYSTEM TOPOLOGY

The topology of the multi-terminal ring flexible DC distribution system is illustrated in Fig. [1.](#page-2-0)

System primarily consists of AC systems, modular multilevel converter (MMC), voltage source converter (VSC), dual active bridge (DAB) DC converters, wind power systems, photovoltaic power systems, AC and DC loads, and DC distribution lines. The individual ports of the system are numbered as shown in the figure, where T1 and T2 are half-bridge submodules based on the MMC, which are connected to the AC system through a 110/10 kV transformer.

FIGURE 1. Multi-terminal flexible DC distribution system topology.

The system adopts a primary-secondary control strategy, with the true-bipolar MMC on port T1 as the master control station, using constant DC voltage and constant reactive power control. The true-bipolar MMC on port T2 is the slave control station using constant active and constant reactive power control. Port T3 is a three-phase, two-level VSC using constant AC-side voltage control, which connects the distribution network to the AC load. Pseudo-bipolar MMCs on ports T4 and T5 with islanding control and simultaneous access to AC loads and wind power systems. Ports T6 and T7 are DC transformers (DCT) that connect the distribution network to the DC load and PV system through a dual DAB cascade in the Input Series Output Parallel (ISOP) topology [\[25\],](#page-11-17) [\[26\]. D](#page-11-18)AB is controlled by single-phase shift control, which controls the low-voltage side voltage.

The ISOP-ISOP line is taken as the research object, and multiple fault points are set. Where *f* 1 is set to 10% of the L2 line length, $f2$ is set to 50 % of the L2 line length, $f3$ is set to 90% of the L2 line length, *f* 4 is set to the beginning of the L3 line, and *f* 5 is set to the end of the L1 line. When a fault occurs in the DC line of the system, the circuit breakers on both sides of the faulty line will isolate the fault in a timely manner, and the ring topology de-ringing for chain power, which ensures the reliability of the system power supply, a unit protection scheme based on improved GSRA of fault currents is proposed on this basis.

III. ANALYSIS OF FAULT CHARACTERISTICS OF DC DISTRIBUTION LINES

When a fault occurs on a DC distribution line, the DC voltage at the fault point drops significantly, and both sides of the line generate a fault current that flows simultaneously to the fault point. The fault current in the faulty initial stage mainly includes the discharge current of the large capacitor

FIGURE 2. DC line current direction characteristics

connected in parallel on the DC side of the VSC, DCT, and the MMC bridge arm. When the fault locations and types are different, the polarity and rising speed of the current measured at the measurement points at both ends of the line are also different. To meet the requirements of protection speed, this study takes line L2 as the object of study and analyses the fault information of the line current in the fault detection phase before converter blocking.

When the system is operating normally, make the positive direction of the current is the bus flow to the line. The equivalent network of the DC line is shown in Fig. 2.*R^f* is the transition resistance, side H is the head of the line, and side E is the end of the line; P for positive lines and N for negative lines; I_{Hp} and I_{Ep} are the positive line head measurement point current and positive line end measurement point current, respectively; *IHn* and *IEn* are the negative line head measurement point current and negative line end measurement point current, respectively. Meanwhile, the currents at the two ends of the positive line are in opposite directions, and the currents at the two ends of the negative line are also in opposite directions, confirming that the currents in the positive and negative lines are traversing currents.

A. ANALYSIS OF FAULT CURRENT POLARITY

When a single-pole grounded short-circuit fault occurs within the system (positive pole-to-ground (PPG) fault as an

FIGURE 3. Directional characteristics of fault current in the DC line area: (a) PPG fault, (b) PP fault.

example, a negative pole-to-ground (NPG) fault is similar to PPG), as shown in Fig. $3(a)$, the current at the fault pole flows simultaneously from both sides of the line to the fault point, and the head and end currents are positive and rapidly increasing, with the same polarity; the current of the healthy pole flows from the E side of the line to the H side of the line, and the currents at the head and the end are one positive and one negative with opposite polarity. When a PP shortcircuit fault occurs within the system, as shown in Fig. [3\(b\),](#page-3-0) the current in the positive line flows simultaneously from both sides of the line to the fault point, and the currents at the head and the end are positive and of the same polarity. Negative line current flows from the fault point to both sides of the line, and the currents at the head and the end are negative and of the same polarity.

The equivalent network of the DC line when an external single-pole ground short-circuit fault occurs in the system is shown in Fig. $4(a)$. The fault pole and healthy pole currents flow from the head of the line to the end of the line and are traversing currents, the head and end currents show positive and negative currents, respectively, with opposite polarity. The equivalent network of the DC line when an external PP short-circuit fault occurs in the system is shown in Fig. [4\(b\).](#page-3-1) The currents at both ends of the positive line flow from the head of the line to the end of the line and are traversing currents, which have the opposite polarity of one positive and one negative. The currents at both ends of the negative line flow from the end of the line to the head of the line and are traversing currents, which have the opposite polarity of one negative and one positive.

The polarity characteristics of fault currents under different operating conditions are listed in Table [1.](#page-3-2) It is found that at least one pole of the line has the same current polarity at both

FIGURE 4. Directional characteristics of fault current outside the DC line area: (a) PPG fault, (b) PP fault.

TABLE 1. Polarity characteristics of fault currents under different operating conditions.

	Positive line		Negative line	
Operating conditions	H side	E side	H side	E side
Internal PPG fault				
Internal NPG fault				
Internal PP fault				
Positive external fault			$+/-$	-/+
Reverse external fault		--		

ends when a fault occurs within the system; no current at either pole of the line has the same polarity when an external fault occurs in the system. On this basis, traditional longitudinal protection based on DC polarity has been proposed [\[20\],](#page-11-12) with extremely rapid fault current change characteristics for fast fault discrimination; however, its reliability is relatively low, especially when affected by noise disturbance and load current.

B. CHARACTERIZATION OF FAULT CURRENT INCREMENTS

Because a serious overcurrent phenomenon would occur after a DC system line fault, the information contained in the fault current also includes information besides the polarity such as the increment in the current. When a PG fault occurs in the DC line, the fault currents at both ends of the fault line grow rapidly in the same direction at approximately the same rate, and their increments are highly positively correlated, while the fault currents at both ends of the healthy line grow slowly at approximately the same rate, and their increments are negatively correlated. When a PP fault occurs in the DC line, the fault currents of both positive and negative lines grow rapidly in the same direction at approximately the same rate, and the current increments at both ends of

the positive and negative lines are highly positively correlated. When an external fault occurs in the DC line, the fault currents at both ends of either pole line grow at the same rate in reverse, and the current increments at both ends of the positive and negative pole lines are negatively correlated. To solve the problem of simultaneously taking into account the unit protection speed and reliability, this study calculates the GSRA of the fault current based on the increments and the slope of the fault current, combined with the polarity characteristics of the fault current. A unit protection scheme based on improved GSRA of fault currents is proposed.

IV. THE UNIT PROTECTION BASED ON CURRENT INCREMENTAL GSRA

A. GSRA

Grey Relational Analysis (GRA) is a dynamic series processing method that is widely used in many fields. Among them, GSRA can delve deeply into the data features of time-series curves, which are consistent with the characteristics of relay protection. Considering that the traditional GSRA method cannot respond to positive and negative correlations and has certain defects, this study improves on it to better adapt to the changing characteristics of fault current increments. The specific calculation steps of the improved GSRA are described in detail below [\[27\].](#page-11-19)

To define the current increment sequence, the number of sampling points of each sequence defined in this paper needs to take into account the speed and reliability requirements of protection, positive and the negative lines on both sides of the fault current incremental expression are as follows [\(1\).](#page-4-0)

$$
\begin{cases}\n\Delta I_{\text{xp}(j,k)} = I_{\text{xp}(j,k)} - I_{\text{xp}(j,k-1)} \\
\Delta I_{\text{xn}(j,k)} = I_{\text{xn}(j,k)} - I_{\text{xn}(j,k-1)}\n\end{cases} \tag{1}
$$

In the above (1) , *j* is the number of the incremental sequence, *k* is the number of different sampling points in each cycle, $k \in [2, N]$, *N* is the total number of sampling points in each cycle, and *N* is an integer, determined by the condition of the equipment sampled by the system. $\Delta I_{xp(i,k)}$ and $\Delta I_{xn(i,k)}$ are the increments of the positive and negative currents on the χ side (χ = H, E represents the head and end of the line in turn) of the DC line, respectively.

The sequences of the positive and negative lines current increments are averaged, as shown in [\(2\).](#page-4-1)

$$
\begin{cases}\n\Delta \bar{I}_{\text{xp}(j)} = \frac{1}{N-1} \left(\sum_{k=2}^{N} \left| \Delta I_{\text{xp}(j,k)} \right| \right) \\
\Delta \bar{I}_{\text{xn}(j)} = \frac{1}{N-1} \left(\sum_{k=2}^{N} \left| \Delta I_{\text{xn}(j,k)} \right| \right)\n\end{cases} (2)
$$

In [\(2\),](#page-4-1) $\Delta I_{x p_{(j)}}$ and $\Delta I_{x n_{(j)}}$ are the mean values of *x* side increments of positive and negative pole lines.

The slope distance of the sequence of positive and negative current increments is calculated, as shown in [\(3\).](#page-4-2)

$$
\begin{cases}\n\sigma_{p(j,k)} = \frac{[\Delta I_{Hp(j,k)} - \Delta I_{Hp(j,1)}]}{|\Delta I_{Hp(j)}|} - \frac{[\Delta I_{Ep(j,k)} - \Delta I_{Ep(j,1)}]}{|\Delta I_{Ep(j)}|} \\
\sigma_{n(j,k)} = \frac{[\Delta I_{Hn(j,k)} - \Delta I_{Hn(j,1)}]}{|\Delta I_{Hn(j)}|} - \frac{[\Delta I_{En(j,k)} - \Delta I_{En(j,1)}]}{|\Delta I_{En(j)}|}\n\end{cases}
$$
\n(3)

In [\(3\),](#page-4-2) $\sigma_{p(j,k)}$ and $\sigma_{n(j,k)}$ are the slope distance of positive and negative pole lines.

The slope correlation sequence $r_{y(i,k)}$ for the currents at both ends of the positive and negative lines is calculated, as shown in [\(4\).](#page-4-3)

$$
r_{y(j,k)} = \pm \frac{1}{1 + |\sigma_{y(j,k)}|}
$$
 (4)

In [\(4\),](#page-4-3) *y* is either p or n, indicating positive and negative lines, respectively. To incorporate the polarity characteristics of the current into the algorithm, the sign is taken as positive when $\Delta I_{xp(i,k)}$ and $\Delta I_{xn(i,k)}$ have the same or opposite sign, and the sign is taken as positive when the product of the two is 0, otherwise, the sign takes the negative.

As a measure of the correlation of two time series by calculating the mean of the slope relation series, γ_y is the slope relation, as shown in (5) .

$$
\gamma_{y} = \frac{1}{N-1} \sum_{k=2}^{N} r_{y(j,k)}
$$
\n(5)

This method improves the traditional GSRA by quantitatively analyzing the dynamic development process of the system while retaining the polarity characteristics of the time series to examine the degree of influence among the factors of the system or the contribution of the factors to the main behavior of the system. In terms of relay protection, this method preserves both the polarity characteristics of the postfault currents and the characteristics of the growth rate and trend of the currents at both ends of different lines after the fault. If the two current series are perfectly positively correlated, then $\gamma_y = 1$, the opposite polarity and uncorrelated characteristics of the two current sequences reduce it. It can be seen that the slope relation can accurately identify the trend of the two curves and calculate the degree of correlation between them.

B. PROTECTION SCHEME DESIGN

The protection scheme described in this paper is derived from a combination of fault current GSRA and fault current polarity characteristics. This is reflected in the numerical calculation and symbolic parts in (4) . The former can synthesize a set of average correlation data over a sampling period with higher protection reliability but a comparatively lower speed, and the latter can react to the fault characteristics for the first time when the current produces a sudden change after the fault, and the protection has higher speed, but the reliability

is lower. The combination of the two can improve the speed and reliability of the fault-current GSRA, and the main part of the protection scheme is described below.

1) FAULT RECOGNITION

When the system operates normally or an external fault occurs, the current flowing through the positive and negative lines in the zone is the traversing current, with opposite polarity. The increments are one positive and one negative, and the calculated correlation has negative and small absolute values, so the protection would not be operated. When an internal fault occurs in the system, the current flowing through the fault pole is a non-traversing current with the same polarity and increments, and the calculated correlation is approximately 1, which is determined as an internal fault.

When the sign part of [\(4\)](#page-4-3) is excluded, γ _v = 1 if the two time series are positively correlated and $\gamma_y = 0$ if the two time series are negatively correlated. When an internal fault occurs in the system, the currents at both ends of the fault pole line are positively correlated, and the calculated value of the slope correlation is approximated as 1. Regardless of the type of external fault in the system, the current at both ends of the same pole line is always negatively related, and the calculated value of the slope correlation is close to 0. The characteristics of slope correlation under different operating conditions are listed in Table [2.](#page-5-0) When an external fault occurs, γ_p and γ_n are both less than 1 and close to 0, this is used as the criterion for fault routing to escape the maximum γ _y setting rectification value γ*set* .

2) FAULT POLE SELECTION

To improve the protection reliability, the fault pole selection scheme still adopts the GSRA characteristics of the fault current as the primary criterion and the fault current polarity as the secondary criterion for comprehensive development, and the fault pole selection scheme with improved slope correlation characteristics is discussed below.

Theoretically, the GSRA can be used to determine and select the fault line directly, but the above correlation is the calculated value of the ideal state. Mutual transformer measurement errors, noise, and other disturbances are simultaneously caused by the actual operation of the system at the same time. In addition, the instantaneous fault current reaches its peak within a few milliseconds after a fault occurs in the DC distribution system, which causes serious damage to the system. Combined with the requirements of the extensive

TABLE 3. Parameters of the system.

literature on the protection time frame, the faulty line must be identified within 2ms [\[28\].](#page-11-20)

Combined with the definition of GSRA, when the system has a PPG fault, the calculated value of γ_p is approximately 1, whereas the single-pole ground fault will only cause a small disturbance to the healthy pole, and the change is not obvious within 1ms after the fault occurs. The direction of the fault current on both sides of the healthy pole line is negatively correlated, and the calculated value of γ_n is closer to 0. When a PP fault occurs in the system, the positive and negative fault currents rise rapidly within 1ms and are positively correlated, and the calculated values of γ_p and γ_n are about 1. Therefore, to ensure the reliability of the protection, the principle of γ*set* is adjusted to evade the maximum value of γ*out*_*max* in the case of an external fault, Considering the length and writing logic of the paper, the deduce process of γ*set* is given in Appendix [A.](#page-0-0) The expression of γ_{set} is shown in [\(6\).](#page-5-1)

$$
\gamma_{set} = K_{rel}\gamma_{out_max} \tag{6}
$$

where γ*set* is the adjustment threshold, which needs to consider a signal-to-noise ratio of 10 dB noise interference and ±5% of the measurement error of the transformer. *Krel* is the reliability coefficient, which and takes values in the range $(1.3, 1.5)$.

The minimum value for internal faults under different operating conditions was divided by the adjustment threshold to calibrate the protection sensitivity coefficient [\[29\].](#page-11-21)

$$
K_{sen} = \gamma_{in_min}/\gamma_{set} \tag{7}
$$

where *Ksen* is the sensitivity coefficient and takes values in the range of (1.25,1.35).

C. PROTECTION PROCESS

Considering the fault recognition criterion and fault pole selection criterion, the protection scheme shown in Fig. [5](#page-6-0) is proposed.

V. SIMULATION VERIFICATION

Build a MTDC distribution system in PSCAD/EMTDC, as shown in Fig. [1.](#page-2-0) Line L2 is used as the protected line, and the measurement points are at the H and E ends of L2 line to verify the rationality of the protection scheme through simulation. The system parameters are listed in Table [3.](#page-5-2)

The polarity characteristics of the fault current and the GSRA would appear after the occurrence of the fault, which,

FIGURE 5. Flowchart of the protection scheme.

in combination with the protection scheme, could identify the fault. Theoretically, the higher the sampling frequency, the more sampling points there are at the same time, and the data will be relatively accurate. Considering the reliability and speed of protection, the selected simulation step is 50 us, and the sampling frequency is 20 kHz. The data window length of the real-time sampled current incremental sequence is 50 us, while the sampling window of the current incremental mean sequence is of the sliding time window type, which consists of three sampling points with a sliding time window length of 150 us.

According to the full-text analysis and simulation verification, γ*set* evades the maximum value of the external fault for the adjustment principle, the final value is 0.7, and the introduction of the Trip signal indicates the correlation of the positive and negative lines on both sides of the current increments, defined as shown in [\(8\).](#page-6-1)

$$
\text{Trip}_y = \begin{cases} 1, & y \geqslant y_{\text{set}} \\ 0, & y < y_{\text{set}} \end{cases} \tag{8}
$$

The output value of Trip is used to determine whether the protection of each measurement point is operating, and when the signal sends 1 three times in a row, it is confirmed that a fault has occurred.

A. ANALYSIS OF PROTECTION BEHAVIOR

1) SIMULATION OF THE INTERNAL PPG FAULT

In this section, the internal PG fault will be verified, and a PPG fault occurs at *f* 2 as an example, and the system runs until 0.75 s when a permanent fault occurs, and the simulation results are shown in Fig. [6.](#page-6-2) The figure shows the currents, current increments, current GSRA, and protection operations

FIGURE 6. Simulation waveform of the internal PPG fault.

on the H and E sides of the line before and after the fault, respectively.

It can be found that when an internal single-pole ground fault occurs, the current polarity at both ends of the fault pole is positive, the current polarity at both ends of the healthy pole is one positive and one negative, the healthy pole current changes slowly, and the current polarity criterion speediness is reduced. Although the healthy pole current increments are almost zero in the initial stage of the fault, the fault pole current GSRA exceeds the adjusted value at the first sampling point immediately after the fault. The protection operates immediately when the Trip signal output at three consecutive sampling points is 1. This protection scheme can quickly and accurately identify an internal PPG fault.

2) SIMULATION OF THE INTERNAL PP FAULT

In this section, an internal PP metallic short-circuit fault is verified. A PP fault occurs at *f* 2 as an example, and the system operates until 0.75 s when a permanent fault occurs, and the simulation results are shown in Fig. [7.](#page-7-0) It can be seen that the current polarity is the same on both sides of the positive line, and the current polarity is opposite on both ends of the negative line. Regardless of whether the line is positive or negative, the fault current increments at both ends of the line show a highly positive correlation trend, and at the first sampling point after the fault, both the positive and negative slope relations exceed the adjusted value. The protection operates immediately when the Trip signal output of three consecutive sampling points is 1. This protection scheme can quickly and accurately identify internal PP shortcircuit faults.

FIGURE 7. Simulation waveform of the internal PP fault.

3) SIMULATION OF THE EXTERNAL FAULT

In this section, an external fault will be verified. As an example, a PPG short-circuit fault occurs at *f* 4, and the system runs to 0.75 s when a permanent fault occurs, and the simulation results are shown in Fig. [8.](#page-7-1) In terms of polarity, the currents on both sides of the positive and negative lines are traversing. In terms of the current GSRA, the fault current increments at both ends of the line show a negative correlation trend, the positive and negative slope relation of each sampling point after the fault does not exceed the adjusted value, the protection is not operated, and the protection scheme can accurately evade external faults.

B. ANALYSIS OF PROTECTION PERFORMANCE

1) THE INFLUENCE OF TRANSITION RESISTANCE

Because the fault current rise rate is weakened in the presence of transition resistance, the current correlation characteristics on both sides of the line will be reduced, and the traditional longitudinal protection, which depends on the change in current polarity, will have a rejection situation. The protection scheme proposed in this study integrates the characteristics of fault current polarity and GSRA, which can better tolerate the transition resistance. To verify that the protection scheme can withstand engineering short-circuit faults that may occur via transition resistance. Fig. [9](#page-7-2) shows a PP fault via 20Ω transition resistor occurs at $f2$ as an example, and the system operates until 0.75 s when a permanent fault occurs.

The protection scheme can withstand the transition resistance that may occur in the real project. The maximum values of the first three improved GSRA and protection actions at each measurement point after the occurrence of the PPG fault

FIGURE 8. Simulation waveform of the external PPG fault.

FIGURE 9. Internal PP fault via 2002 transition resistor.

via transition resistance at different fault locations are listed in Table [4.](#page-8-0) Without considering the actual situation, algorithm can withstand a transition resistance of about 500Ω .

2) THE INFLUENCE OF THE LOAD CURRENT

When a high-resistance fault occurs in the system under a heavy load, the fault current variation is not evident, and the current polarity on both sides of the positive and negative

TABLE 4. Improved GSRA for short-circuit faults via transition resistance under different actual operating conditions.

Fault location	Transition resistance/ Ω	GSRA			
		Positive	Negative	Discriminating results	
		line	line		
fl	5	0.98	0.14	The internal fault	
	10	0.96	0.13		
	20	0.93	0.10		
f2	5	0.96	0.16		
	10	0.98	0.20		
	20	0.98	0.09		
f3	5	0.98	0.18		
	10	0.95	0.19		
	20	0.94	0.12		

FIGURE 10. Internal PPG fault via 2002 transition resistor under heavy load.

lines may be anomalous, at which time the protection may refuse to operate. Fig. [10](#page-8-1) shows the protection operation in the case of a heavy load and high-resistance ground fault. Changing the total load of ports T6 and T7 from 2 MW to 4 MW, a PPG fault via 20Ω transition resistor occurs at $f2$ as an example, and the system operates until 0.75 s when a permanent fault occurs. The scheme proposed in this paper is based on the combination of fault current polarity and current GSRA judgment method, where the current under heavy load does not cause the incremental trend of the positive and negative lines on both sides of the current to change, and is unaffected by the load current.

3) THE INFLUENCE OF THE DISTRIBUTED CAPACITANCE OF THE LINE

In the traditional longitudinal protection scheme based on the change in fault current polarity, when a fault occurs, the protection may operate incorrectly because of the fluctuation

FIGURE 11. Simulation results of current correlation considering line distributed capacitance.

of the current on both sides of the line caused by the transient characteristics of the distributed capacitor. In this study, the line distributed capacitance is expanded to 2, 5, and 10 times the original value. Taking the occurrence of a PPG fault at *f* 2 as an example, the simulation results of the current GSRA considering the line-distributed capacitance are shown in Fig. [11.](#page-8-2)

GSRA uses the current increments at both ends of the line for relational analysis, and the current components of the line distribution capacitance are canceled out and do not participate in the GSRA calculation process during the current increment calculation. It can be observed that when the distributed capacitance of the line increases, the fault current ratio correlation of the positive and negative lines after the fault still presents the same law as the original parameters, the protection can operate correctly, and the protection scheme is not affected by the distributed capacitance of the line.

4) THE INFLUENCE OF OPEN-LOOP OPERATION

When a fault occurs on any line and is isolated by the system, the distribution system is in an open-loop operation. While the power flowing through each port changes after an openloop operation of the system, the incremental characteristics of the fault current do not change after a fault in any line. This paper sets 0.5 s when line L5 is out of operation with *f* 2 occurring PPG fault as an example, the system operates to 0.75 s when a permanent fault occurs, and the simulation results are shown in Fig. [12.](#page-9-0)

It can be observed that the open-loop operation of the system only affects the magnitude of the fault current and does not affect the correct operation of the protection.

5) THE INFLUENCE OF CONVERTER BLOCKING AND DATA WINDOW LENGTH

A DC line generates a serious overcurrent in a short period when a fault occurs in a DC system. To prevent damage to the converter, each converter station monitors the bridge arm current in real time and sets up over-current protection. When the converter self-protection operates, the converter will be blocked, the controllable elements in the converter will be

FIGURE 12. Fault simulation waveform during open-loop operation.

FIGURE 13. DCT bridge arm current during PP fault.

transformed into uncontrolled, and the current characteristics of the faulty line will be changed. Fig. [13](#page-9-1) shows the bridge arm current in the DCT when a PP short-circuit fault occurs at *f* 2. If the converter blocking adjustment value is 2 times the rated current, the converter will be self-protected approximately 1ms after a fault occurs.

The sampling frequency used in the simulation in this paper is 20 kHz, and the identification time of the fault after the occurrence of the fault is $150\mu s$ in total for three sampling points. Considering that the logic signal communication time required for a 5 km DC line is approximately $100\mu s$ [\[30\],](#page-11-22) [\[31\], t](#page-11-23)he protection can be operated before the converter blocking and is unaffected by the converter. If the sampling frequency is changed to 10 kHz, nine current-ratio correlation signals can still be obtained before the converter is blocked, without affecting the protection operation.

6) THE INFLUENCE OF NOISE INTERFERENCE

The distribution system is susceptible to current transformer noise, communication channel noise and device noise. Strong

FIGURE 14. Simulation waveform under noise interference.

noise (< 30 dB) may even cause changes in current increment polarity, and traditional unit protection based on current fault component polarity may be false during normal operation or rejected when a fault occurs under the action of noise [\[32\].](#page-11-24) Therefore, in this study, interference noise with a signal-tonoise ratio of 10 dB is added at 0.75 s during the normal operation of the system, and the simulation waveform under the noise interference is shown in Fig. [14.](#page-9-2) The interference of noise affects the current on both sides of the positive and negative lines simultaneously, which may cause the polarity and amplitude of the current to change and make the protection false. This protection scheme uses the GSRA of the positive and negative line ends of the current increments to constitute the criterion, and the fluctuation of the current has less influence on the increments. Coupled with the threshold adjustment time to consider the reliability of the scheme, the protection does not operate by mistake.

VI. CONCLUSION

This paper proposes a longitudinal protection scheme for multi-terminal flexible DC distribution lines based on fault current incremental GSRA, with comprehensive theoretical analysis and simulation verification showing that the proposed protection scheme has the following advantages:

1) The scheme considers the fault current polarity and current incremental GSRA, which are simple to calculate and meet the requirements of DC line protection speed and selectivity simultaneously.

2) The scheme does not require initiation criteria, and communication only requires transmission logic, which is

FIGURE 15. Simulation waveform under noise interference.

less demanding on system communication and has higher reliability.

3) The scheme has the ability to withstand high transition resistance and is unaffected by load current, distributed capacitance, noise interference, and converter blocking.

4) The scheme is used for a ring or radial DC grid structure and has a certain reference value for unit protection of DC lines in actual projects.

5) Double-ended volume protection relies on communication, and we will try to combine protection with artificial intelligence algorithms in future work to reduce the impact of various communication errors on protection and improve the fault tolerance of protection.

APPENDIX A

The DCT high voltage side is operated via split capacitor grounding. When a PG fault occurs in the DC line, the fault pole produces a serious overcurrent phenomenon and the healthy pole will only be slightly disturbed; when a PP fault occurs in the DC line, the bipolar line short-circuit current grows rapidly and is very likely to damage the power electronics of the flexible system. Here for more difficult to identify the PG fault than the line PP fault for the first end of the fault pole current analysis.

The length of medium voltage DC distribution system cable lines is much smaller than transmission overhead lines, and the distributed capacitance of cable lines is larger, and the transmission of traveling waves on the line will cause the distributed capacitance to charge and discharge several times. The calculation of GSRA is related to the current increment, which is mainly affected by the DC component of the current, in order to eliminate the influence of the line distributed capacitance on the measured current, the measured current at the protection installation is the filtered current after the firstorder low-pass filter [\[32\]. T](#page-11-24)he filtering method is as in [\(9\).](#page-10-7)

$$
I_{\rm Hp}(j) = \tau I_{\rm f}(j) + (1 - \tau)I_{\rm Hp}(j - 1) \tag{9}
$$

where, τ is the filtering time constant and I_f is the shortcircuit current.

Therefore, the fault circuit in the capacitor discharge phase during a PPG fault can be equated to a second-order circuit as shown in Fig. [15.](#page-10-8)

Combined with the above analysis, the line accounts for the proportion of the full length of *x* when a metallic fault occurs, that is $R_f \approx 0$, $L_{eq} = L_{lim} + xL_{line}$, $C_{eq} = nC_s$, $R_{eq} = xR_{line}$. The I_{Hp} expression is given in [\(10\).](#page-10-9)

$$
I_{\rm Hp} = I_0 \sin \varphi e^{-\alpha t} \sin(\omega_d t + \varphi) \tag{10}
$$

where I_0 is the load current, $\alpha = R_{eq}/2L_{eq}$, $\omega_0 = 1/\sqrt{L_{eq}C_{eq}}$, $\varphi = \arctan \frac{2\omega_d}{R + 2U_{dc}/I_0}, \omega_d = \sqrt{\omega_0^2 - \alpha^2}.$

After a high resistance fault, the circuit is in an overdamped state and the I'_{Hp} expression is as in [\(11\).](#page-10-10)

$$
I'_{\rm Hp} = \frac{I_0(R'_{\rm eq}C_{\rm eq} - \sqrt{\Delta}) + 2C_{\rm eq}U_{\rm dc}}{2\sqrt{\Delta}}e^{r'_1 t} - \frac{I_0(R'_{\rm eq}C_{\rm eq} + \sqrt{\Delta}) + 2C_{\rm eq}U_{\rm dc}}{2\sqrt{\Delta}}e^{r'_2 t} \tag{11}
$$

In [\(11\),](#page-10-10) where $R'_{eq} = R_{eq} + 2R_f$, $\Delta = R'^{2}_{eq}C_{eq}^{2} - 4L_{eq}C_{eq}$, and . √

$$
r'_{1,2} = \frac{-R'_{\text{eq}}C_{\text{eq}} \pm \sqrt{\Delta}}{2L_{\text{eq}}C_{\text{eq}}}
$$

According to the measured current expression and the waveform characteristics of the fault current inside and outside the zone, it is known that the GSRA of the fault pole line is 1 when a fault occurs internal the DC line, and -1 when an external fault occurs. However, the actual engineering needs to consider noise interference, mutual sensor error, communication delay, etc. In order to avoid the maximum value of the GSRA during normal operation of the system and external faults, γ*set* is set to 0.7.

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