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## **WE RESEARCH ARTICLE**

# On the Design Flow of the Fractional-Order Analog Filters Between FPAA Implementation and Circuit Realization

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**ABSTRACT** This work explicitly states the design flows of the fractional-order analog filters used by researchers throughout the literature. Two main flows are studied: the FPAA implementation and the circuit realization. Partial-fraction expansion representation is used to prepare the approximated fractional-order response for implementation on FPAA. The generalization of the second-order active RC analog filters based on opamp from the integer-order domain to the fractional-order domain is presented. The generalization is studied from both mathematical and circuit realization points of view. It is found that the great benefit of the fractional-order domain is that it adds more degrees of freedom to the filter design process. Simulation and experimental results match the expected theoretical analysis.

**INDEX TERMS** Fractional-order, analog filter, FPAA, circuit realization.

#### **I. INTRODUCTION**

<span id="page-0-0"></span>Analog filters are one of the most important blocks in designing communications and electronics systems. Developing a filtering block allows the signal to be processed in many system stages. Active filters have many advantages over passive ones, especially when cascading many sections to acquire higher order filters [\[1\]. In](#page-13-0) general, the order of the filter indicates how much one can accurately and smoothly achieve the required response. For discrete technologies, many inductor-less active RC filters have been developed in the literature  $[2]$ ,  $[3]$ ,  $[4]$ ,  $[5]$ . Most of these previous works aimed to achieve high-quality factors or reduce passive and active sensitivities. The design of the second-order active RC filters was of special concern to the authors as they could be cascaded to get higher-order filters without the need to design a complex higher-order filter as a whole [\[6\]. A](#page-13-5)n inductor

<span id="page-0-2"></span><span id="page-0-1"></span>The associate editor coordinating the review of this manuscript and approving it for publication was Yuh-Shyan Hwang<sup>1</sup>[.](https://orcid.org/0000-0002-1486-4059)

<span id="page-0-10"></span><span id="page-0-9"></span><span id="page-0-8"></span><span id="page-0-7"></span><span id="page-0-6"></span><span id="page-0-5"></span><span id="page-0-4"></span><span id="page-0-3"></span>would require a large chip area, developing inductor-less filters, i.e., those that incorporate only capacitors and resistors. became inevitable [\[1\]. In](#page-13-0)teger-order calculus is a subset of a more general science called fractional-order calculus [\[7\].](#page-13-6) Generalizing any system from the integer-order domain to the fractional-order domain gives more flexibility and degrees of freedom in the system design process. This has been a hot research topic in the last decade, implying many applications in different fields like digital modelling on FPGA [\[8\], ch](#page-13-7)aotic systems realization [\[9\] an](#page-13-8)d bio-impedance modelling [\[10\].](#page-13-9) The application of the generalization concept of systems to the fractional-order domain is eligible the integrated circuits technologies, which led to many works in the literature. Extensive research on the applications of the fractional-order analog and chaotic systems for integrated circuits technologies was conducted in [\[11\] a](#page-13-10)nd [\[12\],](#page-13-11) respectively. Further recent applications for the fractional-order filters suitable for integrated circuits technologies were discussed in [\[13\] an](#page-13-12)d [\[14\].](#page-13-13)

<span id="page-1-9"></span><span id="page-1-8"></span><span id="page-1-4"></span>Generalizing the analog filters to the fractional-order domain can be handled on the filter design theory and the filter circuit realization. Generalization of the filter design on the circuit realization can be made by substitution of the integer-order elements with the fractional-order elements [\[15\], \[](#page-13-14)[16\], \[](#page-13-15)[17\], b](#page-13-16)y optimization [\[18\], \[](#page-13-17)[19\], \[](#page-13-18)[20\], \[](#page-13-19)[21\]](#page-13-20) or by curve fitting techniques [\[22\], \[](#page-13-21)[23\]. A](#page-13-22) new technique in designing Butterworth filter depending on pole placement in the complex w-plane was presented in [\[24\]. A](#page-13-23)nother approximation technique for designing a fractional-order Butterworth filter that depends on the weighted sum of the integer-order Butterworth transfer function was discussed in [\[25\]. I](#page-13-24)n [\[26\], t](#page-13-25)ool-based approximation techniques were utilized to approximate the fractional-order filter transfer function with two independent orders with a curve fitting program and another expansion-based program (namely, Padé function toolbox). A fractional-order power-law shelving filter was implemented in [\[27\] us](#page-13-26)ing Oustaloup approximation over Foster-I to realize the fraction-order capacitor. Many works studied the effect of designing specific filters in the fractional-order domain. The fractional-order notch filter was studied with clear realization in [\[28\]. I](#page-13-27)n [\[29\], t](#page-13-28)he design of both the fractional-order band-pass and notch filters was studied over different approximation techniques. An electronically adjustable fractional-order filter was introduced in [\[30\].](#page-13-29)

<span id="page-1-10"></span>Another design flow for systems such as the fractionalorder filters includes the implementation of a Fieldprogrammable Analog Array (i.e., FPAA) [\[31\], \[](#page-13-30)[32\]. I](#page-13-31)n [\[33\],](#page-13-32) a proposed fractional-order PID controller was implemented on FPAA using two different implementation techniques. Finally, in [\[34\],](#page-13-33) a generalized form of a multi-output fractional-order filter with two independent orders was implemented on FPAA.

<span id="page-1-17"></span><span id="page-1-16"></span>This work aims to study and compare the two design flows with a design case and introduce a catalog for the generalized active filters with the explicit transfer function for each filter family. The integer-order passive elements used in designing the filters were a subset of a more general term called the fractional-order elements (i.e., the fractance element) [\[35\], \[](#page-13-34)[36\]. T](#page-13-35)he fractance element has the impedance of the form  $Z(s) = ks^{\alpha}$  where it becomes a resistor for  $\alpha = 0$ , an inductor for  $\alpha = 1$ , a capacitor for  $\alpha = -1$ , and a frequency-dependent negative resistor (FDNR) for  $\alpha =$ −2 [\[37\]. A](#page-13-36) fractional-order capacitor is achieved by choosing  $-2 < \alpha < 0$  while a fractional-order inductor is achieved by choosing  $0 < \alpha < 2$  [\[38\].](#page-13-37)

<span id="page-1-19"></span><span id="page-1-18"></span>While the fractance device is still commercially unavailable as a complete element and the usage of integer order analysis tools and methods is more reliable than that of the fractional order ones, the development of approximation techniques for the Laplacian operator is inevitable [\[39\], \[](#page-13-38)[40\] fo](#page-14-0)r the real-time realization of the CPE. In the Recent work, [\[41\],](#page-14-1) the modelling and circuit realization of the fractional-order element was summarized, and the passive circuit realization of the CPE was reviewed in [\[42\]. A](#page-14-2)nalog approximation

<span id="page-1-7"></span><span id="page-1-6"></span><span id="page-1-5"></span><span id="page-1-0"></span>**FIGURE 1.** Analog approximation types [\[43\],](#page-14-3) [\[44\],](#page-14-4) [\[45\].](#page-14-5)

<span id="page-1-23"></span>techniques could be categorized [\[43\] as](#page-14-3) in Fig. [1.](#page-1-0) [\[35\], \[](#page-13-34)[36\],](#page-13-35) [\[37\], \[](#page-13-36)[38\], \[](#page-13-37)[39\], \[](#page-13-38)[40\], \[](#page-14-0)[41\], \[](#page-14-1)[42\], \[](#page-14-2)[43\], \[](#page-14-3)[44\], \[](#page-14-4)[45\].](#page-14-5)

<span id="page-1-24"></span>This paper starts with some mathematical and theoretical basics for the fractional-order system in Section  $II$  and then goes through the design flow details, starting with the implementation of the approximated constant-phase element in Sections [V-](#page-3-0)[III.](#page-1-2) The design case of the filters on FPAA is then introduced in Section [VI.](#page-3-1) Next, the catalog of the generalized filters is presented in Section [VII](#page-4-0) and the paper finishes with the design case using the circuit approach in Sectio[nVIII.](#page-10-0)

#### <span id="page-1-12"></span><span id="page-1-11"></span><span id="page-1-1"></span>**II. FRACTIONAL-ORDER MATHEMATICAL BACKGROUND**

<span id="page-1-15"></span><span id="page-1-14"></span><span id="page-1-13"></span>The conventional calculus that describes most engineering systems, including analog filters, is a subset of fractional-order calculus. In general, designing systems in the fractional-order domain gives more degrees of freedom for the design factors. Assuming a real-time continuous function  $f(t)$ , the Reimann-Liouville definition  $[46]$  for the continuous-time fractional-order derivative is as follows:

$$
\frac{d^{\alpha}}{dt^{\alpha}} \equiv D^{\alpha}f(t) = \frac{1}{\Gamma(1-\alpha)} \frac{d}{dt} \int_0^t (t-\tau)^{-\alpha} f(\tau) d\tau, \quad (1)
$$

where  $\alpha$  is the fractional order and  $0 < \alpha < 1$ . As This definition is a continuous-time function, it mainly relates to the fractional-order analog systems applications. It is essential to understand the tendency of the fractance elements by applying the Laplace transform on Eqn. [1](#page-1-3) by assuming a zero-time initial condition.

<span id="page-1-26"></span><span id="page-1-25"></span><span id="page-1-3"></span>
$$
L\{od_t^{\alpha}f(t)\} = s^{\alpha}F(S),\tag{2}
$$

which indicates that the fractance device (the fractionalorder element) has an impedance that is proportional to *s* α .

## <span id="page-1-2"></span>**III. APPROXIMATION OF THE LAPLACIAN OPERATOR**

<span id="page-1-22"></span><span id="page-1-21"></span><span id="page-1-20"></span>Approximation of the Laplacian operator *s* <sup>α</sup> has been an active research area in recent decades. The approximation techniques could be based on different criteria, like transfer-function based, circuit-based, state-space based or impulse-response based [\[45\].](#page-14-5) The most popular transfer function approximation techniques were continuous fraction expansion (CFE), Oustaloup, Matsuda and Carlson [\[43\].](#page-14-3) Another type of transfer function approximation was based on the weighted sum of high-pass filter sections. Considering the circuit-based techniques, the work of Valsa et al. [\[47\]](#page-14-7)

<span id="page-2-0"></span>**TABLE 1.** Comparison among different approximation techniques factors controllability.

Technique	(N)	$(\omega_c)$	$(\omega_h - \omega_l)$	$(\Delta \Phi)$
Oustaloup		x		x
Matsuda		X		x
Carlson	x		x	x
<b>CFE</b>			x	x
Valsa				

was the most significant approximation based on RC network expressions. Table [1](#page-2-0) summarizes the configurable parameters for different approximation techniques.

## A. CONTINUOUS FRACTION EXPANSION (CFE) APPROXIMATION

Also known as continued fractions, it is an approximation method used throughout history to approximate polynomials to rational functions (i.e., ratios of polynomials) [\[48\]. T](#page-14-8)he mathematical arguments and discussions on the continued fractions had been studied in the old textbooks as in [\[49\].](#page-14-9) In addition, the application of the CFE was associated with functions that converge much more rapidly than power series expansion [\[44\]. T](#page-14-4)his approximation method, alongside Carlson's method, is a mathematical approach to approximate the fractional-order Laplacian operator  $(s^\alpha)$ , which assumes it is a black box. This, in turn, reflects that both methods can only control the center frequency of the approximation, not the range of the frequencies. However, there is only one advantage of the CFE over Carlson: one can control the order of the resulting approximation fraction as listed in Table [1.](#page-2-0) The continued fractions have the following form:

$$
a_0 + \frac{b_1}{a_1 + \frac{b_2}{a_2 + \frac{b_3}{a_3 + \dots}}},\tag{3}
$$

which could be represented in Pringsheim notation as follows:

<span id="page-2-5"></span>
$$
\left[a_0, \frac{b_k}{a_k}\right]_{k=1}^{+\infty}.\tag{4}
$$

#### B. OUSTALOUP's (CRONE) APPROXIMATION

Oustaloup's approximation was first introduced as a part of the toolbox CRONE in [\[50\]. T](#page-14-10)he techniques distribute equal numbers of poles and zeros around the frequency range of interest, so the order of the approximation and frequency limits are controllable. The approximation has the following form [\[43\]:](#page-14-3)

$$
s^{\alpha} = C \prod_{m=1}^{N} \frac{1 + s/\omega_{z,m}}{1 + s/\omega_{p,m}},
$$
 (5)

where

$$
\omega_{z,m} = \omega_l \left(\frac{\omega_h}{\omega_l}\right)^{(2m-1-\alpha)/2N},\tag{6}
$$

and

<span id="page-2-6"></span><span id="page-2-2"></span>
$$
\omega_{p,m} = \omega_l \left(\frac{\omega_h}{\omega_l}\right)^{(2m-1+\alpha)/2N}.\tag{7}
$$

It is also worth mentioning that in the Oustaloup approximation, the order of the approximation beyond 8 (i.e.,  $N > 8$ ) does not significantly improve the performance [\[51\].](#page-14-11)

#### <span id="page-2-7"></span>C. MODIFIED OUSTALOUP APPROXIMATION

In [\[52\], a](#page-14-12) modified version of oustaloup's algorithm was presented to give a better performance across the whole frequency range  $(\omega_l, \omega_h)$ . The modified equation was presented as follows  $[53]$ :

<span id="page-2-8"></span>
$$
s^{\alpha} = \left(\frac{d\omega_h}{b}\right)^{\alpha} \left(\frac{ds^2 + b\omega_h s}{d(1 - \alpha)s^2 + b\omega_h s + d\alpha}\right) \cdots
$$

$$
\cdots \prod_{m=-N}^{N} \frac{1 + s/\omega_{z,m}}{1 + s/\omega_{p,m}},
$$
(8)

<span id="page-2-4"></span><span id="page-2-3"></span>where  $\omega_{z,m}$  and  $\omega_{p,m}$  are given by Eqns. [6](#page-2-1) and [7](#page-2-2) respectively.

#### D. MATSUDA's APPROXIMATION

Matsuda approximation uses the continued fraction expansion of the single-input single-output fractional-order transfer functions [\[43\]. T](#page-14-3)he application of Matsuda approximation requires the distribution of an even number of frequency points between the frequency limits (i.e.,  $\omega_l$  :  $\omega_h$ ). This is because the algorithm gives *N*/2 number of poles and zeros [\[54\]. T](#page-14-14)herefore, the algorithm could be written with Pringsheim notation as follows [\[43\]:](#page-14-3)

<span id="page-2-9"></span>
$$
G(s) = \left[ d_0(\omega_0); \frac{s - \omega_{k1}}{d_k(\omega_k) +} \right]_{k=1}^N, \tag{9}
$$

where

$$
d_0(\omega) = |G(j\omega)|,\t(10)
$$

and

$$
d_k(\omega) = \frac{\omega - \omega_{k-1}}{d_{k-1}(\omega) - d_{k-1}(\omega_{k-1})}.
$$
 (11)

#### E. VALSA's APPROXIMATION

<span id="page-2-1"></span>In [\[47\], V](#page-14-7)alsa introduced a circuit approximation technique for the fractional-order capacitor across a certain frequency range. The idea is similar to the transfer function approximations in that it realizes an RC network that distributes poles and zeros across the frequency range of interest. Two possible implementations for Valsa's networks are shown in Fig. [2.](#page-3-2) Valsa relied on the required phase variation  $\Delta \phi$  across the frequency range to compute the values of the resistors and capacitors in the realized network. Given a required phase variation  $\Delta \phi$ , Valsa computes *a* and *b* parameters, as shown in Eqn. [12.](#page-3-3) The values of the first branch passives are related to the low-frequency limit  $\omega_{min}$  by Eqn. [13.](#page-3-4) The initial branch value for the series network is shown in Eqn. [14](#page-3-3) and for the

<span id="page-3-2"></span>

**FIGURE 2.** Valsa's approximation networks: (a) series RC network, (b) parallel RC network.

parallel network in Eqn. [15](#page-3-3) where  $1 < k \le m$ . The rest of the branches are calculated by Eqn. [16.](#page-3-3)

<span id="page-3-3"></span>
$$
ab = \frac{0.24}{1 + \Delta \phi},\tag{12a}
$$

$$
a = 10^{\alpha \log(ab)}, \tag{12b}
$$

$$
b = ab/a, \tag{12c}
$$

$$
R_1 C_1 = \frac{1}{\omega_{\min}}.\tag{13}
$$

$$
R_s = \frac{R_1 a^m}{1 - a},\tag{14a}
$$

$$
C_s = \frac{C_1(1-b)}{b},\tag{14b}
$$

$$
R_p = \frac{R_1(1-a)}{a},\tag{15a}
$$

$$
C_p = \frac{C_1 b^m}{1 - b},\tag{15b}
$$

$$
R_k = R_1 a^{k-1},\tag{16a}
$$

$$
C_k = C_1 b^{k-1},\tag{16b}
$$

## **IV. APPROXIMATION OF THE FRACTIONAL-ORDER RATIONAL CONTROLLERS**

## A. CHAREF's APPROXIMATION

Charef approximation was introduced in [\[55\] as](#page-14-15) an approximation technique for the fractional-order controller of the form [\[44\]:](#page-14-4)

$$
H(s) = \frac{1}{(1 + \frac{s}{p_T})^{\alpha}} \approx \frac{\prod_{i=0}^{n-1} (1 + s/z_i)}{\prod_{i=0}^{n} (1 + s/p_i)},
$$
(17)

where poles and zeros are defined as: √

$$
p0 = p_T \sqrt{b},\tag{18}
$$

$$
pi = p_0(ab)^i,
$$
 (19)

$$
zi = ap_0(ab)^i,\t(20)
$$

where,

$$
a = 10^{y/10(1-\alpha)}, \tag{21}
$$

$$
b = 10^{\nu/10\alpha},\tag{22}
$$

$$
ab = 10^{y/10\alpha(1-\alpha)},
$$
 (23)

It was also discussed in [55] that Charef's approximation could give a reasonable estimate for 
$$
s^{\alpha}
$$
.

<span id="page-3-5"></span>

**FIGURE 3.** Fractional-order systems design flows.

## <span id="page-3-8"></span>B. Padé APPROXIMATION

Introduced in [\[56\], t](#page-14-16)he Padé method was meant to give an approximation for rational functions such as the lead/lag compensator presented in [\[57\] o](#page-14-17)r to be used as a reduction model as discussed in [\[58\]. T](#page-14-18)he approximation form is given by:

<span id="page-3-10"></span><span id="page-3-9"></span><span id="page-3-4"></span>
$$
H(s) = R_{[m/n]} = \frac{\left(\frac{p_m}{q_n}\right)s^m + \left(\frac{p_{m-1}}{q_n}\right)s^{m-1} + \ldots + \left(\frac{p_0}{q_n}\right)}{s^n + \left(\frac{q_{n-1}}{q_n}\right)s^{n-1} + \ldots + \left(\frac{1}{q_n}\right)} \tag{24}
$$

where  $p_i$  and  $q_i$  can be calculated by solving a  $m+n+1$  linear equations as shown in [\[57\].](#page-14-17)

## <span id="page-3-0"></span>**V. FRACTIONAL-ORDER ANALOG FILTERS DESIGN FLOWS**

Two main flows can design the filters: circuit realization and FPAA implementation, as depicted in Fig. [3.](#page-3-5)

## A. CASE STUDY FOR THE APPROXIMATION OF THE LAPLACIAN OPERATOR

<span id="page-3-7"></span>The case to be studied implies approximating  $s^{1/3}$  to a frequency range of  $1 \frac{10^4 Hz}{\text{m}}$ . Four transfer function techniques have been chosen, namely, Oustaloup, Matsuda, Carlson and Continued Fraction Expansion (CFE), alongside one circuit approximation, Valsa's. Table [2](#page-4-1) summarizes the MATLAB plot for the approximated functions. Phase variation  $\Delta \Phi$  at frequency limits could be estimated as 8.67 % for Oustaloup, 9.3 % for Matsuda, 67.4 % for Carlson, 53.24 % for CFE.

## <span id="page-3-1"></span>**VI. CASE STUDY FOR THE DESIGN OF A LOW-PASS FRACTIONAL-ORDER FILTER ON FPAA**

Considering the generalization of the normalized secondorder Butterworth filter in the following equation:

$$
T(s) = \frac{1}{s^2 + \sqrt{2}s + 1},
$$
\n(25)

to the fractional-order domain with one constant-phase element with order  $\alpha$ . The fractional-order transfer function of the filter becomes:

<span id="page-3-6"></span>
$$
T(s) = \frac{1}{s^{1+\alpha} + \sqrt{2}s^{\alpha} + 1},
$$
\n(26)

### <span id="page-4-1"></span>**TABLE 2.** Magnitude and phase response for different techniques.



<span id="page-4-2"></span>**FIGURE 4.** Butterworth ideal response: (a) normalized magnitude response, (b) normalized phase response, (c) scaled magnitude response, (d) scaled phase response.

which has the normalized response with cutoff frequency 0.071 *Hz* and phase −30*<sup>o</sup>* as shown in Figs. [4a-4b.](#page-4-2) For more practical operation, a frequency scale factor of  $10^4$  is used to get the cutoff frequency of 714 *Hz*, which could be seen in Figs. [4c-4d.](#page-4-2)

<span id="page-4-3"></span>

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**FIGURE 5.** Butterworth approximated response: (a) magnitude responses, (b) phase responses, (c) magnitude response error, (d) phase response error.

Applying different approximation techniques to the fractional-order Laplacian operator  $\alpha = 0.7$  and then substituting into the transfer function gives the following approximated response as in Figs.  $5a-5b$ . The error of the magnitude and phase responses are shown in Figs. [5c-5d.](#page-4-3)

At this point, the approximated transfer functions can be broken into a sum of integrators, as shown in Fig. [7](#page-6-0) for realization on FPAA. Using the residue function on MAT-LAB and getting the appropriate gain and time constants for integrators, Table [3](#page-5-0) summarizes the realized values. First, AnadigmDesigner software was used to realize the filters on the FPAA AN231E04 kit, as shown in Fig. [6.](#page-6-1) Then the stimulus was injected using an NI ELVIS II kit to perform an AC sweep and get the bode plot for each filter. Finally, figure [8](#page-6-2) shows the experimental results plotted using MATLAB.

## <span id="page-4-0"></span>**VII. TRANSFORMATION OF SOME ACTIVE FILTERS TO THE FRACTIONAL-ORDER DOMAIN**

A generalized form of the integer-order version of the filter should be derived to realize the fractional-order filter circuits, which is the first step in the design process. As the orders of the fractional-order capacitor could be of different values, it has become inevitable to determine the capacitor that contributes to the quality factor term in the integer-order version (i.e., the s term in the denominator). After that, the transfer function will be ready for comparison with the required transfer function to map the components' values. This will be followed by approximating and realizing the calculated fractional-order capacitor and then composing the circuit. This section presents the generalization of some of the well-known alongside some of the forgotten second-order filter circuits to the fractional-order domain.

#### A. BACH FAMILY

Figure [9](#page-6-3) shows the Bach's low-pass filter generalized into fractional order domain with two fractional order capacitors

<span id="page-5-0"></span>



#### <span id="page-5-2"></span>**TABLE 4.** Fliege transfer functions.



*C*<sub>1</sub> and *C*<sub>2</sub> having α and β respectively. By direct analysis, the transfer function of the filter becomes:

$$
H_{LP}(s) = \frac{\left(\frac{1}{R_1 R_2 C_\alpha C_\beta}\right)}{s^{\alpha + \beta} + \frac{s^\beta}{R_1 C_\alpha} + \frac{1}{R_1 R_2 C_\alpha C_\beta}}.\tag{27}
$$

## B. RAUCH FILTERS FAMILY

The Rauch filter was presented In [\[59\]. T](#page-14-19)he fractional-order version of the filter is presented in Fig. [10](#page-6-4) and the transfer function is shown in Eqn. [28.](#page-5-1)

$$
H_{LP}(s) = \frac{\frac{-1}{R_1 R_2 C_{\alpha} C_{\beta}}}{s^{\alpha + \beta} + s^{\beta} \frac{1}{R_1 C_{\alpha}} (1 + \frac{R_1}{R_2} + \frac{R_1}{R_3}) + \frac{1}{R_2 R_3 C_{\alpha} C_{\beta}}}.
$$
 (28)

## C. FRACTIONAL-ORDER FLIEGE FAMILY

The Fliege filter was presented In [\[60\]. T](#page-14-20)he fractional-order version of the filter is presented in Fig. [11](#page-7-0) and the transfer function is shown in Table [4.](#page-5-2)

#### D. DELIYANNIS FILTERS FAMILY

The Deliyannis filter was presented In [\[61\]. T](#page-14-21)he fractionalorder version of the filter is presented in Fig. [12](#page-7-1) and the transfer function is shown in Table [5.](#page-5-3)

## E. MIKHAEL-BHATTACHARYYA (MB) FILTERS FAMILY

<span id="page-5-10"></span>Mikhael and Bhattacharyya presented a filter family In 1975 in [\[62\]. T](#page-14-22)he fractional-order version of the filter is

#### <span id="page-5-3"></span>**TABLE 5.** Fractional-order Delyannis transfer functions.



#### <span id="page-5-4"></span>**TABLE 6.** MB transfer functions.

<span id="page-5-6"></span>

#### <span id="page-5-7"></span><span id="page-5-5"></span>**TABLE 7.** PMG transfer functions.

<span id="page-5-1"></span>

<span id="page-5-9"></span><span id="page-5-8"></span>presented in Fig. [13,](#page-7-2) and the transfer function is shown in Table [6.](#page-5-4)

<span id="page-5-11"></span>F. PADUKONE-MULAWKA-GHAUSI (PMG) FILTERS FAMILY This filter family was presented in [\[63\]. T](#page-14-23)he fractional-order version of the filter is presented in Fig. [14](#page-7-3) and the transfer function is shown in Table [7.](#page-5-5)

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<span id="page-6-1"></span>

 $(b)$ 



 $(c)$ 





**FIGURE 6.** FPAA realization of the FO filter using approximation: (a) CFE, (b) Oustaloup, (c) Modified Oustaloup, (d) Matsuda, (e) Charef.

## G. BERKA-HERPY FAMILY

The BH filter family is a universal building block family that was first proposed in 1981 in [\[64\].](#page-14-24)

<span id="page-6-0"></span>

**FIGURE 7.** Partial fraction expansion representation of the approximated transfer function.

<span id="page-6-2"></span>

<span id="page-6-3"></span>**FIGURE 8.** Experimental Results: (a) magnitude responses, (b) phase responses, (c) magnitude response error, (d) phase response error.



<span id="page-6-4"></span>**FIGURE 9.** Bach's FOLPF circuit schematics.



**FIGURE 10.** Rauch's FOLPF circuit schematics.

## H. FRACTIONAL-ORDER Soliman72 FILTER

<span id="page-6-6"></span><span id="page-6-5"></span>Presented in [\[65\], t](#page-14-25)he fractional-order version of this filter is presented in Fig. [16](#page-8-0) and the transfer function is shown in Table [9.](#page-7-4)

<span id="page-7-0"></span>

<span id="page-7-1"></span>



<span id="page-7-2"></span>**FIGURE 12.** Fractional-order Delyannis Filters: (a) FOBPF I, (b) FOBPF II.





**FIGURE 13.** Fractional-order MB Filters Family: (a) FOLPF, (b) FOHPF, (c) FOBPF, (d) FONF.

## <span id="page-7-6"></span>I. FRACTIONAL-ORDER Soliman73 FILTER

Presented in [\[66\], t](#page-14-26)he fractional-order version of this filter is presented in Fig. [17](#page-8-1) and the transfer function is shown in Table [10.](#page-7-5)

<span id="page-7-3"></span>

**FIGURE 14.** Fractional-order PMG Filters Family: (a) FOLPF, (b) FOHPF, (c) FOBPF, (d) FONF.

#### **TABLE 8.** BH transfer functions.



#### <span id="page-7-4"></span>**TABLE 9.** Fractional-order Soliman72 filter transfer functions.



#### <span id="page-7-5"></span>**TABLE 10.** Fractional-order Soliman73 filter transfer functions.



## J. FRACTIONAL-ORDER Soliman74 FILTER

<span id="page-7-7"></span>Presented in [\[67\], t](#page-14-27)he fractional-order version of this filter is presented in Fig. [18](#page-8-2) and the transfer function is shown in Table [11.](#page-8-3)











<span id="page-8-0"></span>



**FIGURE 16.** Fractional-order Soliman72 Filter Schematics.

## <span id="page-8-7"></span>K. FRACTIONAL-ORDER Soliman76 FILTER

Presented in [\[68\], t](#page-14-28)he fractional-order version of this filter is presented in Fig. [19](#page-8-4) and the transfer function is shown in Table [12.](#page-8-5)

<span id="page-8-1"></span>

**FIGURE 17.** Fractional-order Soliman73 Filters Family: (a) APF I, (b) APF II.

<span id="page-8-3"></span>**TABLE 11.** Fractional-order Soliman74 filter transfer functions.

Filter Type	H ( S			
EO LPE	$(R_a + R_b)(R_c + R_d)$ $R_a R_c R_1 R_2 C_1 C_2$			
	$R_a R_c + R_b (R_c + R_d)$ $s^2+s(\frac{1}{R_1C_1})$ $R_a R_c R_1 R_2 C_1 C_2$ $R_2C_1$			

<span id="page-8-2"></span>

**FIGURE 18.** Fractional-order Soliman74 filter schematics.

<span id="page-8-5"></span>**TABLE 12.** Fractional-order Soliman76 filter transfer functions.

Filter Type	٥
FO LPF	$-as$
	$s^{2\alpha}+s^{\alpha}$ ( $0+1-a$ $R_2C_2$

<span id="page-8-4"></span>

**FIGURE 19.** Fractional-order Soliman76 filter schematics.

<span id="page-8-6"></span>**TABLE 13.** Fractional-order Soliman79 filter transfer functions.

Filter Type	$\epsilon$
FO LPF	$-sa$ $s^2$ <sup><math>\perp</math></sup> $R_1C_2$ $R_1R_2C_1C_2$ $R_1C$ $R_2C_2$

#### <span id="page-8-8"></span>L. FRACTIONAL-ORDER Soliman79 FILTER

Presented in [\[69\], t](#page-14-29)he fractional-order version of this filter is presented in Fig. [20](#page-9-0) and the transfer function is shown in Table [13.](#page-8-6)

<span id="page-9-0"></span>

**FIGURE 20.** Fractional-order Soliman79 filter schematics.

<span id="page-9-2"></span>**TABLE 14.** Fractional-order Soderstrand filter transfer functions.

Filter Type	
FO BPF	$\alpha$ $s^{\alpha+\beta}$ $-\overline{R_2C_{\alpha}}$ + $\overline{R_1R_2C_{\alpha}C_{\beta}(1-K_1K_2)}$ $R_2C_8$ $(1 - K_1 K_2)$

<span id="page-9-1"></span>

**FIGURE 21.** Fractional-order Soderstrand filter schematics.

<span id="page-9-4"></span>**TABLE 15.** Fractional-order AM transfer functions.

Filter Type	
<b>Low-Pass</b>	$R$ 9 $R_{\rm B}$ િવ $s^2+s\frac{1}{C_2}$ $R_6$ $R_2R_4R_5$
High-Pass	$\boldsymbol{s}$ Сe $R_3$ $s^2+s\frac{1}{C_2R_6}$ $R_2R_4R_5$ $C_{2}$
<b>Band-Pass</b>	$r_4C_2$ $R_3$ $s^2 + s \frac{1}{C_2 R_6}$ $C_2R_2R_4R_5$
Notch	$C_1C_3$ Rэ $s^2+s\frac{1}{C_2R_6}$ $R_2R_4R_5$

<span id="page-9-6"></span>**TABLE 16.** Fractional-order HS transfer functions.



## <span id="page-9-7"></span>M. FRACTIONAL-ORDER SODERSTRAND FAMILY

Presented in [\[70\], t](#page-14-30)he fractional-order version of this filter is presented in Fig. [21](#page-9-1) and the transfer function is shown in Table [14.](#page-9-2)

## <span id="page-9-8"></span>N. FRACTIONAL-ORDER AKERBERG-MOSSBERG FAMILY

Presented in [\[71\], t](#page-14-31)he fractional-order version of this filter is presented in Fig. [22](#page-9-3) and the transfer function is shown in Table [15.](#page-9-4)

<span id="page-9-3"></span>



**FIGURE 22.** Fractional-order AM Filters Family: (a) FOLPF, (b) FOHPF, (c) FOBPF, (d) FONF.

<span id="page-9-5"></span>

**FIGURE 23.** Fractional-order HS Filters Family: (a) FOLPF, (b) FOLPF, (c) FOHPF, (d) FOBPF.

## <span id="page-9-9"></span>O. FRACTIONAL-ORDER HAMILTON-SEDRA 1972 (HS I)

Presented in [\[72\], t](#page-14-32)he fractional-order version of this filter is presented in Fig. [23](#page-9-5) and the transfer function is shown in Table [16.](#page-9-6)

## P. FRACTIONAL-ORDER

## BHATTACHARYYA-MIKHAEL-ANTONIOU (BMA)

<span id="page-9-10"></span>Presented in [\[73\], t](#page-14-33)he fractional-order version of this filter is presented in Fig. [24](#page-10-1) and the transfer functions with conditions on the ports are shown in Table [17.](#page-10-2)

#### <span id="page-10-2"></span>**TABLE 17.** Fractional-order BMA family.



<span id="page-10-1"></span>

**FIGURE 24.** BMA general building block.

## <span id="page-10-0"></span>**VIII. CASE STUDY FOR THE DESIGN OF A LOW-PASS FRACTIONAL-ORDER FILTER BY CIRCUIT SCALING IMPLEMENTATION**

Considering the implementation of the transfer function in Eqn. [26](#page-3-6) with the fractional-order Bach's filter in Fig. [9.](#page-6-3) Comparing the required transfer function with that of the filter in Eqn.  $27$  and using frequency scaling of  $10^4$  and magnitude scaling of  $10^3$ , the values of the required passive

#### <span id="page-10-3"></span>**TABLE 18.** Passive elements values for the circuit scaling design case.



elements are summarized in Table [18](#page-10-3) with  $\alpha$  = 1 and  $\beta = 0.7$ . The fractional order capacitor was implemented using four approximation techniques: CFE, Carlson, Matsuda and Oustaloup. The capacitor was then realized using the FosterI RC network, as shown in Fig. [25a.](#page-11-0) Values for resistances and capacitances of the Foster network are summarized in Table [19.](#page-12-0) Using TL084C Chip as active elements and using the fractional-order capacitor on NISC PCB, Fig. [25b](#page-11-0) shows the setup for the filter experiment on the NI ELVIS II kit. Figure [26](#page-11-1) shows the phase realization errors for the fractional-order capacitors in the four cases where ideal, approximation and realizable cases are compared. The realizable case is plotted using the values Table [19](#page-12-0) to account for the round-off error in computing Foster I, which gives an

<span id="page-11-0"></span>

**FIGURE 25.** Fractional-order filter experiment: (a) Foster I schematics, (b) Setup.

<span id="page-11-1"></span>

**FIGURE 26.** Phase response and phase error for the fractional-order capacitor realization by: (a) CFE, (b) Matsuda, (c) Oustaloup, (d) Carlson.

intuition for the best realizable CPE. Data was taken from the NI ELVIS bode analyzer and plotted on MATLAB in Figs. [27-](#page-11-2)[28.](#page-11-3)

## **IX. CASE STUDY FOR THE DESIGN OF A LOW-PASS FRACTIONAL-ORDER FILTER BY DIRECT CIRCUIT IMPLEMENTATION**

In the direct implementation of a fractional-order order filter, three critical frequencies are to be evaluated and mapped to the passive elements to achieve the required performance. The critical frequencies are the maxima frequency, as shown in Eqn. [29,](#page-11-4) the half-power frequency shown in [30,](#page-11-5) and the right-phase frequency shown in Eqn. [31](#page-11-6) [\[74\].](#page-14-34)

$$
\frac{d}{d\omega}|H(j\omega)|_{\omega_m} = 0.
$$
\n(29)

$$
|H(j\omega)|_{w_m} = \frac{1}{\sqrt{2}} |H(j\omega_{passband}|). \tag{30}
$$

<span id="page-11-2"></span>

**FIGURE 27.** Experimental magnitude response for the filter with the fractional-order capacitor realized by: (a) CFE, (b) Matsuda, (c) Oustaloup, (d) Carlson.

<span id="page-11-3"></span>

**FIGURE 28.** Experimental phase response for the filter with the fractional-order capacitor realized by: (a) CFE, (b) Matsuda, (c) Oustaloup, (d) Carlson.

<span id="page-11-6"></span>
$$
\angle H(j\omega)_{\omega_{rp}} = \pm \frac{\pi}{2}.
$$
 (31)

The first design step is to factorize the filter's TF. For example, Bach's FOLPF TF in [27](#page-5-6) gives the factorized transfer function in Eqn. [32.](#page-11-7)

<span id="page-11-7"></span>
$$
H(s) = \frac{b}{s^{\alpha + \beta} + as^{\beta} + b},
$$
\n(32)

<span id="page-11-9"></span>where *a* and *b* are the factors to be computed from solving the critical frequencies equations. The second step is to apply the three frequency equations to Eqn. [32,](#page-11-7) which gives the three implicit nonlinear equations Eqn. [33.](#page-11-8)

$$
AA' + BB' = 0,\t(33a)
$$

$$
A^2 + B^2 = b^2, \t(33b)
$$

<span id="page-11-8"></span>
$$
A = 0,\t(33c)
$$

<span id="page-11-5"></span><span id="page-11-4"></span>where  $A$ ,  $A'$ ,  $B$ , and  $B'$  are summarized in Table [20.](#page-12-1)

<span id="page-12-0"></span>**TABLE 19.** Foster I Rs and Cs values for realization of the fractional-order capacitor.

Index	<b>CFE</b>		Oustaloup		Matsuda		Carlson	
	$R(K\Omega)$	C(nF)	$R(K\Omega)$	C(nF)	$R(K\Omega)$	C(nF)	$R(K\Omega)$	C(nF)
	0.027	$\blacksquare$	39	$\blacksquare$			130	$\blacksquare$
	0.01	150	0.051	150	0.082	68	0.3	100
2	0.160	470	0.22	220	0.22	220	0.47	330
3	0.270	560	0.91	470	0.56	470	1.1	599
4	0.620	1000	3.3	780	1.8	740	9.1	740
	2.2	1000	15	1220	7.5	1000	$\overline{a}$	$\blacksquare$
6	51	560	100	1150	180	800		$\overline{\phantom{a}}$

<span id="page-12-1"></span>**TABLE 20.** Factors of the implicit critical equations.

$\omega^{\alpha+\beta}\cos(\frac{\alpha+\beta}{2}\pi)+a\omega^{\beta}\cos(\frac{\beta\pi}{2})+b$
$\omega^{\alpha+\beta} \sin(\frac{\alpha+\beta}{2}\pi) + a\omega^{\beta} \sin(\frac{\beta\pi}{2})$
$\frac{(\alpha+\beta)\omega^{\alpha+\beta-1}cos(\frac{\alpha+\beta}{2}\pi)+a\beta\omega^{\beta-1}cos(\frac{\beta\pi}{2})}{\phantom{(\alpha+\beta)}% }$
$(\alpha+\beta)\omega^{\alpha+\beta-1}\sin(\frac{\alpha+\beta}{2}\pi)+a\beta\omega^{\beta-1}\sin(\frac{\beta\pi}{2})$

<span id="page-12-2"></span>**TABLE 21.** Passive elements values for the direct circuit design case.



<span id="page-12-3"></span>

**FIGURE 29.** Simulation and experimental magnitude response for the filter with the fractional-order capacitor realized by: (a) CFE, (b) Matsuda, (c) Oustaloup, (d) Carlson.

Considering the same realized CPE in Table [19,](#page-12-0) the set of the critical equations in [33](#page-11-8) could be solved for a half power frequency of  $f_h = 450Hz$  with  $\alpha = 1$ ,  $\beta = 0.7$ , and  $C_\beta = 2.24 \mu F / s^{1-\beta}$ . The calculated values for the rest of the passive elements are summarized in Table [21.](#page-12-2)

Figures [29](#page-12-3)[-30](#page-12-4) show the magnitude and phase responses of the simulation results performed on OrCAD and experimental results using ELVIS II kit and the realized CPEs in Table [21.](#page-12-2)

<span id="page-12-4"></span>

**FIGURE 30.** Simulation and experimental phase response for the filter with the fractional-order capacitor realized by: (a) CFE, (b) Matsuda, (c) Oustaloup, (d) Carlson.

## **X. CONCLUSION**

This work summarized the main possible design flows of the fractional-order filters. These flows can be generalized to the design of any fractional-order analog system if used with the correct corresponding target system's design equations. The work presents two design methods starting, for both methods, with creating the targeted fractional-order transfer function of the desired system.

The first design method is the circuit implementation which consists of selecting the filter topology. The next step is comparing the topology's transfer function with the desired system to acquire values for the filter's resistors and capacitors. This is followed by realizing the fractional-order capacitors (CPEs) using an appropriate approximation technique and realization network. The final step is to compose the circuit and run a simulation and experiment for verification.

The second method is the design of FPAA. This method consists of choosing an appropriate approximation technique for the Laplacian operators in the targeted transfer function. The next step is substituting the Laplacian operator in the transfer function with the approximated Laplacian operator, which results in an integer-order transfer function. The final step is transforming the approximated integer-order transfer function into a state-space representation such as PFE.

The performance of the fractional-order filter is highly dependent on the approximation process of the CPE. An appropriate reduction technique could reduce the approximation order of the Laplacian operator. The fractional-order filters' design flow using FPAA differs from the circuit's. Both flows were presented with a design case. Giving the entire transfer function of the fractional-order filters active topologies is essential to highlight the fractional *S* domain contributor. So, a short survey was presented on some generalized analog filter topologies. This work could be considered a catalog for fractional-order filters and analog systems designers. Future work will consider mapping more design specs, like quality and shaping factors, to the critical frequencies.

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