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# **RESEARCH ARTICLE**

# Mode Information Guided CNN for Quality Enhancement of Screen Content Coding

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**ABSTRACT** Video quality enhancement methods are of great significance in reducing the artifacts of decoded videos in the High Efficiency Video Coding (HEVC). However, existing methods mainly focus on improving the quality of natural sequences, not for screen content sequences that have drawn more attention than ever due to the demands of remote desktops and online meetings. Different from the natural sequences encoded by HEVC, the screen content sequences are encoded by Screen Content Coding (SCC), an extension tool of HEVC. Therefore, we propose a Mode Information guided CNN (MICNN) to further improve the quality of screen content sequences at the decoder side. To exploit the characteristics of the screen content sequences, we extract the mode information from the bitstream as the input of MICNN. Furthermore, due to the limited number of screen content sequences, we establish a large-scale dataset to train and validate our MICNN. Experimental results show that our proposed MICNN can achieve 3.41% BD-rate saving on average. In addition, our MICNN method consumes acceptable computational time compared with the other video quality enhancement methods.

**INDEX TERMS** Convolutional neural network, deep learning, HEVC, quality enhancement, SCC.

#### **I. INTRODUCTION**

With the rapid development of intelligent terminal technology, mobile devices such as smartphones and tablets have made Screen Content (SC) video more and more widespread. Desktop collaboration, screen sharing, cloud gaming, etc., greatly increase the scope of video applications. Especially due to the recent spread of COVID-19, the demand for online education and virtual conferences is rapidly increasing, with Screen Content Coding (SCC) [\[1\] pla](#page-11-0)ying a critical role. Unlike the natural video sequence, as shown in the example of Fig.  $1(a)$ , captured by a camera, the screen content sequence as in Fig.  $1(b)$  can be generated from different mobile terminals directly. It is composed of many static or moving computer-generated images and texts. It often contains many uniform and flat areas, repeated patterns and limited pixel colors, etc. By making use of these screen

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<span id="page-0-5"></span><span id="page-0-4"></span><span id="page-0-3"></span><span id="page-0-2"></span><span id="page-0-1"></span>content characteristics, SCC [\[1\] is p](#page-11-0)roposed as an extension of High Efficiency Video Coding (HEVC) [\[2\] to](#page-11-1) increase the coding efficiency. In addition to the conventional HEVC intra (INTRA) mode  $[3]$ , the SCC standard adopts two dedicated coding modes, Intra Block Copy (IBC) and palette (PLT) [\[4\], \[](#page-11-3)[5\], \[](#page-11-4)[6\]. IB](#page-11-5)C [\[4\] us](#page-11-3)es the reconstructed block of the current frame as the prediction block. IBC performs motion compensation for the current Coding Unit (CU) in the reconstructed region of the current frame, which can improve the compression efficiency of screen content video by more than 30% in [\[5\]. PL](#page-11-4)T enumerates the color value for each coding block to generate a color table and passes an index for each sample to indicate which color in the color table it belongs to. With PLT, compression efficiency is further improved by 15% over the original code with IBC mode [\[5\].](#page-11-4)

<span id="page-0-0"></span>Although the coding efficiency can be improved by introducing the coding tools, screen content videos still contain compression artifacts corresponding to the dedicated tools in the SCC standard.

<span id="page-1-0"></span>

**FIGURE 1.** (a) Original natural frame, (b) original screen content frame.

<span id="page-1-1"></span>

**FIGURE 2.** Overall framework of our proposed method.

An HEVC codec utilizes a deblocking filter (DF) and a sample adaptive offset (SAO) to eliminate blocking and ringing artifacts, thereby enhancing the quality of the reconstructed frames. In recent years, deep learning has made new progress in this field and has achieved impressive performance in video enhancement. A series of neural network architectures were proposed to remove the artifacts in reconstructed videos. Examples include an In-loop Filter using the Convolutional Neural Networks (IFCNN) [\[7\], a V](#page-11-6)ariablefilter-size Residue-learning CNN (VRCNN) [\[8\], a D](#page-11-7)eep CNN based Auto Decoder (DCAD) [\[9\], a](#page-11-8) Multi-layered Deep CNN (MDCNN) [\[10\], a](#page-11-9)nd a Decoder-side Scalable CNN (DS-CNN) [\[11\]. U](#page-11-10)nlike other architectures that replace the in-loop filter, DCAD and DS-CNN were designed to improve the video quality at the decoder side. The advantage of these post-processing methods is that there is no need to modify the HEVC codec inside. Hence, the structure proposed in this paper focuses on video post-processing at the decoded side, as depicted in Fig[.2.](#page-1-1)

<span id="page-1-8"></span><span id="page-1-7"></span><span id="page-1-6"></span>In addition to the development of network structures, the rich side information in the video bitstream can also help to guide the enhancement process of decoded videos. For example, it was found in [\[12\] th](#page-11-11)at the partition tree in the coding process indicates the corresponding compression loss of the decoded video. To utilize the side information in the HEVC codec, the work in [\[12\] s](#page-11-11)ubsequently proposed a doubleinput network by taking the partition mask into account. The mask is generated based on the partition tree of HEVC, as the side information. With the use of the partition mask, the blocking effect is eliminated. However, this approach is designed for natural videos. It still ignores the characteristics of the screen content video. In other words, the utilization of side information is not closely related to the screen content characteristics.

In summary, the novelty and contributions of our work are twofold:

<span id="page-1-10"></span><span id="page-1-9"></span>

- We propose a novel post-processing network for enhancing decoded screen content videos based on the coding mode information embedded in the coded bitstream. Three binary mode masks derived from the dedicated coding tools in SCC are fused with the corresponding decoded frame.
- We establish a large-scale dataset containing 9810 frames for screen content videos. This dataset will be publicly available to facilitate further research.

The remainder of the paper is organized as follows. The related works are provided in Section [II.](#page-1-2) In Section [III,](#page-2-0) we describe the generation of the proposed mode mask and the details of our proposed network architecture. Experiments and ablation studies are brought in Section [IV.](#page-4-0) Section [V](#page-5-0) concludes this paper.

#### <span id="page-1-2"></span>**II. RELATED WORKS**

#### A. DEEP LEARNING-BASED VIDEO QUALITY ENHANCEMENT

<span id="page-1-5"></span><span id="page-1-4"></span><span id="page-1-3"></span>In recent years, deep learning has been successfully applied to computer vision tasks. Many works have been applied to improve the visual quality of HEVC videos. They are divided into two major approaches. One is to modify the internal module within the codec, such as in-loop filtering for visual quality enhancement [\[8\]. A](#page-11-7)nother approach uses post-processing techniques to improve the video quality after the decoder [\[9\], \[](#page-11-8)[11\]. F](#page-11-10)or the former one, the HEVC standard specifies an in-loop filter [\[2\], w](#page-11-1)hich comprises a deblocking filter and a sample adaptive offset (SAO). The in-loop filter is embedded in the encoding and decoding loops, after inverse quantization and before saving the decoded picture in the decoded picture buffer to improve image quality. The work in [\[7\] sug](#page-11-6)gested a new in-loop filtering technology in HEVC using convolutional neural networks (CNN), namely IFCNN, to replace the SAO filter for coding efficiency and subjective visual quality improvement. Inspired by Deep Convolution Networks for Compression Artifacts Reduction (ARCNN), Dai et al. [\[8\] pro](#page-11-7)posed a Variable-filter-size Residue-learning CNN (VRCNN), which can improve the visual quality of HEVC videos without increasing the bit rate compared to the original in-loop filter in HEVC. However, the above networks cannot be directly applied to compressed videos, as they were designed as a part of the HEVC encoder. For the latter approach, a Deep CNN-based Auto Decoder (DCAD) [\[9\] wa](#page-11-8)s developed to improve the visual quality through deep learning only at the decoder side. Later, a Decoder-side Scalable CNN (DS-CNN) was proposed by Yang et al. [\[11\] w](#page-11-10)herein there are two subnetworks, DSCNN-I and DSCNN-B, to reduce the artifacts of intra-coded and inter-coded frames, respectively. In [\[13\], Q](#page-11-12)E-CNN-I and QE-CNN-P were also proposed to enhance the intra-coded frames and inter-coded frames, respectively. In [\[14\], H](#page-11-13)uang proposed a cross feature fusion framework to enhance the gaming video in the decoder side. Notably, these works only focus on the decoded frame as the input of the network. They do not consider the information extracted from the bitstream.

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<span id="page-2-1"></span>

**FIGURE 3.** Dual-input network model structure.

<span id="page-2-2"></span>

**FIGURE 4.** Examples of (a) boundary mask and (b) mean mask.

### B. DUAL-INPUT CNN ON VIDEO QUALITY ENHANCEMENT

Recently, a dual-input network has been proposed for visual quality enhancement on natural videos. The beginning of the dual-input network in Fig. [3](#page-2-1) is composed of two branches –the main branch and the mask branch. The mask branch utilizes the compressed information extracted from the bitstream as side information for the neural network. The main branch and the mask branch in Fig. [3](#page-2-1) are fused at certain position within the network. The post-processed frame with reduced artifacts is finally obtained. For instance, a partitionaware convolution neural network was proposed in [\[15\],](#page-11-14) which uses the partition information produced by the encoder to assist post-processing at the decoder side. In particular, it adopts the boundary mask and the mean mask to guide the neural network. In Fig.  $4(a)$ , the boundary mask represents CU partition information by setting the CU boundary region as 255 and the non-boundary CU region as 0. The mean mask, as shown in Fig. [4\(b\),](#page-2-2) represents CU partition information by filling each CU with the mean value of all pixel values within each CU. Either one of these two masks can be input into the model in Fig. [3](#page-2-1) as a grayscale image. Inspired by He et al. [\[12\], a](#page-11-11)nother dual-input model proposed by Hoang and Zhou [\[16\], a](#page-11-15) Deep Recursive Residual Network with Block information (B-DRRN), also employs the mean mask as side information. However, these dual input networks only focus on natural videos and do not consider specific features of screen content. In contrast, this paper proposes a novel multi-input CNN that utilizes decoded frames with the mode information of SCC as the input. The idea is to utilize three binary masks, including the information of IBC mode, PLT mode, and INTRA mode to further enhance the quality of screen content videos.

<span id="page-2-3"></span>

**FIGURE 5.** (a) Original frame, and (b) associated coding modes (red: INTRA, yellow: PLT, blue: IBC).

# <span id="page-2-0"></span>**III. PROPOSED MULTI-INPUT CNN FOR VIDEO ENHANCEMENT**

In this section, we describe our network architecture in detail. The framework of the proposed MICNN is shown in Fig. [2.](#page-1-1) To exploit the side information from the bitstream, we propose three binary masks dedicated to screen content videos. This is the first work to enhance the SC quality using deep learning with the help of mode information as the binary mask input into the deep network.

#### <span id="page-2-4"></span>A. MOTIVATION

Owing to the block dividing process and quantization in HEVC, the artifact of decoded video corresponds highly to the CU information. Because of that, there are some important clues contained in CU information that can be used to eliminate the artifact of decoded videos. Recently, the works in [\[12\] an](#page-11-11)d [\[15\] ha](#page-11-14)ve proven that using the mean mask or the boundary mask can achieve better performance in the postprocessing method.

<span id="page-2-5"></span>However, screen content videos have different characteristics to natural videos, they often contain many uniform and flat areas, repeated patterns, and limited pixel colors. CU information cannot represent these characteristics. Therefore, various mechanisms of video quality enhancement are required for these different types of content. To identify natural content and screen content such that our MICNN can effectively enhance the reconstruction quality of different contents, it can be guided by the coding mode. Fig. [5](#page-2-3) explains the relationship between content type and coding mode. Fig.  $5(a)$  shows a frame with mixed content, and

<span id="page-3-1"></span>

**FIGURE 6.** Examples of three binary mode masks. (a) Original frame with CU partition, (b) IBC binary mask, (c) PLT binary mask, and (d) INTRA binary mask.

Fig. [5\(b\)](#page-2-3) illustrates the associated coding modes, highlighted by different colors. As shown in Fig. [5\(b\),](#page-2-3) red, yellow, and blue boxes are used to represent INTRA, PLT, and IBC, respectively. INTRA is known to encode natural content. IBC and PLT are designed for screen content: [\(1\)](#page-3-0) IBC can find almost exact matching for certain CUs within the same frame due to the massive existence of texts and computergenerated graphics, and [\(2\)](#page-3-0) PLT can well handle the CUs with only a few distinct colors. Therefore, the coding modes embedded in the coded bitstream are good candidates for identifying CU content types that can be used to guide the video quality enhancement in screen content videos. In the following section, we propose to use three binary mode masks devised by different coding modes, IBC, INTRA, and PLT, in our new MICNN to improve the visual quality of screen content. Through the input of mode information, MICNN can eliminate different artifacts of decoded screen content videos according to the content encoded by different coding modes.

#### B. BINARY MODE MASKS

Based on the above motivation, three binary mode masks, *MIBC*, *MPLT* , and *MINTRA* are defined based on different coding modes – IBC, PLT and INTRA, respectively. They are used for different types of content, resulting in different artifacts in the decoded SC video.

Suppose  $Mode(CU(x, y))$  is the coding mode in which the pixel location  $(x, y)$  belongs to a particular CU, and  $M_{mode}(x, y)$  is the binary value of the element at  $(x, y)$ , where  $mode \in \{IBC, PLT, INTRA\}$ .  $M_{mode}(x, y)$  is set to 1 when  $(x, y)$  belongs to the CU encoded as *mode*  $\in$  ${IBC, PLT, INTRA}$ . Otherwise,  $M_{mode}(x, y)$  is filled with the value of 0. Then, the binary values of the elements of *MIBC*, *MPLT* , and *MINTRA* can be generated as follows:

<span id="page-3-0"></span>
$$
M_{IBC}(x, y) = \begin{cases} 1, & if \; Mode(CU(x, y)) \in IBC \\ 0, & if \; Mode(CU(x, y)) \notin IBC \end{cases} \tag{1}
$$

$$
M_{PLT}(x, y) = \begin{cases} 1, & \text{if } Mode(CU(x, y)) \in PLT \\ 0, & \text{if } Mode(CU(x, y)) \notin PLT \end{cases}
$$
 (2)

$$
M_{INTRA}(x, y) = \begin{cases} 1, & if \; Mode(CU(x, y)) \in INTRA \\ 0, & if \; Mode(CU(x, y)) \notin INTRA \end{cases}
$$
 (3)

Figure [6](#page-3-1) shows the examples of the IBC binary mode mask, PLT binary mode mask, and INTRA binary mode mask based on the assigned values using  $(1)-(3)$  $(1)-(3)$  $(1)-(3)$ .

# C. PROPOSED MODE INFORMATION GUIDED CNN (MICNN)

The baseline CNN architecture is shown in Fig[.7\(a\),](#page-4-1) where our proposed mode information guided CNN (MICNN) is adopted. The MICNN architecture consists of three components, *i.e., feature extraction, feature fusion, and reconstruction.* In the feature extraction stage, one main branch and three sub-branches are used to extract features. The decoded frame is fed into CNN through the main branch and the binary mode masks *MIBC*, *MPLT* , and *MINTRA* are the inputs of the three sub-branches.

The binary mode masks are the side information. They are fed into the neural network and combined with the decoded frame. Therefore, the order of the three binary mode masks fused in the neural network are considered, and ablation study related to various orders will be made later. From Fig.  $7(b)$ , we can see the detail of our proposed fusion method. The features extracted from different binary mode masks will be added to the feature extracted from decoded frame in order.

<span id="page-3-2"></span>Moreover, Residual Dense Blocks (RDBs) represented in Fig. [7\(c\)](#page-4-1) are stacked as the main branch of the proposed MICNN. As shown in Fig.  $7(c)$ , the RDB contains three groups of convolutional layers that are in dense connection [\[17\]. E](#page-11-16)ach group consists of two convolutional layers with a size of  $3 \times 3$  and two ReLU activation functions. Meanwhile, the residual connection in each RDB is employed to reduce the gradient vanishing problem and help the backpropagation. Compared with the original residual block as shown in Figure  $7(d)$ , RDB uses dense connection which can exploit hierarchical features.

To formulate the MICNN model proposed in Fig. [7\(b\),](#page-4-1) it is assumed that the decoded and enhanced frames are represented by  $\tilde{D}$  and  $\tilde{Y}$ , respectively. The composite non-linear mapping including convolutional operation and activation function (ReLU) is denoted as *Hcr* (·). In addition, the RDB is denoted as  $H_{RDB}(\cdot)$ . The output of the main branch in the feature extraction stage can then be obtained by

$$
\tilde{y} = H_{RDB}(H_{cr}(H_{RDB}(H_{cr}(H_{RDB}(H_{cr}(\tilde{D}))))))
$$
 (4)

The output of the sub-branches in the feature extraction stage can be formulated as:

$$
\tilde{m}_{ibc} = H_{cr} \left( M_{IBC} \right) \tag{5}
$$

$$
\tilde{m}_{intra} = H_{cr} \left( M_{INTRA} \right) \tag{6}
$$

$$
\tilde{m}_{plt} = H_{cr} \left( M_{PLT} \right) \tag{7}
$$

where  $\tilde{m}_{ibc}$ ,  $\tilde{m}_{intra}$ , and  $\tilde{m}_{plt}$  are defined as the feature maps of the IBC mode mask, INTRA mode mask, and PLT mode

<span id="page-4-1"></span>

FIGURE 7. (a) The baseline CNN structure without binary mode masks, (b) the proposed MICNN structure, (c) Residual Dense Block (RDB), and (d) Traditional Residual Block.

mask, respectively. These feature maps are then integrated into the main branch in the feature fusion stage, which can be formulated as:

$$
\tilde{y}_{ibc} = \tilde{y} + \tilde{m}_{ibc} \tag{8}
$$

$$
\tilde{y}_{intra} = H_{cr} \left( \tilde{y}_{ibc} \right) + \tilde{m}_{intra} \tag{9}
$$

$$
\tilde{y}_{plt} = H_{cr} (\tilde{y}_{intra}) + \tilde{m}_{plt} \tag{10}
$$

where  $\tilde{y}_{ibc}$ ,  $\tilde{y}_{intra}$ , and  $\tilde{y}_{ibt}$  denote the output after adding the IBC mode mask, the INTRA mode mask, and the PLT mode mask in order, respectively. Finally, the reconstructed frame can be generated as:

$$
\tilde{Y} = H_c \left( H_{cr} \left( \tilde{y}_{plt} \right) \right) + \tilde{D} \tag{11}
$$

where  $H_c(\cdot)$  denotes the convolutional operation.

The proposed network is trained in an end-to-end manner. To optimize our model, we apply Mean Squared Error (MSE) as the loss function. Given a training set  $\left\{\tilde{D}_{i}, M_{\text{IBC},i}, M_{\text{INTRA},i}, M_{\text{PLT},i}, Y_{i}\right\}^{N}$  $\sum_{i=1}^{n}$ , where *N* is the number of patches in the training set. Here,  $Y_i$  is the

ground truth patch of the decoded patch  $\tilde{D}_i$  and the set  ${M_{IBC,i}, M_{INTRA,i}, M_{PLT,i}}_{i=1}^N$  are the patches of mode information. The loss function can be formulated as:

$$
L(\theta) = \frac{1}{N} \sum_{i=1}^{N} \left\| H\left(\tilde{D}_{i}, M_{IBC,i}, M_{INTRA,i}, M_{PLT,i}\right) - Y_{i} \right\|_{2}^{2}
$$
\n(12)

where  $H(\cdot)$  denotes our proposed network and  $\theta$  denotes all the parameters.

# <span id="page-4-0"></span>**IV. PROPOSED POLYUSCC DATASET**

<span id="page-4-2"></span>The work of this paper mainly focuses on video quality enhancement of SC sequences. However, the number of SC sequences is limited. To avoid overlapping with the sequences provided in the Common Test Condition (CTC) [\[19\], S](#page-11-17)C sequences were gathered from other sources  $[18]$ ,  $[20]$ , or self- capture  $[21]$  to form our dataset, ''PolyUSCC''. Thirty-four HEVC standard video sequences of various resolutions HEVC standard video sequences of various resolutions (1920  $\times$  1080, 1680  $\times$  1050, 1280  $\times$  720)

#### <span id="page-5-1"></span>**TABLE 1.** Dataset.



<span id="page-5-2"></span>

**FIGURE 8.** Examples of self-captured sequences. (a) airplanevideocmd, (b) consoledocument, (c) consolenew, and (d) cmd3.

are adopted, as shown in Table [1.](#page-5-1) These sequences can be divided into three types: text and graphics with motion (TGM), animation (A), and mixed (M) content. The mixed content contains natural content and screen content. The text and graphics with motion (TGM) consists of text, graphic and animation. The animation (A) only contains the gaming content. To make the database focusing on the different types of screen content, the number of TGM sequences is twice the amount of the mixed content. The dataset consists of three parts. First, to guarantee data reliability and availability, half of the dataset (15 sequences) are provided from the JCT-VC [\[18\] b](#page-11-18)ut not included in CTC [\[19\]. S](#page-11-17)econd, there are 5 SC

#### <span id="page-5-3"></span>**TABLE 2. Different orders of the binary mode masks at QP=37.**



1: ibc-plt-intra 2: plt-ibc-intra 3: intra-plt-ibc 4: ibc-intra-plt 5: intra-ibc-plt 6: plt-intra-ibc 7: mean mask

sequences from Tsang et al. [\[20\]. T](#page-11-19)o enrich the text and graphics with motion content and mixed content sequences, we further capture 14 video sequences by ourselves. Some examples of our self-captured videos are represented in Fig. [8.](#page-5-2) Our self-captured sequences will be published on website [\[21\].](#page-11-20) During the evaluation of the proposed MICNN, 27 sequences are used for training and the remaining 7 sequences are used for validation, as shown in Table [1.](#page-5-1)

## <span id="page-5-0"></span>**V. EXPERIMENTAL RESULTS**

### A. EXPERIMENTAL SETTING

Training of MICNN requires a dataset of training examples, which are pairs of inputs and the corresponding outputs. The video sequences in PolyUSCC were encoded by the HEVC reference software HM16.20-SCM8.8 [\[2\] un](#page-11-1)der All-Intra (AI) configuration as the input of networks, while the uncompressed raw video sequences were used as the output of networks. Considering that different Quantization Parameters (QPs) in HEVC have different compression results with varying degrees of artifacts, four different QPs of 22, 27, 32, and 37 were set to ensure that the results of the experiments are more representative. One model was trained for one QP. For each frame, only the luminance channel (Y channel) was considered as input for training. Model construction and training were based on PyTorch. The patch size of each input image and its corresponding ground truth were  $64 \times 64$ . We randomly selected one patch from one frame for each iteration. To guarantee the robustness of our dataset, we select all frames in our training process. In our experiments, the learning rate was set to 0.0001 for QP37. We fine-tuned the learning rate as QP decreases. The adaptive moment estimation (Adam) optimization method was used to train the model for 500 epochs. A computer equipped with Windows 10 operating system, Intel i9-10900K CPU, 64 GB RAM, and NVIDIA 3090Ti GPUs was used to perform the model training.

The test set contains 12 video sequences provided in the CTC [\[19\], n](#page-11-17)one of which is the same as the training set and validation set. This is essential to avoid overfitting issue.

<span id="page-6-0"></span>

**FIGURE 9.** (a) Proposed fusion strategy, (b) Early Fusion by Concatenation (EFC), and (c) Late Fusion by Concatenation (LFC).

<span id="page-6-1"></span>**TABLE 3. Different fusion strategies at QP=37.** 



#### B. ABLATION STUDY

As mentioned in Section [III,](#page-2-0) the order of the three binary mode masks fused in our proposed MICNN will affect performance. An ablation study was conducted to decide the order of the three binary mode masks and verify the necessities and the generalization ability of our proposed masks. Various MICNN architectures were compared to find the optimal order of inputting binary mode masks. It includes all possible combinations as in Table [2](#page-5-3) : *ibc-plt-intra, plt-ibc-intra, intraplt-ibc ibc-intra-plt intra-ibc-plt*, and *plt-intra-ibc*. These notations represent different orders of the binary mode masks by name. For example, *ibc-plt-intra* means first use the IBC mode mask, then add the PLT mode mask, and finally use the INTRA mode mask. Furthermore, to verify the superiority of our mode mask, we input the mean mask proposed in [\[15\]](#page-11-14) into the baseline model in Fig.  $7(a)$  with the same number of layers and the same training process. The PSNR improvement

#### <span id="page-6-2"></span>**TABLE 4. Different masks at OP=37.**



#### <span id="page-6-3"></span>**TABLE 5. Different baselines at QP=37.**



1: Mode+Residual Block 2: Mode+Dense Block 3: Proposed **MICNN** 

of various combinations on the validation set under AI configuration is shown in Table [2.](#page-5-3) It can be seen that *ibc-intraplt* can achieve the highest PSNR improvement (0.629 dB) over the SCC baseline at QP=37. So, we will use the order of *ibc- intra-plt* to compare other enhancement algorithms in the following discussions. To further verify the efficiency of our proposed fusion approach as in Fig.  $9(a)$ , we also evaluated two different fusion strategies - Early Fusion by Concatenation (EFC) and Late Fusion Concatenation (LFC), as shown in Fig.  $9(b)$  and Fig.  $9(c)$ , respectively. In EFC, we concatenate the decoded frame and binary mode masks as the input. The main branch of EFC is the same with our proposed MICNN. On the other hand, the subbranch of the LFC is the same as our proposed MICNN. As compared with MICNN, LFC concatenates all feature maps of decoded frame and binary mode masks before the feature reconstruction stage. The PSNR improvements for various fusion strategies are shown in Table [3.](#page-6-1) It can be seen that our proposed fusion strategy can achieve the highest PSNR improvement and it can make better use of the mode information. In Table [4,](#page-6-2) to further verify the contribution of our proposed mode masks, we remove the intra mode mask, ibc mode mask, and plt mode mask, respectively. The result shows that the best performance can be achieved when the three mode masks are adopted.

To verify the power of feature extraction of the RDB, we employed the traditional Residual Block as shown in Figure  $7(d)$  and the traditional Dense Block [\[17\] in](#page-11-16)stead of the RDB for compression. The results are shown in Table [5,](#page-6-3) the RDB can achieve the highest PSNR performance. Combining residual block and dense connection can help to extract the

# <span id="page-7-0"></span>**TABLE 6.** Overall  $\triangle$ PSNR of different models at qp = 22, 27, 32, 37 under ai configuration.



1: BasketballScreen 2: ChineseEditing 3: MissionControlClip2 4: MissionControlClip3 5: scconsole 6: scdesktop 7: scflyingGraphics 8: scmap 9: scprogramming 10: scrobot 11: scSlideShow 12: scwebbrowsing

<span id="page-7-1"></span>TABLE 7. Overall ∆SSIM(10<sup>-3</sup>) of different models at qp = 22, 27, 32, 37 under ai configuration.



1: BasketballScreen 2: ChineseEditing 3: MissionControlClip2 4: MissionControlClip3 5: scconsole 6: scdesktop 7: scflyingGraphics 8: scmap 9: scprogramming 10: scrobot 11: scSlideShow 12: scwebbrowsing

#### <span id="page-7-2"></span>**TABLE 8.** Overall Bd-rate(%) of different models at qp = 22, 27, 32, 37 under ai configuration.



feature and keep the high frequency details. The reason is that the residual connection can prevent the gradient vanishing and the dense connection can reuse the feature from previous layers.

#### C. OVERALL PERFORMANCE

1) OBJECTIVE VISUAL QUALITY ASSESSMENT

In this section, we compare QECNN [\[13\],](#page-11-12) DCAD [\[9\],](#page-11-8) Partition-aware CNN [\[15\], a](#page-11-14)nd QECF [\[14\] w](#page-11-13)ith our proposed

<span id="page-8-0"></span>

**FIGURE 10.** PSNR improvement curves of partition-aware CNN, DCAD, QECNN, QECF and our MICNN method for sequences, (a) scdesktop, (b) scwebbrowsing, and (c) scflyingGraphics.

MICNN. Table [6](#page-7-0) and Table [7](#page-7-1) show the average PSNR improvement ( $\triangle$ PSNR) and the average SSIM improvement  $(\Delta$ SSIM), respectively, over all frames of each test sequence. In these two tables, the best PSNR/SSIM improvement is highlighted in bold and the underline number is the secondbest PSNR/SSIM improvement. We can see that our proposed baseline and MICNN outperform other methods in most cases. Meanwhile, the proposed MICNN achieves better performance than the proposed single input model. It demonstrates the benefit of using our proposed SCC mode masks.

When QP is 37, the highest PSNR improvement of our-MICNN approach reaches 1.20 dB, i.e., for sequence scwebbrowsing. The average PSNR of our MICNN approach is 0.58 dB, which is 0.03dB higher than that of our baseline model (0.55 dB), 0.41dB higher than that of QECF (0.17 dB), 0.18dB higher than that of Partition-aware CNN (0.40 dB), 0.14dB higher than that of DCAD (0.44 dB), and 0.27dB higher than that of QECNN (0.31 dB). It is noted that QECF includes some specific idea to enhance gaming content. However, it is found that our proposed method can also handle gaming content and text content. Compared with the QECF, our MICNN can achieve an acceptable PSNR improvement (0.14dB) and SSIM improvement (0.0012) in gaming content sequence *scrobot* and outperform other sequences. In addition,  $\triangle$ PSNR curves of three pure screen content videos for DCAD, QECNN, partition-aware CNN, QECF, and our proposed MICNN are shown in Fig. [10.](#page-8-0) The *scdesktop* is mixcontent. The *scwebbrowsing* and *scflyingGraphics* are pure screen content. By utilizing the proposed binary mode masks, MICNN can achieve highest PSNR in each frame of different content. That means our proposed method is robust.

<span id="page-8-1"></span>BD-rate [\[22\] is](#page-11-21) used to indicate the bitrate savings of these models under the equivalent PSNR. Experimental results are compared and tabulated in Table [8.](#page-7-2) It shows that our proposed MICNN can achieve higher BD-rate savings than its corresponding baseline. Again, this demonstrates the effectiveness of using mode masks. Our MICNN obtains an average BD-rate savings of 3.41%, while the second-best method achieves an average BD-rate savings of only 2.97%. For the test sequence *scSlideShow*, up to 6.76% BD-rate saving is obtained for the Y component under AI configuration. We conjecture that our MICNN well exploits the mode information to further enhance the decoded frame quality and reduce the BD-rate.

# 2) SUBJECTIVE VISUAL QUALITY COMPARISON

This section compares the subjective quality of different models. Fig. [11](#page-9-0) shows the subjective visual quality performance of various models on the sequences *scSlideShow*, *scprogramming*, and *scflyingGraphics* with QP = 37. From this figure, we can see that the reconstructed frame of HM16.20-SCM8.8 has obvious compression artifacts, which cannot be completely removed by DCAD, QECNN, or QECF. As shown in Fig. [11,](#page-9-0) our MICNN eliminates the artifacts more effectively than other models. For *scSlideShow* and *scprogramming*, it can be observed that the character is blurry, and there are some blocking artifacts in the background around the character, but it becomes clearer after being processed by our proposed MICNN. For *scflyingGraphics*, the lines are blurry in the reconstructed frame but becomes shaper in MICNN. In addition, in the reconstructed frame, the flat areas around the lines contain many artifacts. MICNN can smooth these areas. All these examples in Fig. [11](#page-9-0) show that MICNN is superior to the other models in terms of subjective visual quality. There are no uneven regions at the

<span id="page-9-0"></span>

**FIGURE 11.** Subjective visual quality comparison at QP = 37 on (a) scSlideShow, (b) scprogramming,and (c) scflyingGraphics.

CU boundary and no visual blocking effect from the frame processed by MICNN. This again shows that our MICNN can make use of the mode information to enhance the decoded frame quality subjectively.

### 3) QUALITY ENHANCEMENT AT VARIOUS QPs

To verify the generalization ability of the MICNN model on various QPs, we additionally encode all test sequences at QP of 24, 29, 34, 39 when the model is trained at different QPs, i.e. QP=22, 27, 32, and 37. The performance in terms of  $\triangle$ PSNR is shown in Fig. [12.](#page-10-0) Fig. [12\(a\)](#page-10-0) shows the PSNR improvement of the model trained at  $QP = 22$  and tested at  $QP = 22$  and 24. In Fig. [12\(b\),](#page-10-0) the model is trained at  $QP =$ 27 and tested at  $QP = 27$  and 29. Similarly, Fig.  $12(c)$  and Fig. [12\(d\)](#page-10-0) show  $\triangle$ PSNR of the model trained at QP =32 and 37 and tested at different QPs = 32 and 34, 37 and 39,

<span id="page-10-0"></span>

FIGURE 12. APSNR of the model trained and tested at different QPs under AI configuration. (a) Trained at QP=22, Tested at QP=22 and 24, (b) Trained at QP=27, Tested at QP=27 and 29, (c) Trained at QP=32, Tested at QP=32 and 34, and (d) Trained at QP=37, Tested at QP=37 and 39.

respectively. As shown in this figure, each trained model can obtain acceptable quality enhancement on decoded videos at adjacent QPs, which verifies the generalization ability on various QPs.

<span id="page-10-1"></span>

**FIGURE 13.** Average  $\triangle$ PSNR against computational complexity of different methods in the decoder side.

#### <span id="page-10-2"></span>**TABLE 9.** Comparison of running time per frame.



#### <span id="page-10-3"></span>**TABLE 10.** Comparison of model size.



# 4) COMPARISONS ON COMPUTATIONAL COMPLEXITY IN DECODER

To evaluate the computational complexity of various models, we follow the measurement metric of other post-processing algorithms  $[11]$ ,  $[15]$  by computing the running time per Coding Tree Unit (CTU) at the decoder side. Experiments were conducted using Intel i9-10900K CPU, 64 GB RAM, and NVIDIA 3090Ti GPUs. Fig. [13](#page-10-1) shows the average  $\Delta$ PSNR against running time per CTU for MICNN, DCAD [\[9\], QE](#page-11-8)CNN  $[13]$ , QECF  $[14]$ , and partition-aware CNN  $[15]$ methods. The results shown in this figure are calculated over all the test sequences on average. In Fig. [13,](#page-10-1) the running times of DCAD, QECNN, QECF, partition-aware CNN are 0.40 ms per CTU, 0.66 ms per CTU,0.91 ms per CTU, and 2.20 ms per CTU, respectively. On the other hand, our proposed MICNN model consumes approximately 1.08 ms per CTU but achieves the highest PSNR improvement over other models. In Table [9,](#page-10-2) we also compare the overall time consumption in enhancing one frame in different resolutions of different methods. From Table [9](#page-10-2) and Table [10,](#page-10-3) we can observe that the

performance improvement of our MICNN consumes a reasonable amount of computational time compared to QECNN and DCAD. Moreover, MICNN outperforms partition-aware CNN in both running time and  $\triangle$ PSNR.

### 5) MODEL SIZE

Model complexity in terms of model size for various models is also evaluated in Table [10.](#page-10-3) Model size reflects the number of network parameters. Compared to our baseline model, MICNN adds sub-branches to improve performance without significantly affecting model size. Besides, the proposed MICNN can achieve higher performance than the partitionaware CNN, but with smaller model size. It can be concluded that our MICNN obtains better tradeoff between coding efficiency and model size. In other words, our MICNN is more model-efficient.

#### **VI. CONCLUSION**

By integrating our proposed binary mode masks into a mode information guided deep network model, SCC modes extracted from the bitstream can be utilized to further improve SC video quality. Specifically, the new branch uses the binary mode masks, which are based on the coding modes of SCC, to exploit the characteristics of SCC, and then guide the neural network for quality enhancement on screen content videos. This is the first work to incorporate the SCC mode information into the sub-branches for enhancing SC quality. Experimental results show that our proposed MICNN is more effective than other networks. We believe that our mask branches can be easily adopted to different single-input models for further quality enhancement of SCC. In the future, we will move to create a real-time model, which is essential for the further development of real-time applications.

#### **REFERENCES**

- <span id="page-11-0"></span>[\[1\] J](#page-0-0). Xu, R. Joshi, and R. A. Cohen, ''Overview of the emerging HEVC screen content coding extension,'' *IEEE Trans. Circuits Syst. Video Technol.*, vol. 26, no. 1, pp. 50–62, Jan. 2016.
- <span id="page-11-1"></span>[\[2\] G](#page-0-1). J. Sullivan, J. Ohm, W.-J. Han, and T. Wiegand, ''Overview of the high efficiency video coding (HEVC),'' *IEEE Trans. Circuits Syst. Video Technol.*, vol. 22, no. 12, pp. 1649–1668, Sep. 2012.
- <span id="page-11-2"></span>[\[3\] J](#page-0-2). Lainema, F. Bossen, W. Han, J. Min, and K. Ugur, ''Intra Coding of the HEVC Standard,'' *IEEE Trans. Circuits Syst. Video Technol.*, vol. 22, no. 12, pp. 1792–1801, Dec. 2012.
- <span id="page-11-3"></span>[\[4\] X](#page-0-3). Xu, ''Intra block copy in HEVC screen content coding extensions,'' *IEEE J. Emerg. Sel. Topic Circuits Syst.*, vol. 6, no. 4, pp. 409–419, Dec. 2016.
- <span id="page-11-4"></span>[\[5\]](#page-0-4) *IBC+Palette Realizes Screen Content Coding Optimization*. Accessed: Jun. 2022. [Online]. Available: https://cloud.tencent.com/ developer/article/1453587
- <span id="page-11-5"></span>[\[6\] Z](#page-0-5). Ma, W. Wang, M. Xu, and H. Yu, ''Advanced screen content coding using color table and index map,'' *IEEE Trans. Image Process.*, vol. 23, no. 10, pp. 4399–4412, Oct. 2014.
- <span id="page-11-6"></span>[\[7\] W](#page-1-3).-S. Park and M. Kim, "CNN-based in-loop filtering for coding efficiency improvement,'' in *Proc. IEEE 12th Image, Video, Multidimensional Signal Process. Workshop (IVMSP)*, Jul. 2016, pp. 1–5.
- <span id="page-11-7"></span>[\[8\] Y](#page-1-4). Dai, D. Liu, and F. Wu, ''A convolutional neural network approach for post-processing in HEVC intra coding,'' in *Proc. Int. Conf. Multimedia Model. (MMM)*, 2017, pp. 28–39.
- <span id="page-11-8"></span>[\[9\] T](#page-1-5). Wang, M. Chen, and H. Chao, ''A novel deep learning-based method of improving coding efficiency from the decoder-end for HEVC,'' in *Proc. Data Compress. Conf. (DCC)*, Apr. 2017, pp. 410–419.
- <span id="page-11-9"></span>[\[10\]](#page-1-6) S. Kuanar, C. Conly, and K. R. Rao, "Deep learning based HEVC in-loop filtering for decoder quality enhancement,'' in *Proc. Picture Coding Symp. (PCS)*, Jun. 2018, pp. 164–168.
- <span id="page-11-10"></span>[\[11\]](#page-1-7) R. Yang, M. Xu, and Z. Wang, "Decoder-side HEVC quality enhancement with scalable convolutional neural network,'' in *Proc. IEEE Int. Conf. Multimedia Expo. (ICME)*, Jul. 2017, pp. 817–822.
- <span id="page-11-11"></span>[\[12\]](#page-1-8) X. He, Q. Hu, X. Zhang, C. Zhang, W. Lin, and X. Han, "Enhancing HEVC compressed videos with a partition-masked convolutional neural network,'' in *Proc. 25th IEEE Int. Conf. Image Process. (ICIP)*, Oct. 2018, pp. 216–220.
- <span id="page-11-12"></span>[\[13\]](#page-1-9) R. Yang, M. Xu, T. Liu, Z. Wang, and Z. Guan, ''Enhancing quality for HEVC compressed videos,'' *IEEE Trans. Circuits Syst. Video Technol.*, vol. 29, no. 7, pp. 2039–2054, Jul. 2019.
- <span id="page-11-13"></span>[\[14\]](#page-1-10) J. Huang, J. Cui, M. Ye, S. Li, and Y. Zhao, "Quality enhancement of compressed screen content video by cross-frame information fusion,'' *Neurocomputing*, vol. 493, pp. 486–496, Jul. 2022.
- <span id="page-11-14"></span>[\[15\]](#page-2-4) W. Lin, "Partition-aware adaptive switching neural networks for postprocessing in HEVC,'' *IEEE Trans. Multimedia*, vol. 22, no. 11, pp. 2749–2763, Nov. 2020.
- <span id="page-11-15"></span>[\[16\]](#page-2-5) T. M. Hoang and J. Zhou, ''B-DRRN: A block information constrained deep recursive residual network for video compression artifacts reduction,'' in *Proc. Picture Coding Symp. (PCS)*, Nov. 2019, pp. 1–5.
- <span id="page-11-16"></span>[\[17\]](#page-3-2) G. Huang, Z. Liu, L. Van Der Maaten, and K. Q. Weinberger, ''Densely connected convolutional networks,'' in *Proc. IEEE Conf. Comput. Vis. Pattern Recognit. (CVPR)*, Jul. 2017, pp. 4700–4708.
- <span id="page-11-18"></span>[\[18\]](#page-0-6) *Screen Content Sequences Provided by JCT-VC*. Accessed: Apr. 2022. [Online]. Available: https://mpeg.tnt.uni-hannover.de/testsequences/
- <span id="page-11-17"></span>[\[19\]](#page-4-2) *Common Test Conditions for Screen Content Coding*, document JCT-VC, JCTVC-X1015, May/Jun. 2016, pp. 1–6. Accessed: Apr. 2022.
- <span id="page-11-19"></span>[\[20\]](#page-0-6) S.-H. Tsang, Y.-L. Chan, and W. Kuang, ''Mode skipping for HEVC screen content coding via random forest,'' *IEEE Trans. Multimedia*, vol. 21, no. 10, pp. 2433–2446, Oct. 2019.
- <span id="page-11-20"></span>[\[21\]](#page-0-6) *POLYUSCC Provided by Ziyin Huang*. [Online]. Available: https://github.com/HUANGZiyin1/PolyuSCC
- <span id="page-11-21"></span>[\[22\]](#page-8-1) K. Sharman and K. Suehring, *Common Test Conditions*, document JCTVC-Z1100, 26th Meeting, Geneva, Switzerland, Jan. 2017.



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