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Self-Aligned Top-Gate Amorphous Zinc-Tin Oxide Thin-Film Transistor With Source/Drain Regions Doped by Al Reaction

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ABSTRACT A self-aligned fabrication process for top-gate amorphous zinc-tin oxide (a-ZTO) thin-film transistors (TFTs) is developed, in which the source/drain (S/D) doping is realized through depositing a thin aluminum (Al) film on S/D regions and performing a thermal annealing. Results indicate that a chemical oxidation-reduction reaction between Al and a-ZTO films takes place during the thermal annealing process, and shallow donors of oxygen vacancies and metal tin (Sn) interstitials are thus generated. The formed S/D regions have a high carrier concentration over 1×10^{20} cm⁻³, low sheet resistance of 0.57 kΩ/sq, and high thermal stability even in oxygen ambient. The fabricated a-ZTO TFTs exhibit excellent electrical performances, including a low channel-width-normalized S/D resistance of about 7.05 Ω cm, a high field-effect mobility of 15.7 cm²/Vs, a high on/off current ratio of over 10^8 , and near-zero turn-on voltage. Moreover, good electrical stability with less than 0.2-V threshold voltage shift under ±30-V gate bias stresses is also achieved.

INDEX TERMS Amorphous zinc-tin oxide, self-aligned top-gate, thin-film transistor, Al reaction doping, oxidation-reduction reaction.

I. INTRODUCTION

Amorphous metal oxide semiconductors (AOS) thin-film transistors (TFTs) have been demonstrated to have relatively high carrier mobility, good performance uniformity over large-area, and low fabrication cost, and thereby regarded as one of the most promising devices applicable to advanced large area electronics and high-end integrated circuits [1]–[9]. The high mobility of AOS TFTs is usually attributed to the heavy metal cations, such as indium (In), which have an electronic configuration of $(n-1)d^{10} ns^0$ (n \geq 5) allowing large-radius spherical *s* orbitals and sufficient orbital overlapping [6]–[8]. As a result, the superiorities of the In-based AOS TFTs, like amorphous indium-gallium-zinc oxide (a-IGZO) [5]–[8] and amorphous indium-zinc oxide (a-IZO) [8]–[11] have earned them popularity in various applications. Unfortunately, In is toxic and

short of reserve in earth [5], [12], [13]. On the other hand, the metal tin (Sn) is safe and abundant [12], [13], and has the similar electronic configuration [8] and close coordination number [14] to indium in an amorphous oxide system. Thus, the In-free amorphous zinc-tin oxide (a-ZTO) has been investigated as a promising non-toxic low-cost AOS, and reasonably high performance and stability of a-ZTO TFTs have also been well demonstrated in recent years [12], [15]–[22].

Among various AOS TFT structures, the bottom-gate one is not suitable for advanced systems requiring high integration density due to the large footprint, parasitic capacitance and poor scalability imposed by the large overlap between metallic source/drain (S/D) and gate electrode [10], [12], [22]. In contrast, the top-gate (TG) architecture with self-aligned (SA) S/D is ideal for ultrahighresolution displays, BEOL transistors and other high-end



FIGURE 1. (a) Schematic cross-section view and the major fabrication process and (b) top view of the SATG a-ZTO TFT with AI reaction doped S/D regions.

applications. The vital SA formation of the n^+ S/D with the TG a-ZTO TFTs has been successfully implemented using Ar [12] and NF₃ [22] plasma treatment. However, the plasma induced low resistance of AOS tends to be readily returned to the high-resistance state in subsequent post-annealing treatments in an oxidizing ambient [10], [23], [24]. Recently, it has been reported that the aluminum (Al) reaction is an effective and stable S/D doping method for fabrication of SATG a-IZO and a-IGZO TFTs [10], [23]–[26]. And, the Al reaction with a-ZTO films has also been investigated [27]. To date, however, the Al reaction doping method for fabrication of SATG a-ZTO TFTs has not been studied yet.

In this work, a SATG a-ZTO TFT fabrication process is developed, in which the n^+ S/D doping is performed using the metal Al reaction method. High performance of SATG a-ZTO TFTs with low and stable S/D resistance are fabricated with this process. The applicability of the Al reacted S/D doping method for fabrication of TG a-ZTO TFTs is demonstrated well.

II. EXPERIMENTAL DETAILS

Fig. 1 shows the schematic cross-sectional view and the main fabrication process flow of the SATG a-ZTO TFTs with S/D doped using Al reaction. Firstly, the a-ZTO film with 40 nm in thickness was sputtered on the glass substrate by using a ZnSnO ceramic target with Zn:Sn = 2:1 in atomic ratio. The work gas in the sputtering process was the mixture of Ar and oxygen (O₂) with a flow rate ratio of 48:2. Following the a-ZTO island patterning in diluted hydrochloric acid, the gate insulator (GI) of 200-nm-thick silicon oxide (SiO₂) was grown by plasma-enhanced chemical vapor deposition (PECVD), right after the nitrous oxide (N₂O) plasma treatment for 200 s. Next, the samples were annealed at 400 °C in O₂ ambient for 60 min. Following that, the molybdenum (Mo) film was deposited as the gate



FIGURE 2. (a) Transfer curves of SATG a-ZTO TFT with Al reaction treated S/D regions. (b) Channel width-normalized total resistance $(R_{total} W)$ of the fabricated TFTs biased at various gate voltages versus channel length. (c) Output curves of the fabricated TFT.

electrode, and Mo and GI were patterned to form the gate electrode and expose the a-ZTO in S/D regions.

Then. Al reaction doping process was conducted to form the n⁺- a-ZTO S/D regions. A thin Al film with thickness about 5 nm was sputtered first, followed by annealing at 200 °C in air for 30 min. Finally, 100-nm-thick Mo film was sputtered and patterned using lift-off process to form S/D contact electrodes. The AlO_X film in the contact regions generated during the Al reaction process was removed using diluted tetramethylammonium hydroxide (2.38% TMAH) before the contact metal deposition. Fig. 1(b) shows the top view of the fabricated TFT. The electrical characteristics of the fabricated a-ZTO films and TFTs were measured by an Agilent B1500 semiconductor parameter analyzer in dark ambient environment. X-ray photoelectron spectroscopy (XPS) analysis was performed by using a Thermo Fisher ESCALAB 250X interface analysis system equipped with a monochromatized Al anode X-ray source (hv =1486.6 eV). Hall effect measurement was conducted using an HMS-3000.

III. RESULTS AND DISCUSSION

Fig. 2(a) shows transfer curves of the Al reaction-treated SATG a-ZTO TFT with channel width (W) of 20 μ m and length (L) of 10 μ m. The drain currents versus gate voltages were measured at drain to source voltages (V_{DS}) of 0.1 V and 10 V. As shown, with the Al reaction doped S/D, the a-ZTO TFT exhibits excellent comprehensive performances, such as a relatively high field-effect mobility

TABLE 1. Comparision of the effect of different doping processes on ZTO.

Doping process	ZTO thickness (nm)	$R_{ m sheet}$ (k Ω /sq)	Carrier Conc. (cm ⁻³)	Hall Mobi. (cm²/Vs)	Ref.
w/o	40	$> 1 \times 10^{7}$	$< 1 \times 10^{13}$	~ 13	This work
Al reaction	40	0.57	1.22×10^{20}	22.36	This work
Ar plasma	40	2.5	-	-	[12]
NF ₃ plasma	25	1.9	-	22	[22]

 $(\mu_{\rm FE})$ of 15.7 cm²/Vs, a high on/off current ratio over 10⁸, a near-zero turn-on voltage (V_{on}) and a decent subthreshold slope (SS) of ~ 0.3 V/dec. Fig. 2(b) shows the L-dependent W-normalized total resistance $(R_{total}W)$ of the fabricated SATG a-ZTO TFT biased at various gate voltages. The Wnormalized S/D resistance $(R_{SD}W)$ of the Al-reacted S/D was extracted by using transfer length method (TLM) to be 7.05 Ωcm and much lower than that of the plasma treated-S/D [12]. The extracted sub-micrometer L shrinking (2 ΔL = $0.52 \,\mu\text{m}$) suggests a minor lateral dopant diffusion from S/D into channel and thus explains the low channel carrier concentration reflected by the ideal V_{on} , benefiting from the low thermal budget of metal reaction. Fig. 2(c) shows the output characteristics of the fabricated TFTs. No current crowding effect is observed within the low drain voltage region, indicating that a perfect Ohmic contact between a-ZTO S/D and Mo electrodes, and a low enough series resistance in a-ZTO S/D regions were formed. The effectiveness of Al reaction method for doping S/D of SATG a-ZTO TFT is thus well confirmed.

Table 1 summarizes the sheet resistance (R_{sheet}), carrier concentration and Hall mobility of the Al reaction-doped a-ZTO, as well as those of the plasma-treated ones [12], [22]. As shown, R_{sheet} of Al reaction doped a-ZTO is 0.57 k Ω /sq, much smaller than that of the plasma treated ones [12], [22]. Moreover, such a low resistance of the a-ZTO doped by Al reaction method is mainly attributed to the enormous increase in carrier concentration (about seven orders of magnitude), rather than the slight increase in Hall mobility, thus also enabling the perfect S/D Ohmic contact.

To clarify the carrier generation mechanism of the Al reaction process, chemical states of the critical elements in the a-ZTO films without and with Al reaction treatment were comparatively analyzed by XPS. Fig. 3(a)–3(c) respectively shows XPS spectra of the O 1s level, the Sn 3d level and the Zn 2p level. The O 1s peak shown in Fig. 3(a) can be deconvoluted into two subpeaks. The peak centered at the lower binding energy of ~530.0 eV (blue) is associated with the lattice oxygen in the M-O bond of a-ZTO system, while the other one centered at the higher binding energy of ~531.4 eV (red) is associated with the oxygen vacancy. As shown, with Al reaction treatment, the area percentage of the red subpeak increases from 30 to 54%. The result suggests that large amounts of oxygen vacancies be generated



FIGURE 3. XPS spectra of (a) O 1s level, (b) Sn 3d level and (c) Zn 2p level for a-ZTO films without and with Al reaction treatment. (d) XPS spectrum of Al 2p level for a-ZTO film with Al reaction treatment.

during the Al reaction process [26]–[28]. Regarding the Sn 3d signals shown in Fig. 3(b), in addition to the peaks centered at 494.7 eV for Sn $3d_{3/2}$ and 486.4 eV for Sn $3d_{5/2}$, extra peaks centered at the lower binding energies (492.9 eV for Sn $3d_{3/2}$ and 484.4 eV for Sn $3d_{5/2}$) are also observed for the Al reaction-doped a-ZTO, suggesting the formation of metallic Sn interstitials. This means the reduction of oxidation state of Sn element in the a-ZTO system, also consistent with the increase in oxygen vacancies shown in Fig. 3(a). However, as shown in Fig. 3(c), no obvious difference is seen in Zn 2p signals. Fig. 3(d) further shows XPS of Al 2p level of the Al-reacted a-ZTO film, where almost only the oxidation state can be found, suggesting a thorough oxidization of Al into AlO_X in the doping process.

It is thus inferred that an oxidation-reduction reaction between Al and a-ZTO system takes place in the doping process, much similar to what happens in the Al, titanium (Ti) or manganese (Mn) reaction-doped a-IGZO [26], [28], [29]. More specifically, Al elements react with Sn-O bonds of a-ZTO system as the Gibbs free energy of Al with Sn-O ($\Delta G = -539.2$ kJ/mol at 200 °C) is lower than that of Al with Zn-O ($\Delta G = -207.4$ kJ/mol at 200 °C). During the reaction, Al seizes oxygen from the Sn–O bonds of a-ZTO system, like what Al, Ti or Mn does with the In-O bonds of a-IGZO [26], [28], [29], forming oxygen vacancies and simultaneously leaving the displaced Sn interstitials that act as shallow donors. Thus, the oxidation-reduction reaction could be described as

$$4aAl+Zn_2SnO_m = 2aAl_2O_3 + Zn_2Sn_{1-3a}O_{m-6a} + 3aSn_i^{\cdot} + 6aV_O^{\cdot} + 15ae^{-},$$

where the Sn_i is the ionized Sn interstitial and V_O^{\cdot} is the ionized oxygen vacancy.

In order to investigate the thermal stability of the fabricated SATG a-ZTO TFTs with n+ S/D formed by the Al reaction doping, a thermal anneal at 300 °C in O2 was subjected to the devices with Ar plasma and Al reaction doped S/D regions, respectively. Fig. 4(a) and Fig. 4(b) respectively show the evaluations of transfer curves of these two devices with the anneal durations. After 5-min annealing, the transfer curves of the Ar plasma-treated SATG a-ZTO TFT exhibit a much lowered on-state current, indicating a remarkably increased R_{SD} . This is in consistence with the readily thermal annihilation of oxygen vacancies in the Ar plasma-treated n⁺ AOS [10], [23], [24]. In contrast, no noticeable change is seen in the transfer curves of the Al reaction-treated SATG a-ZTO TFT, revealing a better thermal stability. It is reasonably supposed that the good thermal stability originates from the reaction-generated AlO_X . The AlO_X film could isolate S/D regions from the O₂ during annealing, protecting the underneath oxygen vacancies or Sn interstitials from compensation or re-oxidization. To verify this, the same annealing process was conducted to the Al-reacted device with the AlO_X removed. As shown in Fig. 4(c), the transfer curves of the a-ZTO TFT indeed exhibit a much degraded on-state current, similar to what happens in the Ar plasma treated devices. Therefore, the AIO_X byproduct takes the credit of enhanced thermal stability of the Al-reacted SATG a-ZTO TFTs.

The electrical stability of the Al-reacted SATG a-ZTO TFTs was also evaluated. Figs. 5(a) and (b) show evolutions of the transfer curves under positive bias stress (PBS) and negative bias stress (NBS), respectively. It is seen that the devices exhibit a negligible threshold voltage shift (ΔV_{TH}) after being stressed for 3600-s under the PBS and NBS, respectively. It is thus believed that the Al reaction-doping process should not bring the a-ZTO TFTs additional defects which would worsen the electrical stability.

IV. CONCLUSION

A SATG a-ZTO TFT technology featuring Al reaction doping of S/D regions has been developed. The doped a-ZTO film exhibits a low resistivity with the carrier concentration higher than 1×10^{20} cm⁻³. The high concentration of carriers is experimentally verified to originate from the induced oxygen vacancies and metal Sn interstitials in the oxidation-reduction reaction between Al and a-ZTO system. The fabricated SATG a-ZTO TFTs with the proposed process exhibit high performance metrics, such as a high $\mu_{\rm FE}$



FIGURE 4. Transfer curve evolutions after O₂ anneal in (a) Ar plasma-, (b) Al reaction-treated SATG a-ZTO TFTs and (c) Al reaction-treated TFT with the generated AlO_X removed. The inset in Fig. 4(c) is schematic cross-section view of the Al reaction-treated TFT with the generated AlO_X removed.

FIGURE 5. Time evolutions of transfer curves of the fabricated SATG a-ZTO TFTs with Al reaction doped S/D under (a) positive bias stress (PBS) and (b) negative bias stress (NBS).

of 15.7 cm²/Vs, a high on/off current ratio over 10⁸, a nearzero V_{on} , a decent SS of ~ 0.3 V/dec, and negligible ΔV_{TH} of less than 0.2 V under \pm 30-V gate bias stresses, benefiting from the low resistance of the Al-reacted a-ZTO S/D regions and the minor defect diffusion into channel. Moreover, the excellent oxygen-isolation effect of the by-produced AlO_X film ensures a good thermal stability.

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