

# Editorial

## Exciting Progress

**W**E ENTER 2019 with enforced spirit and confidence. The JOURNAL OF ELECTRON DEVICES SOCIETY has developed extremely well over the past few years.

The latest statistics show an exciting increase of submitted and published journal papers. To mention a few numbers we have received 419 submissions in 2018 and published 183 of them. This indicates an acceptance ratio of slightly over 43% which is in line with the policy.

Since 2015 we have received about 4 times more submitted papers and at the same time we have actually lowered the time from submission to publish from 11.8 (2017) to 10.6 weeks 2018. A big thank you to all our editors and reviewers. We are currently discussing ways of reducing the submission to publication time by implementing a shorter review cycle, in addition, to being more stringent on the number of accepted revisions. When expanding a journal this fast it is of utmost

importance to maintain our focus on the highest possible scientific level.

The high interest in publishing in JEDS puts of course a great burden on our editors, so during the fall 2018 we accepted 2 new valuable editors to the team. I'm very glad to welcome Dr. Mirelle Mouis, Research director at the CNRS, Grenoble, and Dr. Nadine Collaert, distinguished member of technical staff, IMEC, Leuven. They will bring additional competence in front-end device processing technology as well as new nanoscale device functionality to the team. Their bios are presented below. During 2019 we will bring a few more editors to the team.

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**Mirelle Mouis** is a Senior Researcher (Directeur de Recherches) with the French National Center for Scientific Research and the Director of the Federation on Micro Nano Technologies, Grenoble, France. Her research deals with the physical analysis of ultimate CMOS architectures, as well as that of nanoscale devices that may extend performance and functionality of future integrated circuits, such as sensors or energy harvesters. This involves device simulation and advanced electrical characterization under various conditions. She has developed strong collaborations with academic, preindustrial and industrial partners and has been participating or managing several research projects at the regional, national, or European level, some of them on-going. She has authored or co-authored 8 chapter books and over 220 papers in international conferences and refereed journals. She has been serving as the Electron Device Society Chapter Chair for the IEEE France section.



**Nadine Collaert** received the M.S. and Ph.D. degrees in electrical engineering from ESAT Department, KU Leuven, Belgium, in 1995 and 2000, respectively. Since 2000, she has been involved in the theory, design, and technology of FinFET devices, emerging memory devices, transducers for biomedical applications and the integration and characterization of biocompatible materials, e.g., carbon-based materials. From 2012 to 2016, she was a Program Manager of the imec LOGIC program, focusing on high mobility channels, TFET, and nanowires. Since 2016, she has been a Distinguished Member of technical staff, responsible for the research on novel CMOS scaling approaches based on heterogeneous integration of new materials with Si and new material-enabled device and system approaches to increase functionality. She has authored or co-authored over 300 papers in international journals and conference proceedings. She holds over ten patents in the field of device design and process technology. She has been a member of the CDT Committee of the IEDM conference and she is still a member of the Program Committees of the international conferences ESSDERC, ULIS/EUROSOI, NMDC, S3S, and the VLSI Technology Symposium.