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Uniform Luminescence at Breakdown in 4H-SiC 4◦-Off (0001) p–n Diodes Terminated With an Asymmetrically Spaced Floating-Field Ring

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ABSTRACT Conventional floating-field rings, which are used to reduce the peak electric field at the periphery of power devices, cause nonuniform avalanche multiplication when applied to planar junctions formed on 4H-SiC substrates misoriented from (0001) toward [1120]. Accordingly, a novel asymmetrically spaced floating-field ring (AS-FFR) was applied to 4H-SiC $4°$ -off (0001) p–n diodes and found to be effective against such nonuniform avalanche multiplication; that is, luminescence at breakdown was nearly uniform when the spacing between the edge of the anode and the inner edge of the AS-FFR was 2.0 μ m in the [1 120] direction and 1.5 μ m in the [1120] direction. This result should contribute to exploring the possibility of 4H-SiC power devices with higher avalanche ruggedness.

INDEX TERMS Aluminum, ion implantation, power semiconductor devices, silicon compounds.

I. INTRODUCTION

4H-silicon carbide (SiC) is an attractive material for high-voltage and high-frequency power devices [1]–[3]. Robust 4H-SiC devices can only be fabricated by using a properly designed termination structure that reduces the peak electric field at the periphery of a device [4]. Among the possible termination structures, floating-field rings (FFRs) [5] are often used because of the simplicity of their fabrication process; however, FFRs were reported to cause non-uniform avalanche multiplication when applied to planar junctions formed on misoriented (0001) substrates [6], [7].

Misorienting a 4H-SiC (0001) substrate is indispensable for step-flow epitaxial growth of 4H-SiC [8]. Aluminum acceptors vertically implanted into a misoriented substrate should thus have asymmetric profiles due to the negligible diffusion of aluminum [9]–[13]. Such asymmetric acceptor profiles are considered to cause the non-uniform avalanche multiplication in 4H-SiC 4◦-off (0001) p-n diodes terminated with a conventional concentric FFR [7].

In the case of merged pn-Schottky diodes terminated with junction termination extension (JTE), continuous avalanche test was reported to form a burn spot when luminescence was observed only at one side of the JTE [14]. Avalanche multiplication in termination region should thus be as uniform as possible, even in the case of FFRs. In this study, a novel asymmetrically spaced FFR (AS-FFR), which was designed based on the recently proposed numerical-analysis methodologies [15], [16], was applied to 4H-SiC 4◦-off (0001) p-n diodes. The resultant nearly uniform luminescence at breakdown should contribute to exploring the possibility of higher avalanche ruggedness.

II. SIMULATION

A schematic device-simulated structure of a p-n diode terminated with a 5- μ m-wide FFR is shown in Fig. 1(a). The structure is similar to that proposed by Cheng et al. for calculating the "*BV-spacing curve*" for the optimal FFR [15], except for the etched surface. Etching the 4H-SiC (0001) surface to expose the experimentally used $4°$ misoriented (0001) surface is necessary [16] since the surface orientation is on-axis (0001) in such commercial device simulators as Sentaurus Device [17] and ATLAS [18]. Structures A and B, respectively, represent an FFR in the $[1\overline{1}20]$ direction and an FFR in the $[11\overline{2}0]$ direction (i.e., the off direction). In both structures, only the outer part of the anode and the inner part of the FFR are included [15]. The anode and a 5-µm-wide FFR were simultaneously formed by Monte Carlo simulation of four-fold aluminum implantation (at room temperature) normal to the wafer surface under the following conditions: $145 \text{ keV}/1.0 \times 10^{14}$ cm⁻², 95 keV/3.5×10¹³ cm⁻², 60 keV/3.0×10¹³ cm⁻², and 35 keV/1.5×10¹³ cm⁻² [16].

The breakdown behavior of a junction, which is protected by an FFR with a spacing *d'*, was simulated at room temperature by assuming vectorial driving forces [17] (with anisotropic impact ionization coefficients α reported by Hatakeyama *et al.* [19]) and a sheet fixed-charge density at the SiO₂/4H-SiC interface of 2×10^{12} cm⁻² [16]. The simulated drift layer had a thickness of 30 μ m and a donor density of 2.9×10^{15} cm⁻³.

Fig. 1(b) and (c) shows the simulated "*BV-spacing curve*" [15], where breakdown voltage of negatively-biased anode is shown as a function of *d'*. The effective reverse bias of FFR represents the electrostatic potential of FFR when the anode undergoes avalanche breakdown. Note here that Fig. 1(b) and (c) shows the breakdown voltage of structures A and B, not the breakdown voltage of the real device structure having the outer half of FFR. Therefore, the value of vertical axis cannot be directly compared at a fixed value of horizontal axis.

As explained by Chen *et al.* [15], the breakdown voltage of anode (*BV*) decreases with the increase of *d'* because of a less protection from the FFR. And when *d'* becomes very large ($> 3.5 \mu$ m) [Fig. 1(b) and (c)], *BV* will decrease to its cylindrical junction value. Under the optimized breakdown condition, the effective reverse bias of FFR thus becomes this cylindrical junction value. The effective reverse bias of FFR becomes 200 V at a d' of 2.0 μ m in structure A [Fig. 1(b)] and 210 V at a d' of 1.6 μ m in structure B [Fig. 1(c)]. These values of *d'* correspond to the optimal spacing. And when these values of *d'* are used, the optimized breakdown voltage of anode becomes 380 V in structure A and 390 V in structure B.

Due to the immaturity of two-dimensional Monte Carlo simulation, i.e., insufficient lateral straggling of aluminum, *BV* becomes less than half the experimental value, as shown as the simulated and measured *BV* at $d' = 2.0 \mu m$ in Fig. 1(b). However, the difference in the simulated *BV* at $d' = 2.0$ and 4.0 μ m (i.e., 180 V) and the difference in the measured *BV* at $d' = 2.0 \mu m$ and infinity (i.e., 178 V) agree very well. Therefore, the optimal *d'* in the $[11\overline{2}0]$ direction is expected to be determined from Fig. 1(c), i.e., 1.6 µm.

FIGURE 1. (a) Modeling structure used in simulation. (b) and (c) Simulated and measured breakdown voltage at RT as a function of spacing *d'***. The measured breakdown voltage at infinity shown in Fig. 1(b) represents the breakdown voltage of a p–n diode without FFR.**

The difference between the above-described simulation result and a well-known result that when compared with the same *d'*, the *BV* of structure B is higher than the *BV* of structure A [7] can be understood as follows: When *d'* in structure B is decreased from 2.0 to 1.6 μ m, the difference between the breakdown voltage of negatively-biased anode and the effective reverse-bias of FFR (Δ) decreases from 190 to 180 V [Fig. 1(c)]. Since the latter value of Δ coincides with Δ of $d' = 2.0 \mu m$ in structure A [Fig. 1(b)], the resultant AS-FFR-terminated p-n diode has the same *BV* in structures A and B but consumes less termination area in structure B (Fig. 2).

FIGURE 2. Schematic of AS-FFR-terminated p–n diode that consist of structures A and B [Fig. 1(a)] with the outer halves of each FFR.

III. EXPERIMENTS

A schematic plan view of the fabricated AS-FFR-terminated 4H-SiC p-n diodes is shown in Fig. 3. Forty-eight diodes were fabricated on a 3-in 4H-SiC epitaxial wafer (with a 30-µm-thick drift layer) and doped with nitrogen donors at 2.9×10^{15} cm⁻³. This epitaxial-layer specification corresponds to a drift layer of 3.3 kV-class power devices terminated with several FFRs [20]. In this study, a single FFR was chosen for simplicity, so the breakdown voltage becomes low (Section II); however, the luminescence at breakdown can be discussed because it is mainly affected by the peak electric field, not by the breakdown voltage itself. In the optimal structure, the peak electric field of an FFR-terminated p-n diode should be the same as that of a multiple FFR-terminated p-n diode [15].

The 4H-SiC substrate, the surface of which was misoriented by $4°\pm0.5°$ from (0001) toward the [1120] direction, had a thickness of 350 ± 25.4 µm and a resistivity of 0.015 -0.025 Ω cm. An ellipsoidal anode was used to exclude possible avalanche multiplication around corners of the anode [6]. Its major axis was $500 \mu m$ long and parallel to the $\langle 1100 \rangle$ direction, while its minor axis was $400 \mu m$ long.

The anode and a 5-µm-wide AS-FFR were simultaneously formed by four-fold aluminum implantation at room temperature under the conditions described in the previous section. The resultant aluminum concentration was about 1×10^{19} cm⁻³ [7].

Spacing between the edge of the anode area and the inner edge of the AS-FFR in the $\overline{11}$ 120] direction was fixed at 2.0 μ m (Section II). In the [1120] direction, on the other hand, spacing (denoted by *d* in Fig. 3) between the edge of the anode area and the inner edge of the AS-FFR was varied from 1.5 to 2.0 μ m.

After the implants were activated by annealing at $1700\textdegree C$, the surface was passivated with dry oxidation. As frontside and backside electrodes, titanium and nickel contacts were formed, respectively. Since this study also aims at applying AS-FFR to junction-barrier Schottky diodes [21],

FIGURE 3. Plan view of a diode with spacing between the edge of the anode area and the inner edge of the floating-field ring of 2.0 *µ***m in the [1 120] direction and** *d* **(1.5, 1.6, 1.7, 2.0** *µ***m) in the [1120] direction. The major axes of p-type anode and anode electrode are, respectively, 500 and 450** *µ***m long.**

FIGURE 4. Example of reverse current density/voltage characteristics of Schottky barrier and p–n diodes, whose anode electrodes are non-Ohmic, terminated with conventional junction termination extension.

the anode contact was non-Ohmic. However, even with such non-Ohmic anode electrode, *BV* can be precisely determined [22], as shown as an example in Fig. 4; *BV* of Schottky barrier and p-n diodes, whose anode electrodes were non-Ohmic, coincides when reverse current density exceeds 10^{-3} A/cm².

The visible-light emissions were observed, and the reverse current/voltage characteristics of the AS-FFR terminated diodes were measured at room temperature.

IV. RESULTS AND DISCUSSION

In agreement with a previously reported result [7], all 12 of the fabricated diodes with d of 2.0 μ m emitted light at avalanche breakdown ($BV = 748 \pm 1$ V at reverse current I_R of 2 mA) on the [1 120] side of the anode [Fig. 5(a) and dashed line in Fig. 6]. When reverse voltage is larger than 720 V in Fig. 6, reverse current density exceeds 10^{-1} A/cm², which is well beyond 10^{-3} A/cm² (Fig. 4). The luminescence shown in Fig. 5 is thus considered to correspond to avalanche multiplication. According to Ono et al., luminescence at

FIGURE 5. Photographically recorded light emissions from a floating-field-ring-terminated 4H-SiC p–n diodes operated at reverse current of 2 mA. (a) $d = 2.0 \mu$ m. (b) $d = 1.7 \mu$ m. (c) $d = 1.6 \mu$ m. (d) $d = 1.5$ μ m.

FIGURE 6. Examples of reverse current/voltage characteristics of fabricated p–n diodes terminated with a conventional FFR (dashed line) and an AS-FFR (solid line).

breakdown of 4H-SiC p-n diodes is attributable to bandto-band transition, interband transition, and Bremsstrahlung (braking radiation) by hot carriers in a Coulomb field of charged impurities [23].

It has to be repeated here that the simulated *BV* is less than half the measured *BV*. This is also the case with the *BV* simulated by assuming scalar driving forces [17] (with isotropic α reported by Konstantinov *et al.* [24]) [7]. These results indicate that the asymmetric nature of impact ionization [19] has little influence on the measured *BV* and that the simulated asymmetric aluminum concentration contours are not accurate enough to reproduce the measured *BV*.

FIGURE 7. Simulated locations (red regions) where impact ionization rate exceeds 1×10¹⁹ cm−3s−¹ with an assumption of a sheet fixed-charge density at the SiO2/4H-SiC interface of 2×10¹² cm−2, together with junction lines (brown lines), of p–n diode terminated with concentric FFR $(d = 2.0 \mu m)$.

On the basis of the accuracy of one-dimensional Monte Carlo simulation [7], the real lateral straggling of aluminum is considered to be larger than the simulated one [16]. The accuracy of two-dimensional Monte-Carlo simulation thus needs to be improved. However, as stated in Section II, quantitative discussion on *d* is considered to be valid.

Fig. 7 shows the peak simulated impact ionization rate (*R*) at breakdown in the $\left[\overline{1} \overline{1} 20\right]$ direction of p-n diodes with *d* of 2.0 µm. The regions where *R* exceeds 1×10^{19} cm^{−3}s^{−1} have about the same cross section around the edge of the anode and around the outer edge of the FFR. Therefore, a half of *IR*, i.e., 1 mA, can be considered to flow from the outer edge of the FFR toward the outer edge of the anode. This corresponds to a fairly large power density (about 200 kW/cm²) on the [1 120] side of the device.

When *d* was reduced, luminescence at breakdown extended toward the other side (i.e., the [1120] direction) of the anode [Fig. 5(b)–(d) and solid line in Fig. 6]. Of all 36 of the diodes with $d < 2.0 \mu m$, BV was 738 \pm 3 V at $I_R = 2$ mA.

These results, together with the reported slightly temperature-dependent *BV* of 4H-SiC diodes [14], indicate that the part of the AS-FFR in the $[11\overline{2}0]$ direction came to function as a FFR; thus, the increase in the temperature of the luminescent region in the $\overline{1}\overline{1}20$ direction is less than that in the case of the conventional FFR. The slightly lower breakdown voltage of the fabricated diodes with reduced *d* thus indicates more rugged operation through avalanching in larger area with less temperature increase.

When d was 1.5 μ m and I_R was increased to 10 mA, nearly uniform light emission was observed around the whole outer edge of the AS-FFR (denoted by A and C in Fig. 8) as well as from the edge of the anode in the [1 120] direction (denoted by B in Fig. 8). The results shown in Figs. 5 and 6 indicate that region C would function more effectively as a FFR if d were decreased to less than 1.5 μ m. This result also supports the need for improvement of the accuracy of two-dimensional Monte Carlo simulation described in Section II.

FIGURE 8. Photographically recorded light emission from the diode with *d* **of 1.5** *µ***m operated at a reverse current of 10 mA, and a schematic cross section of the diode. A, B, and C are possible points from which strong light is emitted.**

Avalanche capability of the fabricated diodes is yet to be measured; however, this observation indicates that avalanche ruggedness of AS-FFR-terminated p-n diodes was possibly improved compared to that of conventional FFR-terminated p-n diodes.

V. CONCLUSION

An asymmetrically spaced floating-field ring (AS-FFR) was found to be effective against non-uniform avalanche multiplication at 4H-SiC planar junctions formed on misoriented (0001) substrates. Luminescence at breakdown was nearly uniform when the spacing between the edge of the anode and the inner edge of the AS-FFR was $2.0 \mu m$ in the $[1\,120]$ direction and 1.5 μ m in the [1120] direction. This result should contribute to exploring the possibility of 4H-SiC power devices with higher avalanche ruggedness.

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