

Brain Inspired Color Feature Selection Chip

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Abstract—Inspired by the mechanism of visual attentional selection, a color feature selection unit consisting of photoreceptors and an attentional selection circuit (ASC) is presented. The detection and wavelength recognition of trichromatic light are accomplished by the photoreceptors with RGB filters. The ASC, fabricated in a standard CMOS technology, activates the competition among photoreceptors to select the principal color-component feature defined as the attentional focus. The number of focuses and resolution can be changed with a top-down stimulus. Electrical test results show that the ASC can select and interpret the most important color-component feature. In optical tests, the ASC chip-based unit can extract the principal color-component of single-color and dual-color light in real-time to complete color recognition. The percentage ratio between the intensity of stray light relative to the light with maximum intensity is up to 93.58%. And the maximum achievable recognition frame rate is 1 kHz, which is more than one order of magnitude higher than the frequency of the biological vision systems. Besides, the unit's outputs can be transformed into binary codes as labels for color recognition, and the color recognition process does not require the participation of ADC, CPU, and memory, consequently avoiding the von Neumann bottleneck.

Index Terms—Attentional selection, color feature selection, color recognition, standard CMOS technology.

I. INTRODUCTION

VISION is the most dominant and important sense of all primates including humans, with 80% or more of all sensory information being perceived through sight [1]. Color is one of the most potent and apparent elements of visual attributes [2]. Therefore, processing the color information perceived by three types

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of cones in the retina is significant for human beings to sense changes in their surroundings [3]. Mimicking the human eye's spectral response, the artificial image sensor system decodes visible light into three current values through photodetectors covered with wavelength selective filters. Data acquisition is completed with analog-to-digital conversion circuits, and the collected data is amplified, compared, and integrated to realize color recognition, which is completely dependent on CPU and memory [4]. These systems are based on the traditional von Neumann architecture, which consumes a lot of energy in data transmission and processing and may not be able to meet real-time recognition requirements [5]. In particular, these color recognition systems have to deal with the overloaded information at the cost of system complexity or power consumption.

In the human visual system, a tremendous amount of visual information falls on the retina at any given moment, far more than the brain's capacity to process. Surprisingly, the brain can deal successfully and meaningfully with the information in a low-power, efficient and rapid way [6]. This process owes much to the mechanism of attentional selection [7]. Visual attentional selection is the most powerful strategy used by the human visual system, which can select, modulate, and sustain focus on the most behaviorally-relevant information while discarding insignificant ones [8], [9]. Most recently, the mechanism of visual attentional selection has been introduced into visual tasks such as machine translation [10], image recognition [11], and semantic scene segmentation [12]. However, most of these applications are based on very large scale integration (VLSI) circuits, which exist bottlenecks in terms of speed and power consumption [13]. With the boom of deep learning, the artificial neural network is being used in the visual system to simulate attentional selection [14], but in practice it may lead to an inaccurate model due to insufficient data and training time [15]. Therefore, it is of great significance for promoting the development of artificial vision to design a hardware implementation of the attentional selection with simple structure, real-time recognition, low power consumption, and miniaturization.

The color recognition process of the human brain is shown in Fig. 1. The photoreceptor cells located in the retina transform visible light into nerve impulses. The neural networks improve the signal selectivity and contrast by weakening the response of nerve impulses near significant targets to select the significantly associated features, completing the color recognition [16]. Attentional selection is attributed to the regulation of neural activity in the primary visual cortex, including the first visual region (V1) and the frontal lobe eye area [17]. Inspired by the visual attentional selection, a color feature selection scheme was proposed, which is also shown in Fig. 1. The color feature

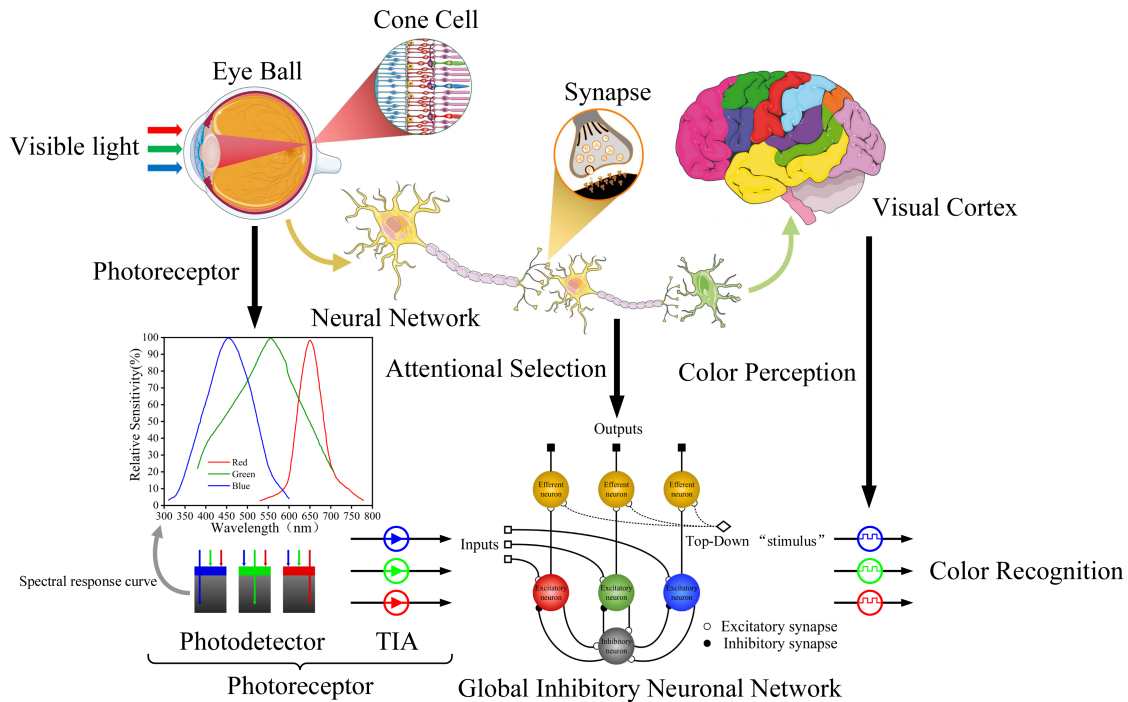


Fig. 1. The functional diagram of the color feature selection unit inspired by visual attentional selection. The photoreceptors are like human cone cells located in the retina that transform visible light into nerve impulses. As one of the most classic global inhibitory neuronal networks, the interaction between excitatory neurons and inhibitory neurons in the ASC can simulate the neural activities related to visual attentional selection.

selection unit can select the most significant information from visible light, and all decisions and information processing are carried out at a low power level. Thus, the unit overcomes the limitations of the Von Neumann architecture [18]. The unit can be divided into two main functional blocks. The first block is photoreceptors, which contain photosensing devices and transimpedance amplifiers (TIAs), generating analog signals whose amplitude is proportional to the light intensity. The second block is the signal processing module, where the attentional selection circuit (ASC) emulates the visual attentional selection and shifts its attention to the most behaviorally-relevant information in real-time. The feature selection unit has the initiative and selectivity [19] like humans by adaptive adjusting the focus of attention to select the principal color-component feature, and it is a miniaturized hardware implementation of the visual attentional selection, which increases performance over existing computer vision feature extraction approaches.

II. ATTENTIONAL SELECTION AND CIRCUIT IMPLEMENTATION

In all primates, attention is a critical factor in the regulation of behavior and neurocognitive functioning [20]. The attentional selection works throughout the brain and participates in almost every stage from sensory processing to decision-making and cognition. The research showed that there are two types of attentional selection [21]. The first one driven by environmental stimuli is usually named as “bottom-up” attention. The other is the voluntary attention to external and internal stimuli controlled by the brain, called “top-down” attention. Attentional selection

promotes the expression of the most important information in the human brain by improving recognition speed and contrast, enabling organisms to capture prominent targets [22]. The ASC, the hardware implementation of the attentional selection mechanism, selects the most significant signal by activating the competition among inputs and actively shifts attention to the position with the salient feature.

The bottom-up attentional shift: Since the silicon-based MOS transistors operating in the subthreshold state have the work function to model biological processes [23], they can achieve a physical process similar to cell membrane current regulation of biological neurons with low power consumption [24]. Based on these fundamental properties, a three-branch attentional selection circuit for color recognition was proposed, which is shown in Fig. 2(a). The proposed circuit responds to the branch with the highest input voltage while suppressing the responses of the other branches, concentrating processing power on the most important information. As a result, its electrical behavior resembles the neurological behavior of specific neural networks in the visual cortex. Fig. 2(b) depicts the I_D - V_{GS} relationship of input transistors M_B , M_G and M_R as excitatory neurons. When the transistors operate in the subthreshold region, the I_D increase exponentially with increasing V_{GS} (gate-source voltage), thus making the circuit extremely sensitive to the variation of input voltage. The common node voltage V_C changes with the input voltage as shown in Fig. 2(c). Similar to the mechanism of inhibitory interneurons in the human brain, the transistor $M_{inhibition}$, as an intermediate inhibitory neuron, mediates global inhibition through the common node. When the input

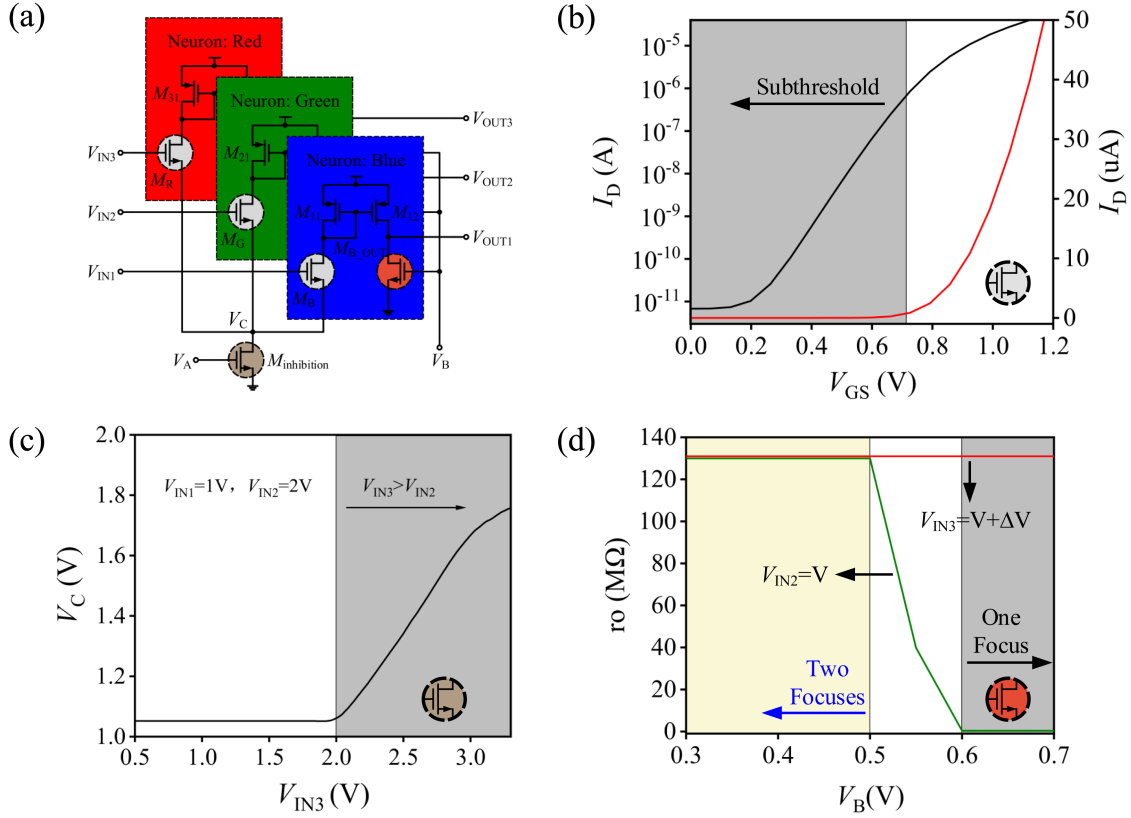


Fig. 2. (a) The attentional selection circuit; (b) The I_D - V_{GS} characteristic of “excitatory neuron”. As excitatory neurons, transistors M_B , M_G , and M_R can sense the difference in input voltage; (c) The inhibition curve of “inhibitory neuron”. The transistor $M_{inhibition}$ provides global inhibition for the whole circuit as an intermediate inhibition neuron. The electrical behavior of $M_{inhibition}$ is close to the neuroelectrical behavior of middle inhibitory neurons in the human brain [26]; (d) The variation of internal resistance of efferent neurons with top-down stimulus. As efferent neurons, M_{B_OUT} , M_{G_OUT} , and M_{R_OUT} transform the current into spike voltage signals, which indicate this branch becomes the attentional focus. The number of attentional focuses and the voltage resolution can be changed by applying a top-down stimulus, which adjusts the internal resistance of efferent transistors.

voltage of a branch is stronger than that of the other two, this branch gains most of the current from $M_{inhibition}$. The source voltage of the input transistor follows the change of the gate voltage, increasing the source voltage, which is V_C . Since all input transistors share a common source, the responses of other branches are suppressed until they are turned off. Finally, the currents flow from the current mirror formed by M_{i1} and M_{i2} ($i = 1, 2, 3$) to the efferent transistors M_{B_OUT} , M_{G_OUT} , and M_{R_OUT} as efferent neurons and then are converted into spike voltage signals. Based on the global inhibitory feedback mechanism [25], the proposed circuit quickly switch its attention to the branch having the strongest input and eventually tends to a stable state with only one attentional focus.

The top-down attentional distraction: To be more sensitive and accurate in perceiving changes in the external environment, the brain sometimes actively controls the visual system to distract attention toward the information outside the attentional focus. To achieve the attentional distraction, a top-down stimulus is applied to the efferent transistors. Supposing $V_{IN1} = V - \Delta V$, $V_{IN2} = V$, $V_{IN3} = V + \Delta V$, when no significant stimulus is applied, only the V_{IN3} branch gets the full attention of the circuit. The internal resistance can be iteratively adjusted by changing V_B . The variation of internal resistance of efferent transistors M_{G_OUT} and M_{R_OUT} with V_B is shown in Fig. 2(d). As

V_B decreases, the internal resistance, as a reward, gradually increases, and the attention of the V_{IN2} branch is therefore strengthened. When the reward is strong enough, the V_{IN2} branch transforms into an additional focus of attention. As a reward-driven attentional distraction, the reward can also adjust the degree of attentional distraction. When compared with the traditional winner-take-all circuit [27], except for the maximum signal selection, the ASC can change the number of attentional focuses and resolution by applying a top-down stimulus. When different stimuli are applied to the various branches, the circuit’s attention only concentrates on the information outside the attentional focus to meet higher-level recognition requirements.

The DC simulation results of the ASC are shown in Fig. 3(a). Where $V_{IN1} = 1$ V, $V_{IN2} = 2$ V, and V_{IN3} gradually increases from 0 V to 3.3 V. When V_{IN3} is far less than 2 V, the circuit only pays attention to the V_{IN2} branch and thus, $V_{OUT2} = 3.3$ V. Similarly, when V_{IN3} exceeds the other two values, the attention switches to its branch. As V_{IN3} approaches V_{IN2} , the ASC adjusts its attention in real-time according to the inputs. The number of attentional focuses and the resolution of the ASC versus V_B are shown in Fig. 3(b). The resolution of ASC with double attentional focuses is defined as the maximum difference between the input voltages of the corresponding branches of focus. When the input difference is within the resolution, the

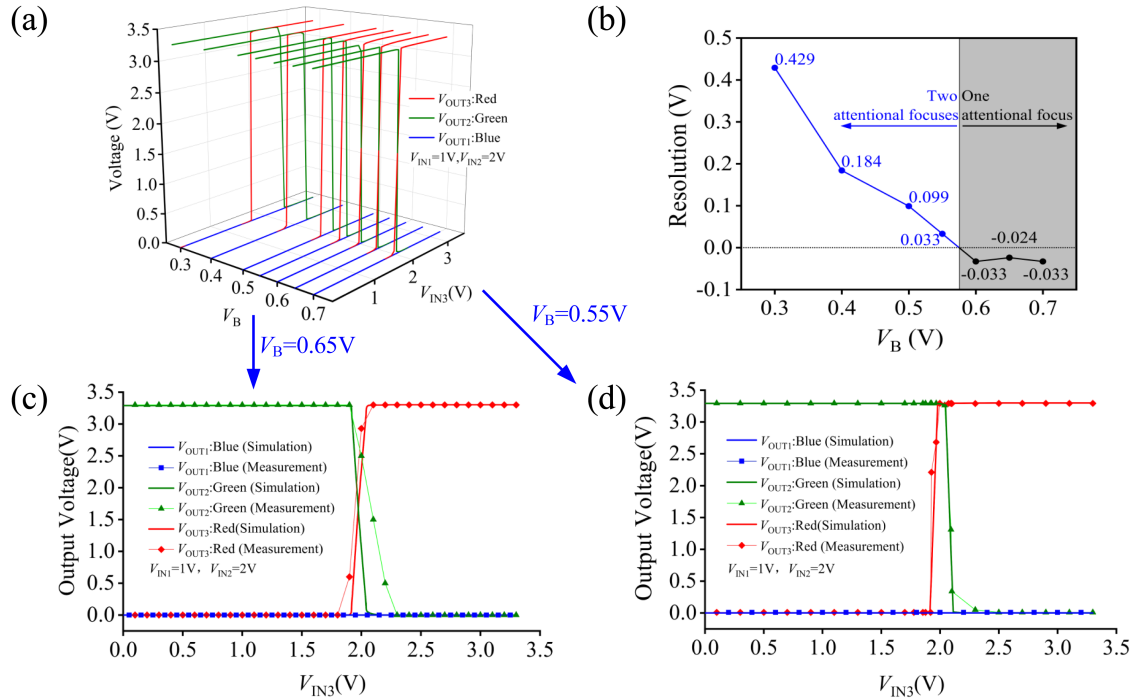


Fig. 3. (a) DC response of the ASC (simulation). The DC curves of the ASC with different V_B voltages; (b) The voltage resolution of the ASC. The evolution of the resolution is shown in Fig. 3(b). The number of attentional focuses and the level of attentional distraction can be adjusted by changing V_B ; (c) DC response of the ASC when $V_B = 0.65$ V; (d) DC response of the ASC when $V_B = 0.55$ V. Fig. 3(c) and (d) are the comparisons between experiment and simulation results. It can be seen that ASC can achieve attentional concentration and distraction, and excellent agreement between experiment and simulation results was realized.

circuit can distract its attention to the additional focus. The resolution of ASC with a single attentional focus is the minimum voltage difference that the circuit can recognize when the inhibition weight of $M_{inhibition}$ to each branch and the ASC's attention is constantly adjusted according to the input voltage variation. The circuit switches from the adjustment state to the steady state with only one focus once the voltage difference is outside the resolution. It can be seen that by changing V_B , the voltage resolution and the number of attentional focuses can be adjusted. When V_B is 0.65 V and 0.55 V, the circuit has the optimal voltage resolutions of 24 mV and 33 mV in the corresponding state, respectively.

As a whole, The ASC can simulate the dynamic process of attentional selection, and its simple structure paves the way for low power, efficient hardware designs.

III. EXPERIMENTS AND TEST RESULTS

A. Electrical System Measurements: the ASC Chip

The ASC was fabricated in the Taiwan Semiconductor Manufacturing Company (TSMC) 0.18 μm standard complementary metal-oxide-semiconductor (CMOS) technology, and the supply voltage is 3.3 V. To make the circuit more stable and easily accommodate to different conditions, V_A can be adjusted externally. During the test process, the value of V_A is about 0.5 V. The micrograph of the ASC is shown in Fig. 4(a). The ASC chip, including pads, occupies an area of $850 \mu\text{m} \times 800 \mu\text{m}$, whereas the core area of the ASC chip is only 0.0285 mm^2

($150 \mu\text{m} \times 190 \mu\text{m}$). All the pads of the chip are marked in the figure, and the ASC chip was mounted on a test printed circuit board (PCB) using wires bonding.

The test condition of the ASC is consistent with the simulation. The test results at V_B of 0.65 V are shown in Fig. 3(c). The resolution with single attentional focus is about 80 mV. The test results show that the ASC can focus on the branch with the maximum input voltage in real-time. Note that there is a slight delay in the response of V_{OUT3} compared to the simulation results when V_{IN3} slightly exceeds V_{IN2} , which may be caused by process variations and layout mismatches [28]. The results at $V_B = 0.55$ V are shown in Fig. 3(d). The circuit can select double attentional focuses simultaneously when the inputs are close, and excellent agreement between experiments and simulations is achieved.

B. Optical System Measurements: The Color Feature Selection Unit

Based on the fabricated ASC chip, the proposed color feature selection unit was assembled on a specially designed PCB, as shown in Fig. 4(b). The photoreceptor is located in the blue frame, and the photo-sensing devices (photoreceptors) are in the white frame. It can be seen from in Fig. 1 that the central wavelengths of the three photoreceptors are 450 nm, 550 nm, and 650 nm, respectively. The TIA is located in the yellow frame, and its circuit diagram and frequency response is shown in Fig. 4(c). Operational amplifiers (OPA847, Texas Instruments) are used to

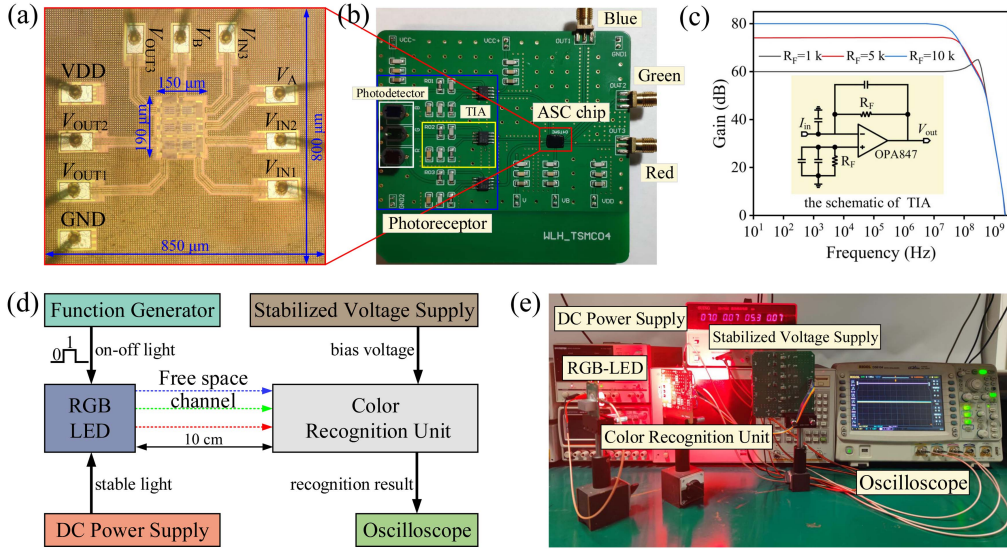


Fig. 4. (a) The micrograph of the ASC chip; (b) The test board of the color feature selection unit; (c) The circuit diagram and frequency response of the TIA; (d) The experimental setup to measure the optical characteristics of the color feature selection unit. The stabilized voltage supply ensures the proper functioning of the ASC; (e) The optical test link. The ratio of RGB colors is achieved by varying the electrical power injected into the LED.

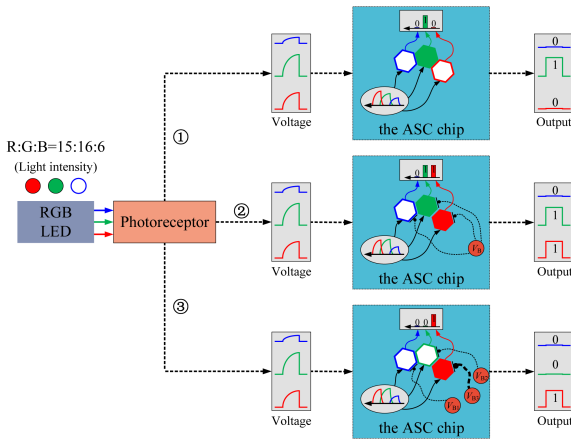


Fig. 5. The schematic diagram of the steady-state color feature selection. The upper bound on the number of attentional focuses is set by V_A by controlling the current flowing through the ASC, while V_B can determine whether the branch outside the attentional focus can become an additional focus by giving it enough “attention”. In this paper, the ASC possesses multiple attentional focuses simultaneously. The recognition of single-color light and dual-color light can be achieved by changing the number of attentional focuses. In particular, the special attentional target can be set by adjusting V_B . For example, if the red light is set as the attentional target, the unit’s attention can be guided by the target and shifts to the red light while ignoring the green light with the maximum intensity. When different V_B (V_{B1} , V_{B2} , V_{B3}) are applied to the branches, the gain of the red branch is greater than those of the blue and green branches, thus forcing the red branch to receive extra attention. In this case, even though the green light is stronger than the red light, the chip still focuses on the red light unless the green light is strong enough to offset the gain of V_{B3} to the red light.

form TIAs, and the gain of the TIA is adjusted by changing the feedback resistance R_F . To assure the color feature selection chip work properly and meet the input voltage requirements of ASC, whose input common mode range (ICMR) is about 3 V, the output of the TIAs ranges from 0V to 3V by varying the R_F . The experimental setup to measure the optical characteristics is

illustrated in Fig. 4(d). An RGB-LED is used as the experimental light source (peak wavelength 455 nm, 550 nm and 635 nm), and its color and light intensity can be adjusted by changing LED’s drive voltage. For example, a dual-color light can be obtained by applying DC voltage to the two control ports of the LED, and the periodical on-off light can be realized by using a square-wave signal as a drive voltage. The optical test link is shown in Fig. 4(e). The color feature selection unit is placed on the right side of the LED, and an optimal distance between LED and photoreceptors of about 10 cm is selected. The light intensity on the surface of photoreceptors is measured by the optical power meter (PM100D, Thorlabs), and the results of the color feature selection unit are displayed on the Digital oscilloscope (DS6104, Rigol).

Steady-state optical recognition measurements: Fig. 5 is the steady-state recognition diagram of the color feature selection unit. The intensities measured on the surface of photoreceptors are shown in Fig. 6(a), and the corresponding outputs are shown in Fig. 6(b). Firstly, the RGB light is set as the strongest incident light at test point 1-3, and V_B is fixed at 0.65 V. It can be seen that the unit can recognize the single-color light with maximum intensity and suppress the response of the branches that correspond to the stray light. Then, the dual-color light intensity ratio is set about 1:1 at test point 4-6 and V_B is adjusted to 0.55 V. Note that the unit senses the existence of dual-color light and distracts attention to their corresponding branches in real-time. The corresponding values of the output voltage (V_{OUT1} , V_{OUT2} , V_{OUT3}) can be transformed into the binary format as the color labels, and the obtained truth table is reported in Fig. 6(c). Since the main purpose of this design is to implement the selection of principal color-component features, so the special situation ($R = 1$, $G = 1$, and $B = 1$) is not considered. Moreover, the color feature selection unit can suppress the redundant signals and interference caused by stray light, thus reducing the negative

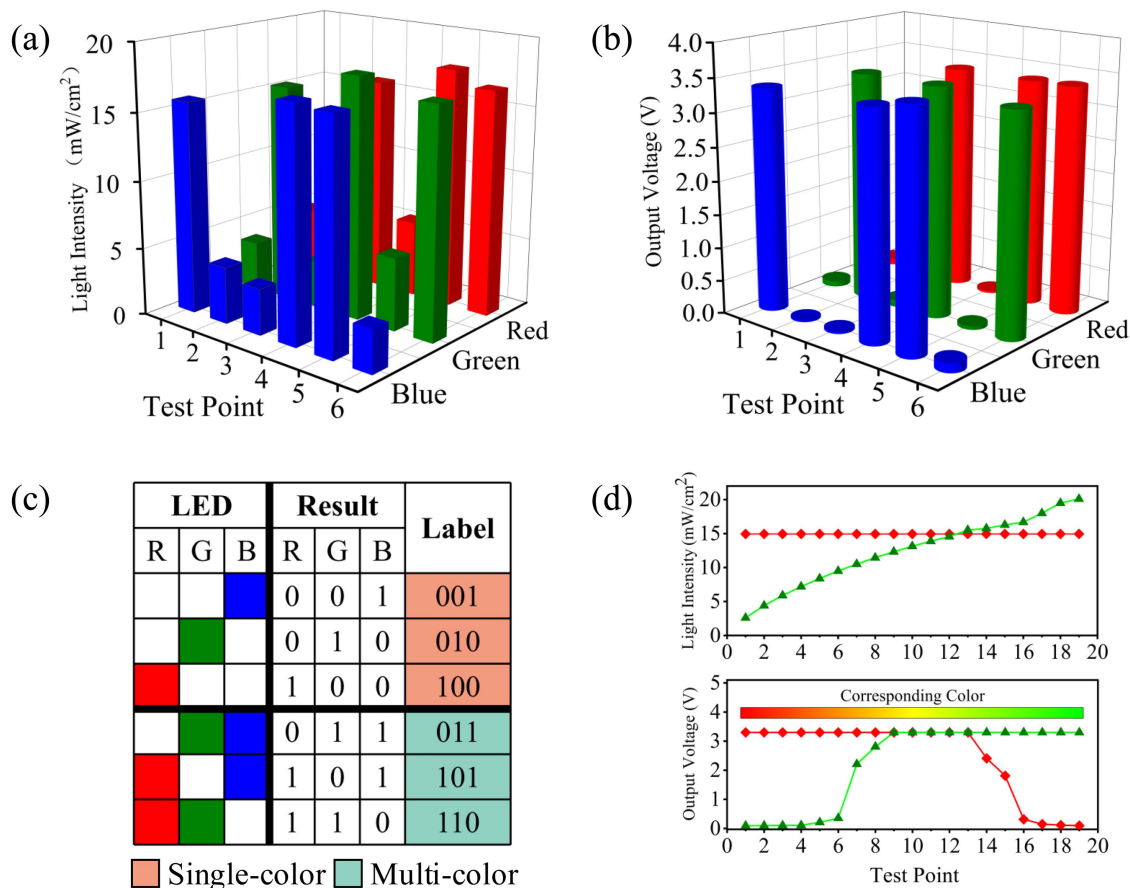


Fig. 6. (a) The light intensity measured on the photoreceptor surface; (b) The recognition results; (c) Measured truth table. The outputs of the unit are defined as digital labels for color recognition. The logic “1” is the attentional focus of the unit, indicating the branch has the maximum light intensity; (d) The light intensity of green and red lights and the corresponding recognition results.

impact on color feature selection caused by the spectral overlap of the photoreceptors [29] and the difficulties of subsequent signal processing.

To further analyze the shifting-attention of the color feature selection unit when the light intensity changes, the red light with fixed intensity and on-off green light are selected for further experiments. The blue light as the background light, whose intensity is relatively weak, would not influence the results. The light intensities and the corresponding recognition results are plotted in Fig. 6(d). By increasing the intensity of green light, the ratio of green-to-red light intensity gradually rises from 2:15 to 20:15. It can be seen that the color feature selection unit can adjust its attention to cope with the change of light intensity, and the recognition results consist with that of the ASC (Fig. 3(d)). At test point 16, the unit can recognize the percentage ratio between the intensity of stray light relative to the attentional focus is up to 93.58% (the output voltage of stray light is 10% lower than that of the strongest light), indicating the unit has excellent light intensity sensitivity and anti-interference ability. That is, the minimum recognition resolution is equivalent to 4-bit ADC. In particular, the sensitivity can be adjusted by V_B to satisfy different requirements.

Color pattern recognition measurements: An experiment was carried out to demonstrate color pattern recognition in image

processing. As shown in Fig. 7(a), the input map is a 12×12 pixel array. Each pixel represents the LED luminous color and is proportional to the light intensity. The driving voltage of the LED is configured according to the input map, and the color recognition results are recorded in the recognition map. The recognition map with single attentional focus is shown in Fig. 7(b). The unit can recognize the single-color light with maximum intensity in each pixel. The results are in good qualitative agreement with the color information of the input map, and readers will see “18” if they focus on green and red, respectively. The GB and RB recognition maps with double attentional focus are shown in Fig. 7(c) and 7(d). The recognition unit distracts the attention to the red and green light in the pixels with approximate equivalent intensity and evokes the response of both branches simultaneously. It is obvious which one has higher light intensity when compared to the recognition map of a single attentional focus. Combining the GB and RB recognition maps of the double attentional focus, the RGBY recognition map with double attentional focus is demonstrated in Fig. 7(e). The yellow pixel represents that the attention is drawn to the red and green light. The unit can recognize the input map more accurately and comprehensively, and a pattern of “is” can be obtained due to the additional focus. In practical applications, the recognition sensitivity can be adjusted to meet

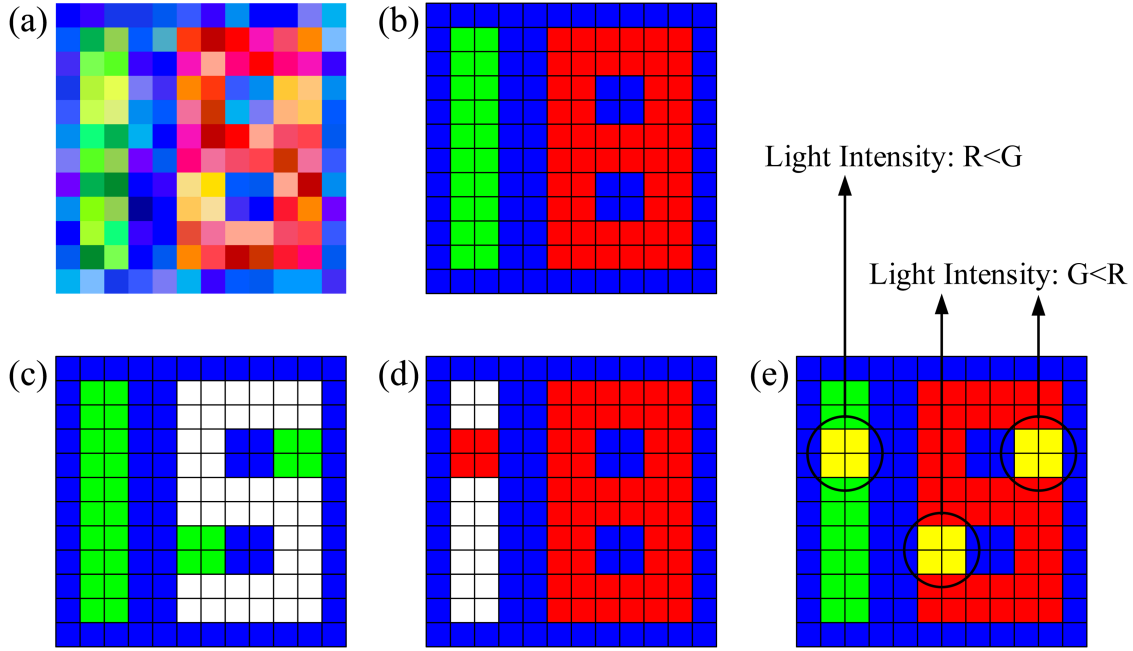


Fig. 7. Color perception experiments. (a) Input map: “is” or “18”. Readers will see “18” if they focus on green and red, respectively. Readers will notice “is” if they focus on both green and red; (b) The RGB recognition map with single attentional focus; (c) The GB recognition map with double attentional focuses; (d) The RB recognition map with double attentional focuses; (e) The RGBY recognition map with double attentional focus. The yellow pixel represents that there are two attentional focuses.

different functional requirements by changing V_B . Unlike the traditional pattern recognition systems [30], the proposed color feature selection unit does not require relevant processing such as grayscale and retains the image’s color information. More importantly, the unit can directly perform color feature selection at the data position without additional data processing and burdensome training, thereby overcoming the von Neumann bottleneck.

Dynamic optical recognition measurements: The dynamic recognition measurements are carried out to evaluate the dynamic optical characteristics and the sensitivity of the attention. The schematic diagram of dynamic recognition is shown in Fig. 8. The measurement setup is similar to that of the steady-state test. The only difference is that LED is driven by a periodic square wave voltage to generate on-off keying signal. When the driven voltage is high, the RGB light intensity ratio is 15:17:8. Conversely, the intensity ratio is 15:9:8 with low-level driven voltage. The green and red light alternately became the single-color light with maximum intensity, and the blue light as background light does not contribute to the recognition process. V_B is fixed at 0.65 V during this measurement. The driving voltages and the recognition results with the green light at different frequencies are illustrated in Fig. 9(a) and Fig. 9(b). It can be observed from the results that the unit can rapidly switch its attention to the light with the maximum light intensity in any period. The maximum achievable recognition frame rate is 1 kHz. It is confirmed that the color feature selection unit has a strong anti-interference ability in capturing the dynamic change of incident light, even though the stray light has a relatively strong intensity.

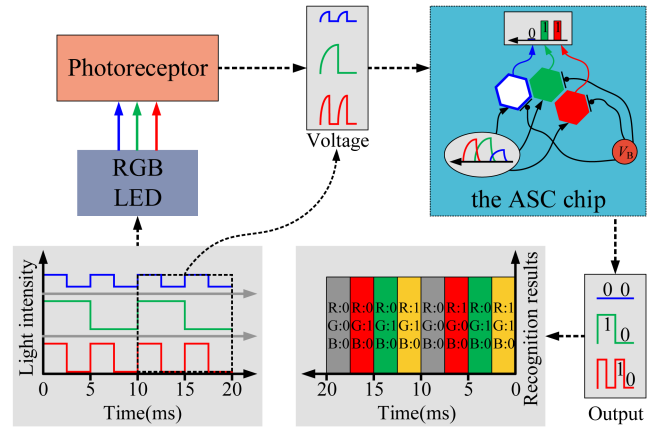


Fig. 8. The schematic diagram of the dynamic color feature selection. In dynamic recognition, the multi-color light in front of photoreceptors flashes on and off freely in different periods, and the final recognition results can show the periodic color change of the color recognition unit.

Furthermore, freely combined optical signals in different frequencies and light intensities (15:15:4) are fed into the recognition unit with a fixed V_B of 0.55 V. Fig. 9(c) demonstrates the driving voltage and recognition results. The unit can respond to periodical on-off light and quickly shift its attention to the branch whose photoreceptor exhibits the most intensive response. And when the red and green light are simultaneously illuminated onto the photoreceptors, the unit can distract the attention to the respective branches. When the results are thoroughly examined, it is not difficult to conclude that the unit’s attentional set-shifting and distraction can be implemented in real-time. The

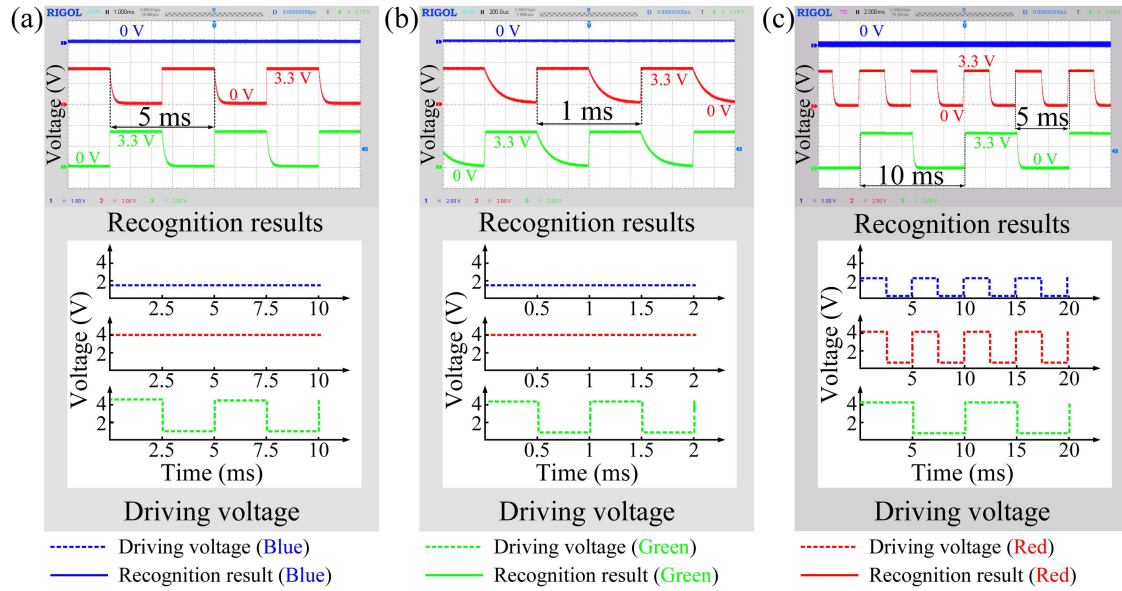


Fig. 9. Dynamic recognition measurements of the color feature selection unit. (a) The recognition results with G-LED driving frequency at 200 Hz; (b) The recognition results with G-LED driving frequency at 1 kHz. It can be seen that the unit can shift its attention according to the change of green light. When the green light is off (driving voltage is low), the red light comes into attentional focus (output voltage is 3.3 V) in real time, and when the green light is on (driving voltage is high), the attentional focus is the green light; (c) The recognition of dynamic trichromatic light. For further confirmation, the unit can recognize the on-off lights in different periods, and the results show the periodic changes of color connected with the incident light.

TABLE I
SUMMARY AND COMPARISON WITH SEVERAL SYSTEMS INSPIRED BY THE BIOLOGICAL VISION SYSTEMS

Inspiration	Fabrication process	Device structure or architecture	Recognition rate (Hz)	
Visual attention mechanism	CMOS VLSI	Neuromorphic hardware	3	[13]
Retinal	Vapour-phase approach	Diode and perovskite nanowire	52-42	[31]
Sensory adaptation	Transition-metal dichalcogenide	Phototransistor	1-3	[32]
Retinal	PAN-coated glasses	NIR organic photosensor	1	[33]
Visual system	Configuration of Pd/MoOx/ITO	Photoelectronic synaptic device	>10	[34]
Visual attention mechanism	Software and algorithms	Deep neural network	12.5	[35]
Visual attention mechanism	Standard CMOS technology	Color feature selection chip	1000	This work

unit can encode the color information into the peak amplitude and frequency of the output signals, enabling real-time optical communication. Moreover, the output signal period follows that of the LED driving voltage without signal cross-talk caused by the spectral overlap of photoreceptors, and the output signals are compatible with subsequent processing circuits and algorithms, thus reducing the design complexity of the whole system.

Note that, the color recognition unit has an inherent delay of 0.5 ms. It may be due to the slow carrier extraction speed of ASC, which results in the inability of the unit to complete the state transition and limits the recognition speeds. Several novel technologies have been proposed to develop the artificial neuromorphic visual system, as listed in Table I. The dynamic recognition speed is an order of magnitude faster than biological vision systems and two orders of magnitude higher than

current artificial vision systems. Moreover, the unit based on standard CMOS-compatible processes has significant advantages in terms of low power consumption, anti-interference, and low hardware complexity compared with several visual systems fabricated in advanced manufacturing processes.

IV. CONCLUSION

In this paper, a color feature selection unit consisting of photoreceptors and an ASC was designed. The experiments have shown that the ASC can simulate the brain's visual attentional selection that shifts attention to the most significant color feature in real-time or pay special attention to non-significant color feature by applying a top-down stimulus. The steady-state test results show that the unit can recognize single-color and dual-color light, and the percentage ratio between the intensity

of stray light relative to the strongest light is up to 93.58%. Experiments related to images processing proved that the unit can be used to recognize color patterns and satisfy different color resolution requirements by adjusting the number of attentional focuses and circuit resolution. The dynamic test results show that the unit has a high resolution to periodical on-off visible light, and the maximum achievable frame rate is 1 kHz, which is an order of magnitude faster than biological vision systems. In conclusion, the unit can filter the interference of photoreceptor's spectral overlap and overcomes the von Neumann bottleneck without memory requirements. In addition, it closes the branches outside of the attentional focus, greatly reducing the overall power consumption. Moreover, the recognition unit of RGB light presented in this paper can be easily expanded to more distinct colors and attentional focuses in accordance with the color resolution requirements, which increases performance over existing computer vision feature extraction approaches, providing new insights for the development of artificial neuromorphic visual systems to simulate the initiative, selectivity, and adaptability of biological vision systems.

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