

Low-Loss Silicon Photonic 16×16 Cyclic AWGR Based on SOI Platform

Rui Huang , Haiyang Huang , Yingxuan Zhao , Yang Li , XiaoJuan She, Han Liao, Junbo Zhu, Zijian Zhu, Xiang Liu, Hongbao Liu, Zhen Sheng, and Fuwan Gan 

Abstract—We experimentally demonstrate a low-loss and low-crosstalk 16-channel 200 GHz-channel-spacing arrayed waveguide grating router (AWGR) with a size of $0.67 \times 0.37 \text{ mm}^2$, targeting for C-band wavelength division multiplexing (WDM) interconnect routing applications. As a result, the AWGR features a channel spacing of $200 \pm 10 \text{ GHz}$, a free spectral range of 25.62 nm and a 3 dB channel bandwidth of 0.9 nm. Based on comprehensive optimal design, the measured characterization revealed 5.55 dB maximum channel loss non-uniformity with 1.13 dB best-case channel insertion loss. Besides, the fabricated device exhibits good cyclic-frequency operation for all 16×16 port combinations with channel crosstalk of -15.11 dB .

Index Terms—Arrayed waveguide grating router (AWGR), dense-wavelength division multiplexing (DWDM), optical interconnects.

I. INTRODUCTION

WITH the development of cloud computing and the increasing number of servers, data center (DC) is required to be cost-effective and energy efficient. The optical interconnection density of data center will be expanded by using wavelength division multiplexing (WDM) technology [1]. Arrayed waveguide grating routers (AWGRs) are the key components in dense wavelength division multiplexing (DWDM) with advantages of

broadband and low power consumption. It can provide all-to-all communication among N nodes with N wavelengths simultaneously [2]. In addition to the traditional inter such as rack-scale [3] and board-level interconnects [4], a deeper interconnection can be achieved through on-chip integrated optical technology. Recently, researchers have proposed optical multi-socket board (MSB) architecture [5], following Network-on-Chip (Noc) interconnect [6] based on AWGRs.

AWGRs have been realized in various material platforms. Silica (SiO_2) [7] AWGRs have low insertion loss due to the low refractive index contrast. Polymer (Pol) [8] materials have an advantage of low propagation loss in waveguides. AWGR have been developed in Si-nitride (SiN) [9] and Si-oxynitride (SiON) [10] platform which have moderate refractive index contrast. However, these devices cannot be easily integrated with other photonics devices due to their large footprint. For the high refractive index contrast between silicon and silica, the AWGRs based silicon-on-insulator (SOI) waveguide have smaller waveguide bending radius which can help to further reduce the size of the device. Moreover, the fabrication process is compatible with commercial CMOS technology. Recently, Si-based AWGRs with different channel spacing and number of channels have showed good performance [11]–[17]. However, the SOI waveguides used in the array are sensitive to phase errors, resulting in higher crosstalk and larger insertion loss than low-index contrast device. The phase error of the waveguide is related to the waveguide width variation and its correlation length, which are related to the fabrication process. Besides, it is also related to the derivation of the effective respect to the waveguide width, which can be optimized by widening the width of arrayed waveguides [23]. Therefore, by improving the fabrication process and optimizing the width of arrayed waveguide, the phase error and associated crosstalk can be reduced. Therefore, AWGRs based on SOI platform with low loss, low crosstalk and compact footprints have attracted more attention. Wang *et al.* experimentally demonstrated an 8×8 AWGR with a channel spacing of 400 GHz that showed a crosstalk of -17 dB and insertion loss of 2.92 dB, by using rib waveguides instead of strip waveguides [18].

In this work, we propose and demonstrate a low-loss and low-crosstalk 16×16 Si-photonic cyclic- AWGR with core size of $0.67 \times 0.37 \text{ mm}^2$. The AWGR center wavelengths are aligned to 1550 nm, with $200 \pm 10 \text{ GHz}$ channel spacing (CS), 0.9 nm 3-dB bandwidth and a free spectral range (FSR) of 25.62 nm. The device features proper cyclic frequency properties

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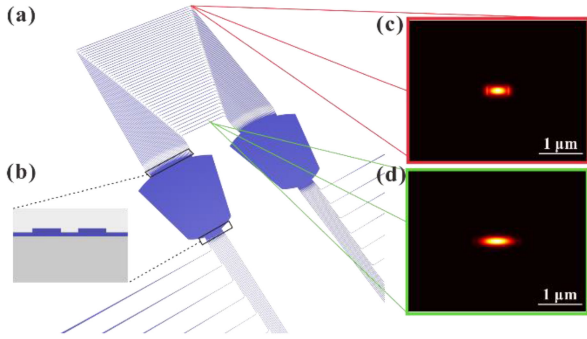


Fig. 1. (a) Schematic diagram of 16×16 200 GHz silicon-based AWGR, (b) 70 nm etch at the boundary of FPR, (c) calculated fundamental TE mode for 450-nm wide bending waveguide; (d) calculated fundamental TE mode for 1- μm wide arrayed waveguide.

for all 16×16 port combinations with channel insertion losses ranging from 1.13 dB to 6.68 dB and a maximum insertion loss non-uniformity of 5.55 dB. The low loss is achieved by optimizing the width of arrayed waveguide and shallowly etching the junction area between free propagation region (FPR), arrayed waveguides and input/output waveguides. We adopted a wider arrayed waveguide, and obtained a device with lower loss ensuring lower crosstalk level. However, shallowly etching also strengthens the coupling between the output waveguides, resulting in crosstalk. The channel crosstalk was measured to be -15.11 dB, which is still lower than the majority of the reported devices.

II. OPERATION PRINCIPLE AND DESIGN

As shown in Fig. 1(a), the AWGR includes two parts: free propagation region (FPR) and an array of waveguides (WGs) with linearly increasing length. The AWGR operation can be described as follows: An optical signal is input to FPR and diffracts. Then the optical components are distributed and transmitted through arrayed waveguide. Due to the length increment of adjacent arrayed waveguides, the optical components gain different delays. Finally, the optical components interfere at the output FPR and are directed to the output waveguides. Optical diffraction and interference that occur in the FPR are the essential principle of an AWG. The grating diffraction equation [19] is:

$$n_s(\lambda) d_a \sin \theta_i + n_a(\lambda) \Delta L_a + n_s(\lambda) d_a \sin \theta_o = m\lambda$$

where $n_s(\lambda)$ and $n_a(\lambda)$ are the effective indices of the FPR slabs and arrayed waveguides, respectively. d_a is the pitch of the end of adjacent arrayed waveguides, ΔL_a is the length increment of adjacent arrayed waveguides, θ_i is the propagation angle from one of the input waveguides, θ_o is the propagation angle toward one of the input waveguides, and m is the diffraction order. To realize cyclic wavelengths routing, adjacent diffraction orders should be strictly matched. Therefore, the FSR of the AWG is equal to the product of channel spacing and the channel number,

$$FSR = \frac{\lambda_c^2}{N_a \Delta L} = N \Delta \lambda$$

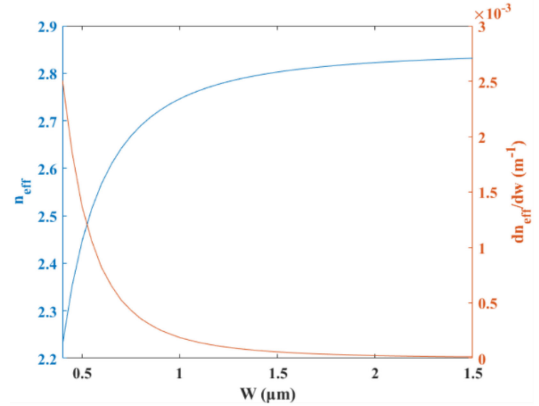


Fig. 2. Calculated effective index n_{eff} as a function of waveguide width w and its derivative dn_{eff}/dw .

where λ_c is the center wavelength and N_a is the group refractive index of arrayed waveguides. For $N \times N$ AWGR, when the input channel changes, the wavelength response at the output channel makes a cyclic rotation. The performance of the AWGR is usually characterized by the insertion loss and crosstalk in each channel. For AWGRs, low loss is an essential property usually including the material loss, bending loss, waveguide scattering loss and transition loss between the FPR and the arrayed waveguides. In this work, we choose silicon as the material of the AWGR, and the bending radius is set to $5 \mu\text{m}$ to obtain a small footprint. Thus, in order to achieve a low-loss AWGR, the waveguide scattering loss and transition loss must be fully optimized.

The scattering loss is dominated by the sidewall roughness of waveguides. Therefore, it can be reduced by improving fabrication technology. On the other hand, the scattering loss also depends on the mode interacts with the sidewalls, which can be optimized by designing the waveguide structure, such as using a nano-scale core layer. But the thickness of the silicon core layer is fixed for mature CMOS process. With the increase of waveguide width, the effective index increases while the increasing rate decrease, as shown in Fig. 2. It can be seen that the increasing rate has been reduced to a lower level when the waveguide width is $1 \mu\text{m}$. If the waveguide width continues to increase, more high-order modes will be excited, which will degrade the loss performance. Based on the above considerations, $1 \mu\text{m}$ is a good trade-off to obtain smaller insertion loss. Calculated fundamental TE modes profile for 450 nm and $1 \mu\text{m}$ wide waveguide are shown in Fig. 1. It indicates that the mode will be less affected by the sidewalls when the waveguide becomes wider.

The transition loss is also another important part of AWGR on-chip loss, which is mainly due to gaps between the FPR and the arrayed waveguides. It is inversely proportional to gap size but is limited by the fabrication process. In order to reduce transition loss, a double-etched taper is used as mode converter between FPR and arrayed waveguides. A similar approach is using vertical tapers at the junctions [21], but it suffers from fabrication difficulties.

TABLE I
DESIGN PARAMETERS OF THE 16×16 AWGR

Parameter	Design
Number of channels	16
Center wavelength	1550
Channel spacing (nm)	1.6
Diffraction order	44
Length increment (μm)	24.683
Length of FPR (μm)	154
Pitch of adjacent arrayed WGs (μm)	2.2
Pitch of adjacent input/output WGs (μm)	2.4
Number of arrayed WGs	47

The device was fabricated on an SOI wafer with a 220-nm-thick top silicon layer and a 2- μm -thick buried oxide layer. As mentioned earlier, the width of arrayed waveguide is 1 μm to reduce the interaction between mode profiles and the side walls. This method is also effective to reduce the crosstalk because it reduces the sensitivity of the effective refractive index to the sidewall roughness. The waveguides at bends have a width of 450 nm and a bending radius of 5 μm to ensure single-mode transmission and a low propagation loss. Also, we use a 70 nm-deep etched taper which is linearly tapered from 450 nm to 2.0 μm to reduce mode mismatch loss between the waveguide mode and slab mode in FPR region. The depth of 70 is determined by the general process standard. And the waveguide with a width of 450 nm is used to ensure single-mode transmission and a low propagation loss. Reducing waveguide gap also can reduce the transition loss [25]. Therefore, the gap between the tapers is 0.2 μm which is limited by the fabrication process. The detailed design parameters of AWGR are listed in Table I.

In order to evaluate the performance of the device, such as peak IL, center wavelength, loss non-uniformity (defined as the difference between minimum and maximum IL value), the transmission spectrum of input #8 in Fig. 3 is obtained by MATLAB code which is based on a 2D-Kirchhoff diffraction formula. It can be seen that the spectral response is repeated over a wavelength span which is called free spectral range (FSR). The results indicated a CS of 200 GHz and a FSR of 25.6 nm which are consistent with the design. The simulation non-uniformity and crosstalk are 1.83 dB and -30 dB, respectively. The simulated transmission spectral of the device for width variation of 0, ± 5 and ± 10 nm is shown in Fig. 3. The width variation within ± 10 is reasonable for CMOS fabrication process, so the variation of wavelength position can be controlled within 1.6 nm. To better control the wavelength shift, we can adjust the wavelength position through thermal tuning.

III. FABRICATION AND CHARACTERIZATION

The proposed device was implemented on Semiconductor Manufacturing International Corporation (SMIC). Layout and scanning electron microscope (SEM) images of the fabricated Si AWGR device are shown in Fig. 4. The measured footprint of our device is $1.27 \times 0.37 \text{ mm}^2$ where the core size is $0.67 \times 0.37 \text{ mm}^2$. Besides, the overall size has potential to be optimized

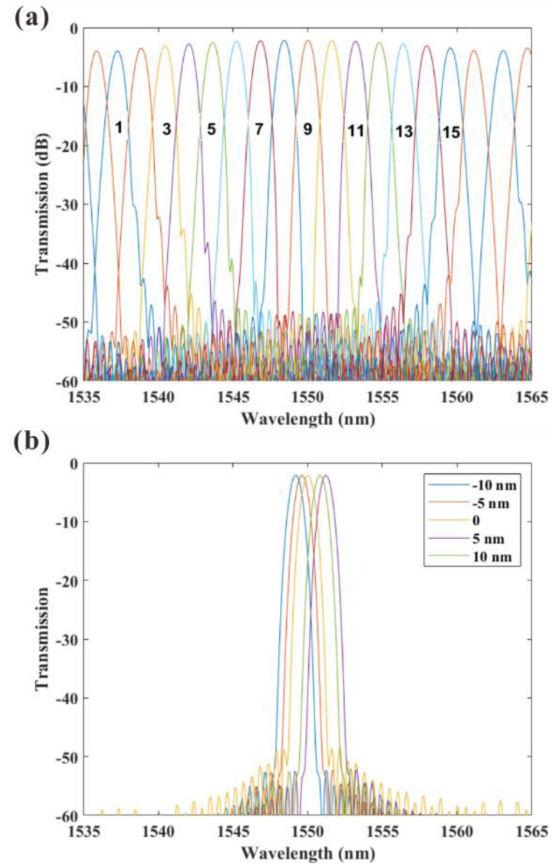


Fig. 3. (a) Simulated transmission spectral of input 8 to all output ports of the 16×16 AWGR. The channel spacing and free spectral range are 200 GHz and 25.6 nm respectively. (b) Simulated transmission spectral of input 8 to output #9 with different width variation.

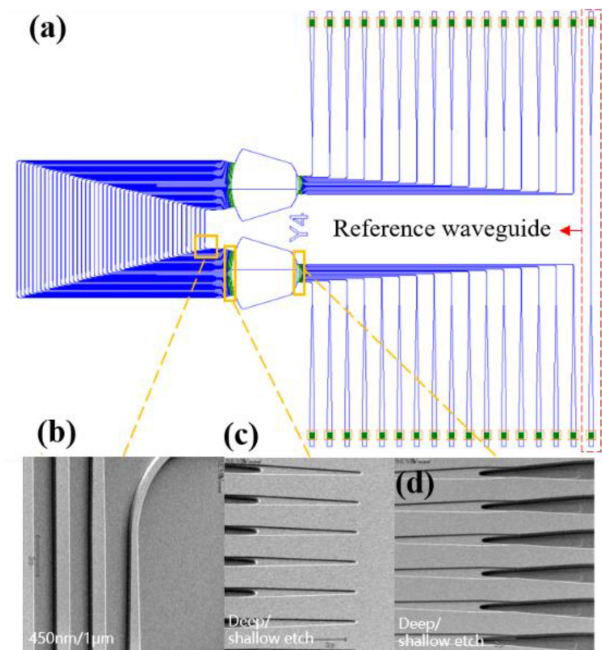


Fig. 4. (a) Layout image of AWGR with a core size of $0.67 \times 0.37 \text{ mm}^2$. SEM images of (a) 1- μm wide arrayed waveguides and 450-nm wide bending waveguides and (c-d) 70 nm etched at the boundary of FPR.

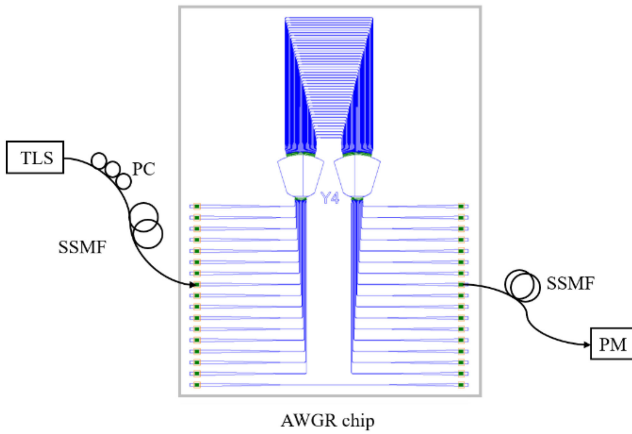


Fig. 5. The experimental setup for evaluating the transmission characteristics of the Si AWG.

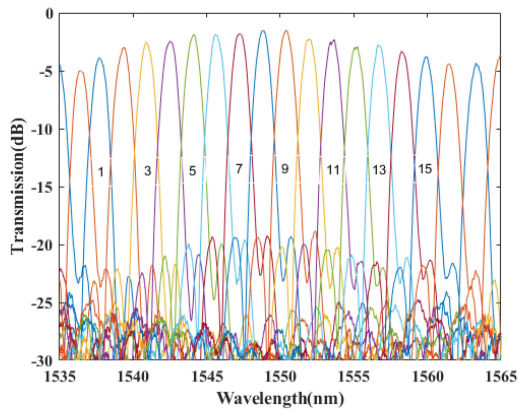


Fig. 6. Measured transmission spectral of input 8 to all output ports of the 16×16 AWGR.

by arranging the grating couplers in the width direction [25]. It can be seen that a reference waveguide is fabricated under the device to normalize the spectral responses. The grating coupler, targeting for TE-polarization operation, is used to couple light into the AWGR at C-band. Fig. 4 show a coupled structure of a 70 nm etched taper near FPR and 220 nm etched waveguide in the array. Pitch of adjoint arrayed waveguide and adjoint output waveguide are $2.2 \mu\text{m}$ and $2.4 \mu\text{m}$ with $2.0 \mu\text{m}$ -wide tapers. The width of bending waveguide is gradually tapered from 450 nm to 1 μm as shown in Fig. 4.

To test the performance of the device, a standard single-mode fiber (SSMF) is used to couple the input light from a tunable laser source (TSL) to the AWGR through grating couplers. A polarization controller (PC) is used to control the polarization to the TE mode. Finally, a power meter (PM) is used to receive the light passing through the AWGR. Fig. 5 show the experimental setup. The loss of reference waveguide is 9.75 dB including the coupling loss and the straight waveguide loss is 0.027 dB.

Fig. 6 shows the normalized transmission spectrum of input #8 to all output ports of the 16×16 AWGR in the wavelength range from 1535 nm to 1565 nm. The FSR and CS are 25.62 nm and 200 ± 10 GHz which are very close to the simulated values.

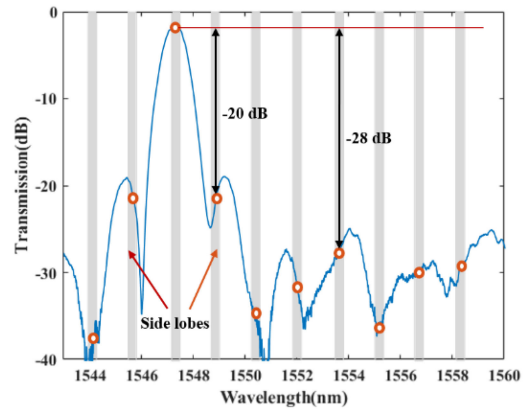


Fig. 7. The measured spectral response for output channel #8.

From the normalized responses shown in Fig. 6, it can be seen that the lowest IL of the fabrication AWGR is 1.45 dB, which is mainly due to double-etched tapers and small gaps between arrayed waveguides. The loss non-uniformity is about 2.37 dB due to the Gaussian-shaped far-field distribution of the arrayed waveguides. These data indicate the simulation results match the experiment results.

Fig. 7 shows the measured transmission response of #8 output channel. It can be seen that the cross talk from the adjacent channels is -20 dB while the non-adjacent crosstalk is as low as -28 dB. The experimental results show that there are two side lobes on both sides of the main peak. This is due to the coupling between the output waveguides [20], which was not taken into account in the simulation. In our design, we use 70 nm etched taper to reduce the transition loss between FPR and input/output waveguides as shown in Fig. 4(d). Therefore, the crosstalk can be improved by deep etched output waveguide or widening the gap of adjacent output waveguides.

After normalized to a reference straight waveguide, the measured spectral response of 16×200 GHz AWGR is shown in Fig. 8 and revealed cyclic frequency operation. For off-center channels, the crosstalk and the insertion loss increased. The AWGR operating channel mean wavelength value is presented in Fig. 9. A max deviation of 0.44 nm was observed in the measured operating channels compared to the respective mean wavelengths. The discrepancies are the results of phase errors accumulated due to the roughness of arrayed waveguides. The phase error caused by fabrication process variation is mainly cause of wavelength shift. TiN heaters can be used to optimize the wavelength shift [28], [29].

The measured channel peak loss and non-uniformity values of the AWGR are shown in Fig. 10. The horizontal axis represents the input channel, and the vertical axis represents the measured insertion loss of the output channels. The length of the error bars represents the non-uniformity [17]. The channel peak losses are ranged from 1.13 dB to 6.68 dB and the loss non-uniformity is 5.55 dB for all channels. But the peak loss is increased to 3.95 dB because the light is tilted into the FPR region and cannot be fully coupled into the arrayed waveguides. As mentioned above, the

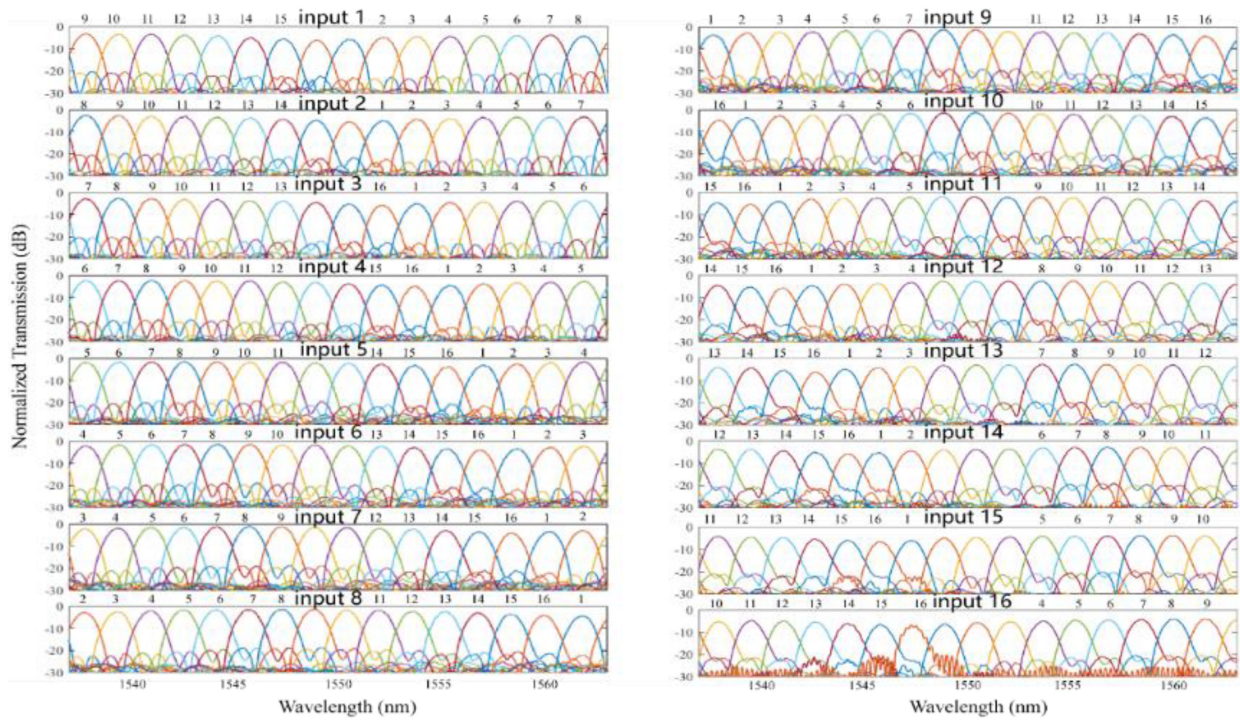


Fig. 8. Measured spectral response of the 16×16 AWGR.

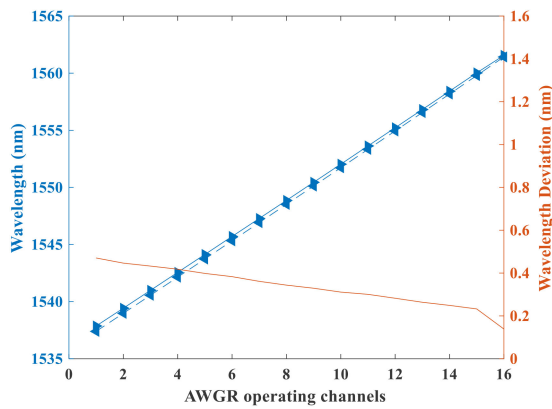


Fig. 9. Measured wavelength and wavelength deviation between design value. The max wavelength deviation is 0.44 nm.

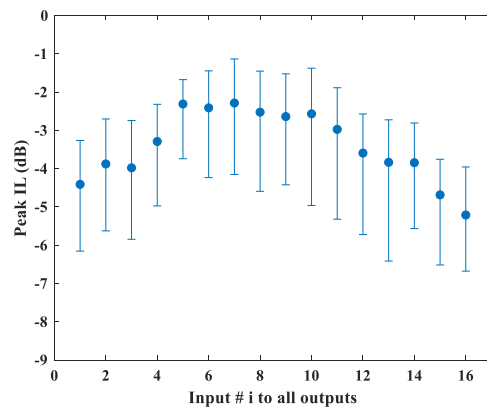


Fig. 10. Measured channel peak insertion loss and uniformity for all Input #i to all output port combinations. The dot means the average loss for all outports. The minimum loss is 1.13 dB and the loss uniformity is 5.55 dB. The length of error bars represents the non-uniformity of insertion loss.

loss is improved by the double-etched taper at the boundary of FPR.

As shown in Fig. 11, it is a heat map with crosstalk values for all Input #i to all output port combinations. The measured crosstalk ranges from -24.45 dB to -15.11 dB. The crosstalk level can reach -24.45 dB for Input #8 to Output #3. The worst-case single channel crosstalk of -15.11 dB is because of the shallow etched tapers between the FPR and the output waveguides. Therefore, crosstalk characteristics can be improved by optimizing the output waveguide structures, such as widen the gap between adjacent tapers or use 220-nm etched tapers. In principle, the output channel with larger channel number and that

with smaller channel numbers are symmetrical, so the spectrum is symmetrical as shown in Fig. 2. We measured different device over different fabrication runs, the variation of the crosstalk level for different output channels is irregular. Therefore, this phenomenon is mainly caused by fabrication variations. The performances of AWGRs on SOI platforms are listed in Table II for comparison. All measures insertion loss and crosstalk in the table are for the center channel. From this comparison, our device has a good performance in terms of both the insertion loss and crosstalk.

TABLE II
COMPARISON BETWEEN STATE-OF-THE-ART AWGRS

Ref	Channel spacing (GHz)	Channel number	FSR (nm)	Insertion loss (dB)	Crosstalk (dB)	Footprint (μm^2)
[25]	250	4	6	3.5	-12	425×125
[26]	200	16	25.3	2.2	-17	500×200
[12]	400	16	51.2	3	-19	475×330
[11]	200	16	29	2	-22.5	920×446
[11]	400	16	54	1.5	-26	530×435
[22]	200	16	24.5	3.5	-16	1200×1000
[24]	200	16	25.8	3	-16	-
[24]	400	8	25.8	3	-18	-
[27]	200	16	25.6	2.2	-8	580×170
This work	200	16	25.62	1.45	-15.11	670×370

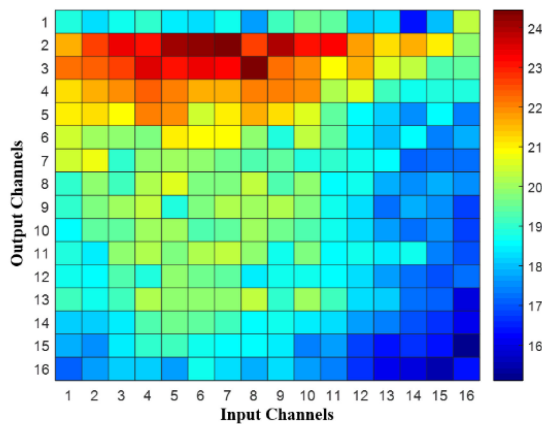


Fig. 11. Heat map with measured crosstalk values for all Input/Output combinations. The crosstalk is about -15.11 dB.

IV. CONCLUSION

In this paper, we designed and experimentally demonstrated a low-loss 16×16 AWGR targeting for C-band DWDM applications on 220 nm-thick SOI platform. The device has a compact core size of $0.67 \times 0.37 \text{ mm}^2$. The AWGR shows proper cyclic-frequency operation for all channels, with minimum insertion loss of 1.13 dB. We also obtained a channel spacing of 200 ± 10 GHz and a free spectral range of 25.62 nm. Besides, the crosstalk level is -15.11 dB. The proposed AWGR with good cyclic property and low insertion loss has a great potential in various optical interconnect and optical communications systems.

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