in the same area), flicker noise, and even headroom.

The transistor f_t drops when it is biased in weak inversion. But SoC radios in the 2–5-GHz range are usually designed on 280-nm-and-below process nodes. The MOS transistors with such a short channel have a very high f_t even in weak inversion, and a few gigahertz frequencies are nearly dc for these. It seems that the only domains left for strong inversion are the 60-GHz-and-above applications.

Panel Summary

Here, the panelists provided guidance on testing, modeling, and circuit design

for both beginning and experienced engineers. Specifically, the panelists suggested that the ignored aspects are the ones that are most likely to come back and bite you. This is particularly true for interactions between digital and analog circuits and among the start-up circuits, floating nodes, and test-circuit interfaces.

References

- H. T. Bui, "Design of an all-digital variable length ring oscillator (VLRO) for clock synthesis," in *Proc. Int. Symp. Circuits Syst.*, Seattle, WA, May 2008.
- [2] S. Kundu et al., "25.5: A self-calibrated 1.2-to-3.8GHz 0.0052mm2 synthesized fractional-N MDLL using a 2b time-period comparator in 22nm FinFET CMOS," in *Proc. IEEE Int. Solid-State Circuits Conf. (ISSCC) Dig. Tech. Papers*, Feb. 2020, pp. 388–389.

- [3] G. Singh, R. Wu, Y. Chae, and K. A. A. Makinwa, "A 20bit continuous-time ΣΔ modulator with a Gm-C integrator, 120dB CMRR and 15 ppm INL," in *Proc. 2012 ESSCIRC (ESSCIRC)*, Bordeaux, France, pp. 385–388. doi: 10.1109/ESSCIRC .2012.6341366.
- [4] R. B. Staszewski, "Digital deep-submicron CMOS frequency synthesis for RF wireless applications," Dept. of Electrical Engineering and Computer Science, Univ. of Texas at Dallas, Richardson, TX, Aug. 2002. [Online]. Available: https://researchrepository.ucd.ie/ handle/10197/8119
- [5] B. Staszewski, C.-M. Hung, K. Maggio, J. Wallberg, D. Leipold, and P. Balsara, "All-digital phase-domain TX frequency synthesizer for Bluetooth radios in 0.13µm CMOS," in *Proc. IEEE Solid-State Circuits Conf. (ISSCC)*, San Francisco, Feb. 2004, pp. 272–527. doi: 10.1109/ ISSCC.2004.1332699.
- [6] R. B. Staszewski et al., "DSP-coupled 2.4 GHz RF transmitter in 130 nm CMOS," in Proc. IEEE Dallas CAS Workshop (DCAS): Implementation High Performance Circuits, Dallas, TX, Sept. 2004, pp. 163–166. doi: 10.1109/DCAS.2004.1360451.

ISSCC Special Event: ICs in PandemICs

The COVID-19 pandemic has imposed a powerful test globally. As the current situation unfolds, revolutionary social and economic changes have accelerated what would have otherwise taken decades to materialize, especially the digital transformation of the whole world, enabling a virtual presence. The IC industry continues to forge ahead, providing the building blocks for innovations that improve the economic and social prosperity of the world. From smarter robots and automation to connected medical devices and artificial intelligence (AI)driven data analytics, cost-effective, secure, portable, and high-accuracy IC technology is already in place.

The special session "ICs in PandemICs" at the International Solid-State Circuits Conference was cosponsored by the IEEE Solid-State Circuits Society (SSCS) and the SSCS Women in Circuits Committee and brought together experts from industry and academia in cloud-connected biosensors,

Digital Object Identifier 10.1109/MSSC.2021.3072271 Date of current version: 24 June 2021



The invited speakers of the IC for PandemICs special event at ISSCC 2021.

advanced algorithms, and AI to discuss our preparedness to combat the spread of infectious diseases now and in the future.

The event, moderated by Ulkuhan Guler from Worcester Polytechnic Institute, USA, was kicked off by two distinguished speakers. Hillery Hunter, chief technology officer of IBM Cloud, discussed how custom ICs are being used today to help protect data in the cloud and how processors designed with confidential computing in mind can help researchers and enterprises compute in the cloud with confidence. Prerna Dogra, senior product manager for health care at NVIDIA, USA, took the audience in a time machine and covered how NVIDIA is accelerating the pace of innovation for healthcare research and drug discovery with its partner ecosystem to solve one of the greatest challenges of our times. These highly interesting and technical presentations were followed by two invited talks by aspiring young professionals: Mahsa Shoaran from EPFL, Switzerland,

and Milin Zhang from Tsinghua University, China. Shoaran discussed in depth the use of machine learning and algorithmic tools toward more effective neural prosthesis designs. Zhang walked the audience through the possible beneficial longer-term impacts of a COVID-19 breakout on biomedical IC designs. The talks were followed by a live Q&A, during which the attendees asked notably probing questions.

-Zeynep Toprak Deniz

Live Panel at ISSCC 2021: Making a Career Choice

The "Making a Career Choice" live panel and Q&A session were organized by Denis Daly, S. Zeynep Lulec, and Rabia Tugce Yazicigil as a part of the 2021 International Solid-State Circuits Conference. The special event was held virtually.

The event included six distinguished panelists who represented a broad variety of career choices in the areas of start-ups, industry, research, and academia available to graduates in electrical and computer engineering. Following short introductory remarks

Digital Object Identifier 10.1109/MSSC.2021.3072307 Date of current version: 24 June 2021



Special event 5: "Making a Career Choice" was presented at ISSCC 2021.



A slide presented by Seng-Pang (Ben) U during the live panel.