

A slide from Prof. Murmann's presentation, illustrating his sandwich analogy indicating the advantageous position of IC designers for collaborations.

on up, encompassing the hardware, software, and algorithms, to build practical and efficient

machine learning applications. Beyond the concept of codesign, stretching beyond one's specialty

to other domains is required to solve challenging machine learning problems. The tinyML Foundation believes collaborative partnerships like this one with the IEEE SSCS are essential to support the cross-functional community involved in ultralow-power machine learning at the edge. We look forward to another event as soon as possible." —*Ira Feldman, tinyML, United States*

—*Sevil Zeynep Lulec, Alvin Loke, Xinfei Guo, Ka-Meng Lei, Po-Hsuan Wei, Shahriar Mirabbasi, Abira Altvater, and Kelsey Rodriguez*

IEEE SSCS Oregon Chapter Remembers Dr. Barrie Gilbert, Recognizes Award Winners

The start of 2020 brought some exciting news for IEEE Solid-State Circuits Society (SSCS) Oregon Chapter members and also some very sad news. Dr. Barrie Gilbert, IEEE Life Fellow, who helped relaunch the SSCS Oregon Chapter, sadly left us on 30 January 2020 after a fall at his home in Portland, Oregon. He was a naturally gifted circuit designer who started experimenting with circuits at age nine. Gilbert became one of the most famous analog circuit designers in the world.

In 1979, Gilbert founded the first remote design center for Analog Devices, which was located in Portland. The Oregon SSCS Chapter was relaunched in 2018, with its first technical seminar, "Electron Tornadoes, Paper Circuits, and Semiconductor Spinning Tops," presented by Gilbert. The auditorium was filled with young and old SSCS members, all of whom were entertained by Gilbert's talk and the opportunity to



Dr. Farhana Sheikh, IEEE SSCS Oregon Chapter chair.

see the cool devices he brought with him from his home museum. The Oregon Chapter will miss Gilbert and his dedicated support.

February brought exciting news to select members of the IEEE SSCS Oregon Chapter. Dr. Christopher Hull, Dr. Stefano Pellerano, and their team from Intel were awarded the prestigious International Solid-State Circuits Conference (ISSCC) 2019 Lewis Award for Outstanding Paper and the 2019 Demonstration Certificate of Recognition for their work "A Scalable 71-to-76-GHz, 64-Element

Phased-Array Transceiver Module With 2×2 Direct-Conversion IC in 22-nm FinFET CMOS Technology." In addition to Hull and Pellerano, SSCS members who were acknowledged with awards are as follows:

- Dr. Steven Callender
- Dr. Woorim Shin
- Dr. Yanjie Wang
- Dr. Somnath Kundu
- Dr. Abhishek Agrawal
- Peter Sagazio
- Brent Carlton
- Dr. Farhana Sheikh
- Dr. Arnaud Amadjikpe
- Dr. William Lambert
- Divya S. Vemparala
- Mark Chakravorti
- Satoshi Suzuki
- Robert Flory.

Due to COVID-19 restrictions, the Chapter had to improvise on monthly meetings. In March and April, online conference calls were held with the Executive Committee, and it was decided to start working on holding online events. The Chapter virtually hosted its first SSCS Distinguished Lecturer, Prof. Dejan Markovic,

Digital Object Identifier 10.1109/MSSC.2020.3001886
Date of current version: 25 August 2020



Members of the Intel team receive the ISSCC 2019 Lewis Award.

University of California, Los Angeles, with his talk “Next-Generation Chip and System Solutions.” The presentation covered technologies that enable the extension of Moore’s law using heterogeneous integration and flexible compute architectures and circuit designs. The event was well attended and led to a fruitful Q&A session. The SCS Oregon Chapter will continue to hold virtual events throughout 2020.

—Dr. Farhana Sheikh,
IEEE SCS Oregon Chapter chair

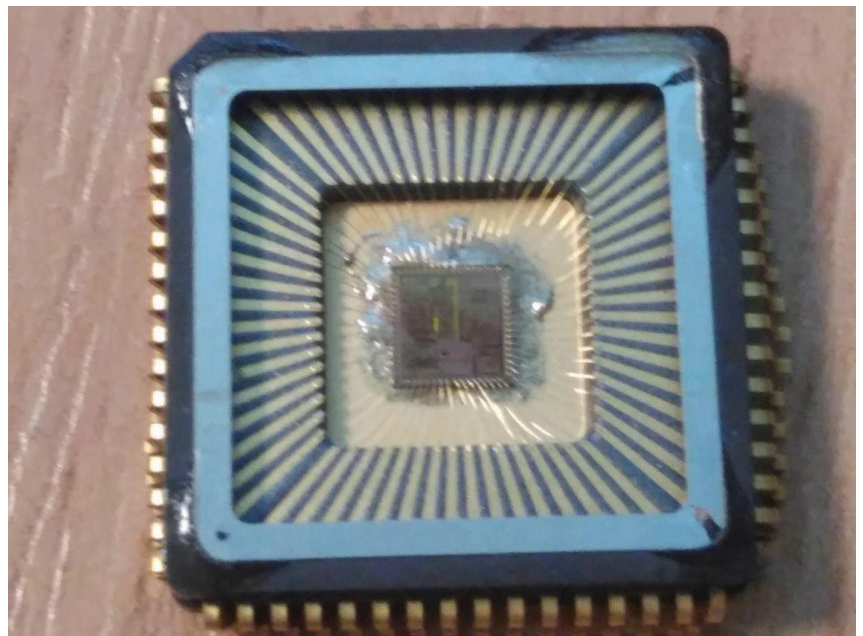
IEEE SCS Romania Chapter Promotes Engineering Through Technical Talk

On 8 January 2020, the IEEE Solid-State Circuits Society (SSCS) Romania Chapter coorganized a technical tutorial at Roman Vodă National College (CNRV), Romania, focusing on promoting technology among youth and strengthening the partnership between the two organizations. The lecture, “Hardware Design Challenges for Future Implementations,” was delivered by Cristian Andriesei, IEEE Senior Member and lecturer at “Gheorghe” Asachi Technical University of Iasi (TUIASI), Romania. The event chair was Constantin Ostafe, a CNRV professor of physics and an educator who shows genuine openness to new technology trends, practical implementations, and interdisciplinary approaches (such as robotics).

The event took place in Roman, a city with ancient historical traces dating back to 1392 [1]. The tuto-

rial covered multiple perspectives, offering insights into topics such as hardware technology (including the CMOS process), IEEE Society

and benefits, the SCS mission, In-ventics event services, embedded applications, field-programmable gate arrays, digital design, hardware



A microchip designed and manufactured by TUIASI in cooperation with two other Romanian universities and sponsored by Infineon Technologies Romania.

Digital Object Identifier 10.1109/MSSC.2020.3001887
Date of current version: 25 August 2020