

IEEE SSCS Italy Chapter Holds Courses on Microelectronics

The 12th IEEE Topics on Microelectronics (ToM) courses were held at the University of Milano-Bicocca, Italy, emphasizing the increased microelectronics activity at this institution. The program was organized in collaboration with the Ph.D. school at the University of Milano-Bicocca and the IEEE Solid-State Circuits Society Italy Chapter. The course was divided into two

sessions: 9–11 May and 18–20 September. As in previous years, ToM was broken down into lectures delivered by academic or industrial speakers.

The program for the 2018 ToM event included lectures by Dieter Draxelmayr (Infineon, Austria); Danilo Pau, Valeria Tomaselli, and G. Nicollini (STMicroelectronics, Italy); Cristiano Calligaro (RedCat Devices, Italy); Antonio Strollo (University of Napoli, Italy); C. Bruschini (École polytechnique fédérale de Lausanne, Switzerland); E.

Sacchi (Huawei, Italy); and Paolo Pavan (University Modena Reggio, Italy).

Topics included analog-to-digital converters, deep-learning systems, rad-hard digital ICs, power-efficient digital ICs, time-of-flight positron emission tomography implementations, optical radio frequency ICs, bandgap reference voltage, and device modeling.

More than 30 people, including Ph.D. students and industry professionals, attended both ToM courses.

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Dieter Draxelmayr gives a lecture to attendees at the 12th IEEE ToM.



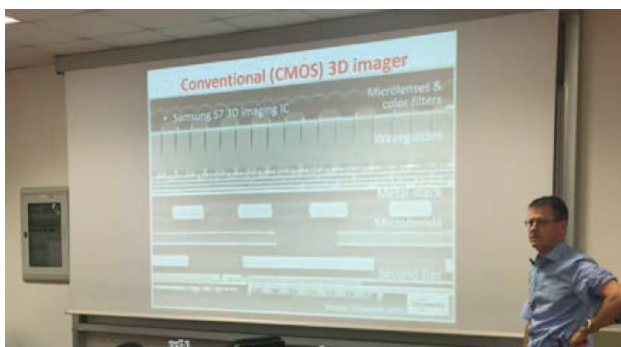
Danilo Pau (left) and Valeria Tomaselli present a talk at ToM.



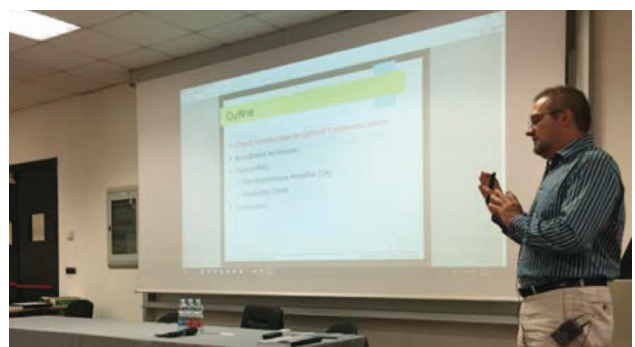
Cristiano Calligaro delivers a lecture at ToM.



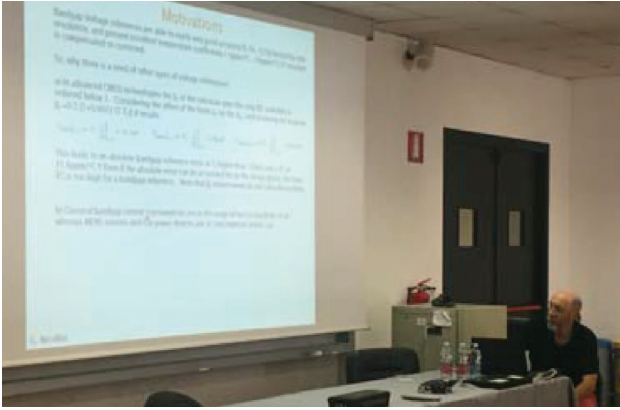
Antonio Strollo presents a talk at ToM.



Claudio Bruschini gives a lecture to attendees at ToM.



Enrico Sacchi delivering a talk at ToM.



Germano Nicollini gives a lecture to attendees at ToM.



Paolo Pavan presents a talk at ToM.

—Andrea Baschirotto

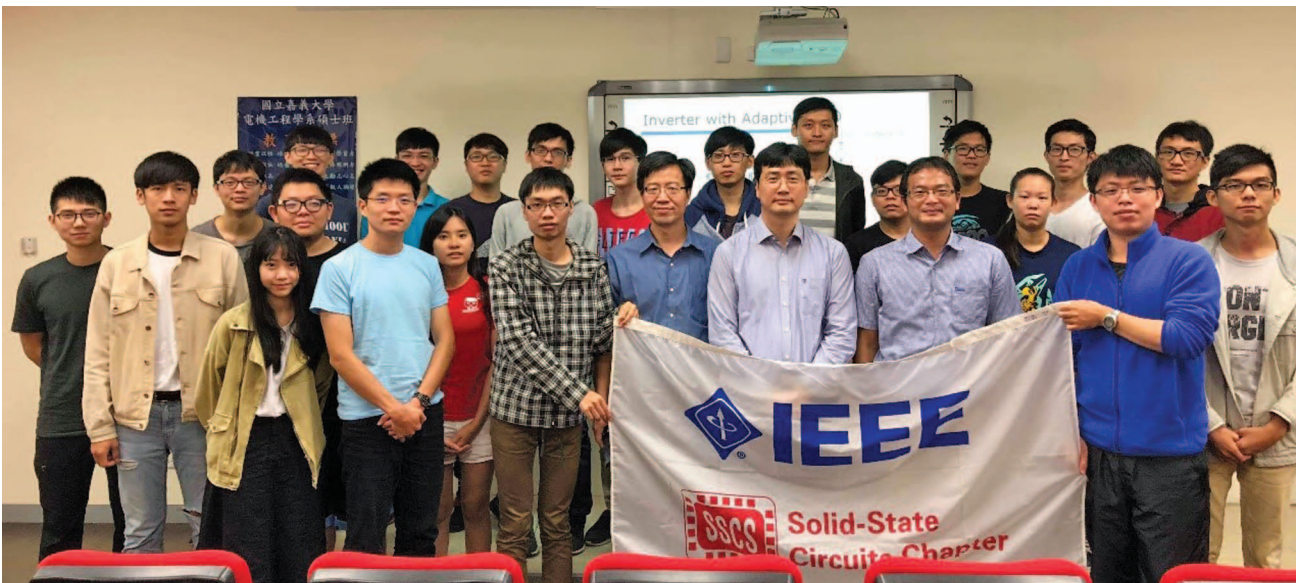
IEEE SSCS Tainan Chapter Hosts DL Youngcheol Chae

The Tainan Chapter of the IEEE Solid-State Circuits Society welcomed Distinguished Lecturer (DL) Youngcheol Chae, associate professor of electrical and electronic engineering at Yonsei

University, Seoul, South Korea. The talk was held on 8 November 2018 at the National Chia Yi University, Taiwan. Prof. Chae's talk, "Energy-Efficient Inverter-Based Amplifiers," attracted a large number of participants who had many questions and engaged in lively discussions.

The continuous feature-size scaling in CMOS technology has enabled the system to decrease power consumption and lower costs while increasing reliability. The design of low-voltage operational amplifier often poses significant challenges in the scaled CMOS technology. Dynamic amplifiers based

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Dr. Chae (center) and attendees of the SSCS Tainan Chapter seminar.