

Comments and Corrections

Correction to “Wafer-Level Vacuum-Packaged High-Performance AlN-on-SOI Piezoelectric Resonator for Sub-100-MHz Oscillator Applications”

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- 1) Left column, Page 3576: “... is **2.02 ps (rms) from 12 to 10 MHz.**” in the current version should be changed to “... is **2.02 ps (rms) from 12 kHz to 10 MHz.**”
- 2) Left column, Page 3579: “...with a **Keysight impedance analyzer E4490A.**” in the current version should be changed to “...with a **Keysight impedance analyzer E4990A.**”
- 3) Right column, Page 3581: “...with **offsets of 1, 10, 100, and 1 MHz, respectively.**” in the current version should be changed to “...with **offsets of 1 kHz, 10 kHz, 100 kHz, and 1 MHz, respectively.**”
- 4) Right column, Page 3581: “... is **2.02-ps from 12 to 10 MHz.**” in the current version should be changed to “... is **2.02-ps from 12 kHz to 10 MHz.**”

REFERENCE

- [1] G. Wu *et al.*, “Wafer-level vacuum-packaged high-performance aln-on-soi piezoelectric resonator for Sub-100-MHz oscillator applications,” *IEEE Trans. Ind. Electron.*, vol. 65, no. 4, pp. 3576–3584, Apr. 2018.