

Modularized Electrosurgical System With a Hybrid CPU-FPGA Chip for Real-Time Thermal Lesion Approximation

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Abstract—Electrosurgery that ablates the target tissues such as tumor and nerve cells using radio-frequency (RF) heating has been widely employed in the medical industry. Although the thermal lesion plays a key role in the efficacy and safety for this method, it is still difficult to identify the depth and size of the lesion during the treatment using recent electrosurgical systems. Herein, we propose a novel electrosurgical instrument for real-time approximation of thermal lesions during RF ablation (RFA). Thermal lesions were numerically calculated based on theoretical thermal models using a hybrid central processing unit (CPU)-field-programmable gate array (FPGA) chip. Other functions such as RF control, voltage, and temperature measurements were implemented using RF components in a modular manner. It can solve voltage distribution in 6 ms by repeating the calculation 5000 times and can anticipate the thermal lesion in 15.6 ms within a time step in real-time simulations. As a real-world validation, the feasibility of the system was demonstrated through an animal study using a swine model. The system is modularly designed using off-the-shelf chips and RF components to improve flexibility and scalability. It can be easily compatible with existing RF surgical applications and medical imaging devices and can improve the efficacy of RFA therapy.

Index Terms—Field-programmable gate array (FPGA), finite-difference time domain (FDTD), real-time numerical simulation, radio-frequency ablation (RFA), renal denervation.

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I. INTRODUCTION

IN THE medical industry, electrosurgery, which generates heat from tissue using radio-frequency (RF) energy, called RF ablation (RFA) surgery, has considerably improved the operational efficiency and postoperative recovery [1]. RFA has been widely employed in minimally invasive surgery using robotic, laparoscopic, and endoscopic system [2], [3]. It can effectively ablate the target tumor or nerve cells in the brain [4], liver [5], spinal cord [6], and vascular tissues [7]. An important requirement for effective RFA procedures is the capability of localizing heat on the target tissues to minimize irreversible damage to adjacent normal tissues, which is a critical side effect of any thermal treatment. Therefore, surgeons should pre-plan the details of RFA applications to avoid thermal injury and to improve the effectiveness of treatment.

Using theoretical models about RFA [8], offline simulation tools (e.g., ANSYS, COMSOL, and Sim4life) can analyze the electrical and thermal effects of RFA applications using CT or MRI results of patients [9], [10]. Such tools, which perform predictive analysis of RFA results under various surgical environments considering the structure of a patient's organs, tissue conditions, blood flow, or perfusions, have proved to be useful for improving the clinical outcomes of the procedure. However, during surgery, anatomical variations that could not be identified with conventional imaging devices and unexpected physiological variations such as blood flow and conductivity make the accurate prediction of the thermal damage caused by the electrosurgical system difficult before the treatment. Therefore, the thermal lesion during RFA must be approximated while considering the various surgical environments more realistically.

Recent electrosurgical instrument controlled by a microcontroller unit (MCU) can monitor the electrode temperature and transmitted RF energy during treatment. Some papers have attempted to use bio-impedance spectroscopy to provide information regarding thermal lesions during RFA without imaging devices [11]–[13]. However, this method requires additional RF electrodes and is only applicable to limited organs composed of homogeneous tissue such as the liver. In addition, performing real-time simulations is difficult for MCU-based systems due to their limited processing speed and memory. This feature is possible if the theoretical thermal models are solved numerically within a time step in real-time simulation. Therefore, approximating thermal lesion during

RFA requires in-depth knowledge of the theoretical models of RFA, numerical analysis, high-performance computing, and RF systems. In this context, these technical and clinical unmet needs largely motivated the development of an electrosurgical instrument integrated with real-time approximation of thermal lesions using numerical simulation techniques on a hybrid central processing unit (CPU)-field-programmable gate array (FPGA) chip. FPGA has gained a preferred position in real-time simulation for various industrial applications [14]–[16]. FPGA-based systems can compute complex problems using hardwired computational engines in a parallel manner. In addition, the development of a hybrid CPU-FPGA chip significantly increases the flexibility and lowers development costs. In contrast, by programming the entire system with FPGA, the developer can allocate sequential tasks into the CPU as high-level design and combinational tasks into FPGA as low-level design.

In this study, we developed a novel electrosurgical instrument that can conduct RFA, numerically calculate the voltage distribution during operation, and approximate thermal lesions considering the heat intensity, duration, and tissue type. With these capabilities, the proposed system can adjust the ablation scheme even in real-time, and the effectiveness of the procedure can be optimized further from the pre-planning procedure. The main contributions of this study are as follows.

- 1) We propose a real-time numerical computing procedure on a hybrid CPU-FPGA chip to numerically solve the bio-heat equation by combining the electrical and thermal problem, the results of which are the heat distribution in the tissue. The electrical problem was solved using a dedicated accelerator composed of five-stage pipelined computing units in FPGA. If the electrical and thermal boundary conditions are determined, the heat distribution on a hybrid CPU-FPGA chip can be calculated.
- 2) The functions of the electrosurgical system such as RF generation, the measurement of electrode temperature and voltage, and the approximation of thermal lesions were modularly implemented using commercially available chips and RF components. The modular design method can improve flexibility and scalability. Each module in the system can be individually compatible with existing electrosurgical systems or medical imaging devices.
- 3) We developed a novel electrosurgical system consisting of the proposed chip and RF components. The system delivers RF energy to a surgical electrode and monitors the electrical and thermal boundary conditions, such as the electrode temperature, the transmitted RF energy, and the load impedance. Concurrently, the system approximates the thermal lesion considering surgical situations and displays the results on a monitor during surgery. To the best of our knowledge, this is the first study in which an electrosurgical system capable of real-time approximation of thermal lesions on a single chip has been developed.

II. THEORETICAL MODELS ABOUT RFA

A. Offline Numerical Simulation

To predict the heat distribution during electrosurgery via offline numerical simulation, the first step is to model the physical phenomena in the actual surgical situation. In this step, a simulation domain is defined, and the detailed structure including the electrodes and adjacent tissues is modeled. After designing the models, the mathematical equations governing RFA are arranged. For example, the simulation of RFA consists of two physics (electrical and thermal) fields. The governing equation for computing the electrical field distribution is the Laplace equation which can solve the induced electrical potential (V) distribution

$$\nabla \cdot \sigma \nabla V = 0 \quad (1)$$

where σ is the electrical conductivity

$$Q_J = J \cdot E = \sigma (\nabla V)^2. \quad (2)$$

The electrical potential generates Joule heat (Q_J) in tissue following Ohm's law and is coupled with the thermal field

$$\rho C \frac{\partial T}{\partial t} - \nabla \cdot [k \nabla T] = Q_J - Q_{\text{cooling}}. \quad (3)$$

The governing equation for computing the thermodynamics is the heat conduction in tissue that combines the tissue temperature (T) changes in the time domain (t) with density (ρ) and heat capacity (C), the spatial temperature distribution with tissue thermal conductivity (k), the Joule heat generation term, and the cooling effect term. The cooling effect terms depend on the surgical environment such as air flow, bulky blood flow, or blood perfusion. This multiphysics equation is the so-called bio-heat equation [8], [17]. After setting the electrical and thermal parameters of the designed models, a numerical method such as FDTD can solve the governing equations.

B. Real-Time Thermal Lesion Approximation

To perform online numerical analysis during RFA, a simulation tool should numerically solve the coupled electrical-thermal problems in a few milliseconds before the tissue temperature is changed. As a real-world validation to increase the credibility of this study, we considered our previously developed emerging laparoscopic renal denervation (LRDN) technique, in which a renal artery is wrapped and renal nerves are ablated by generating heat using the RF energy [see Fig. 1(a)] [7]. In LRDN, thermal lesions should be concentrated on the outer arterial wall to damage nerves while protecting the artery from thermal damage. Therefore, monitoring subsurface thermal injury of a renal artery is important for effectiveness and safety of the LRDN. The design methodology of the online simulation applicable to the RF system is divided into five steps.

1) *Simplification of the Simulation Model*: Reducing computation tasks by simplifying the simulation domain or model should take precedence to solve the numerical problems on a chip that uses limited computational resources. To simplify the model, we analyzed the heat distribution using offline

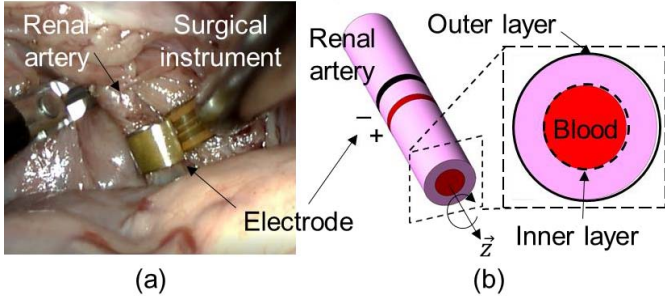


Fig. 1. (a) Actual surgical situation during laparoscopic renal denervation and (b) its simulation model.

numerical simulations and observed the actual surgical situation [7], [9], [18], [19]. In those previous studies, LRDN at the main renal artery without stenosis was analyzed. To simulate the LRDN, the artery was modeled as a cylinder, and the bipolar electrode was modeled as two circular plates (see Fig. 1).

A blood cooling effect occurs on the inner arterial wall; however, the blood perfusion cooling effect is negligible because the renal artery is detached from adjacent fat tissues and capillaries during treatment [20]. Thus, the LRDN model has three unique features: 1) a renal artery (target tissue) and electrodes wrapping the artery can be considered as concentric circles; 2) the simulation model with macro-fragments of the artery and blood is set to be homogeneous; and 3) the surgical electrode is a parallel bipolar electrode which completely wraps a renal artery. From features 1 and 2, the 3-D simulation domain can be simplified into a 2-D cylindrical coordinate system (radial distance r and height z). Feature 3 indicates that the electric field on the outer arterial wall (\vec{E}_w) between two parallel electrodes is constant.

We used FDTD to solve the coupled electrical–thermal equation in a 2-D cylindrical coordinate [21]. In this rectangular domain, FDTD has an advantage in terms of mesh generation to solve the linear electrical–thermal equation [22]. Therefore, (1) can be discretized as follows:

$$V_{r,z} = \frac{\Delta r^2 \Delta z^2}{2(\Delta r^2 + \Delta z^2)} \times \left(\frac{1}{r} \frac{V_{r+1,z} - V_{r-1,z}}{2\Delta r} + \frac{V_{r+1,z} + V_{r-1,z}}{\Delta r^2} + \frac{V_{r,z+1} + V_{r,z-1}}{\Delta z^2} \right). \quad (4)$$

In addition, (3) is approximated by employing the second-order central difference scheme for the spatial derivative and the explicit Euler for time advancement

$$T_{r,z}^{f+1} = T_{r,z}^f + \frac{k\Delta t}{\rho C} \times \left(\frac{T_{r+1,z}^f - T_{r-1,z}^f}{2r\Delta r} + \frac{T_{r+1,z}^f - 2T_{r,z}^f + T_{r-1,z}^f}{\Delta r^2} + \frac{T_{r,z+1}^f - 2T_{r,z}^f + T_{r,z-1}^f}{\Delta z^2} \right)$$

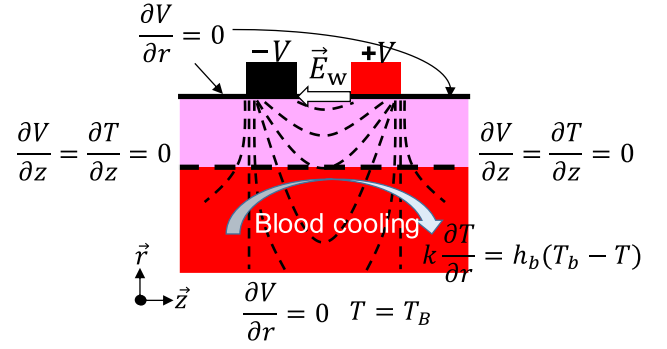


Fig. 2. Cross-sectional view of the modeled bipolar electro-surgery on an artery in a 2-D cylindrical coordinate system (dashed line is an isoline of $|\vec{E}|$).

$$+ \frac{k\Delta t}{\rho C} \times \left(\left(\frac{V_{r+1,z} - V_{r-1,z}}{2\Delta r} \right)^2 + \left(\frac{V_{r,z+1} - V_{r,z-1}}{2\Delta z} \right)^2 \right). \quad (5)$$

2) *Determination of the Boundary Conditions:* Once the simulation model is simplified and the bio-heat equation is discretized, the electrical and thermal boundary conditions must be determined (see Fig. 2). In the electrical field, an electrode–artery interface is set to have a positive or negative electric potential on two parallel electrodes (the Dirichlet boundary condition). Because the electric field between the two electrodes is constant, the potential linearly increases from the cathode to the anode. The other sides of the rectangular domain have a null flux (the Neumann boundary condition). Therefore, to set the electrical boundary condition during RFA, the RF electro-surgical system should measure the voltage on the electrode.

In the thermal field, the blood cooling effect was modeled by a linear cooling law on the artery–blood interface (inner arterial wall) [23]

$$k \frac{\partial T}{\partial r} = h_b(T_b - T), \quad \text{at inner arterial wall} \quad (6)$$

where the temperature of blood (T_b) was 310 K and the convective heat-transfer coefficient of blood (h_b) was 300 W/(m²K). The thermal conductivity of the artery was 0.45 W/(mK) [24]. FDTD solves (4) and (5) numerically in a 2 (r -axis) \times 3.85 (z -axis) mm² computational domain ($\Delta r = 0.167$ mm, $\Delta z = 0.035$ mm). In this study, we simulated the cooling effect using a linear cooling law on the artery–blood interface rather than modeling a bulked blood flow. Both of these methods have been used in simulation studies of RFA [8]. We thoroughly examined the effect of these boundary conditions in a previous study [7]. The cooling effect due to the bulked blood flow skewed the heat distribution along the flow direction, whereas the cooling effect due to the linear cooling law did not. However, these two methods identically generated the maximum temperature at the middle of the parallel electrode, which is the main cause of thermal lesion. In addition, in terms of hardware resource on a single chip, such as memory, adaptive logic modules, and DSP, it is better to use the linear cooling law than to use bulky blood

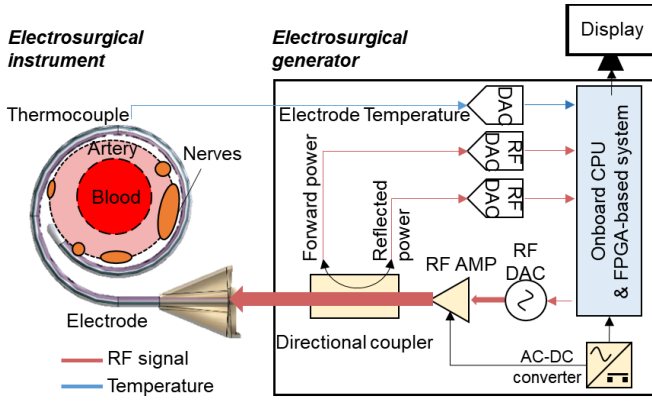


Fig. 4. Schematic representation of the electrosurgical system applicable online numerical simulation.

chip controls the system and numerically calculates the heat distribution inside an artery. All results including the heat distribution, transmitted power, and operating duration are displayed on a monitor. We used a board (DE1-SoC, Intel) comprising a monolithic integrated circuit (Cyclone V) that combines a hard processor system (HPS) and FPGA. The HPS comprises an ARM Cortex A9 dual-core processor as a CPU, on-chip memories (DDR3), and diverse peripheral devices.

B. FPGA-Based Accelerator to Solve the Laplace Equation

The electrical–thermal governing equation consists of the Laplace equation and heat equation. At each time step, numerical analysis of the Laplace equation requires thousands of iterations, whereas the heat equation only requires one computation. The iterative techniques are significantly effective when FPGA that can implement parallel processing element (PE) is used. Therefore, we solve the Laplace equation using FPGA to reduce the computing time and the heat equation on CPU.

In this computing system, we employed a 27-bit floating-point format by modifying the IEEE Standard 754. The 27-bit format consisted of a single bit sign, 8-bit exponent, and 18-bit fraction. The multiplication or addition of 18-bit fraction utilizes one DSP unit of the target FPGA (Cyclone V). In addition, because this is equivalent to a floating number in C programming by adding five zeros to the fraction of the least significant bit, FPGA can communicate with the CPU without any additional hardware design. We designed the add operation of the 27-bit format as a combinational circuit and the multiple operation as a two-stage sequential circuit.

Equation (4) was reshaped to optimize the 27-bit floating operation and to be processed by the pipelining technique

$$V_{r,z} = \frac{C_1}{r} \times (V_{r+1,z} - V_{r-1,z}) + C_2 \times (V_{r+1,z} + V_{r-1,z}) + C_3 \times (V_{r,z+1} + V_{r,z-1}) \quad (12)$$

where $C_1 = \Delta r^2 \Delta z^2 / 4 \Delta r (\Delta r^2 + \Delta z^2)$, $C_2 = \Delta r^2 \Delta z^2 / 2 \Delta r^2 (\Delta r^2 + \Delta z^2)$, and $C_3 = \Delta r^2 \Delta z^2 / 2 \Delta z^2 (\Delta r^2 + \Delta z^2)$. Consequently, we designed a five-stage pipeline computing unit to solve the Laplace equation (see Fig. 5).

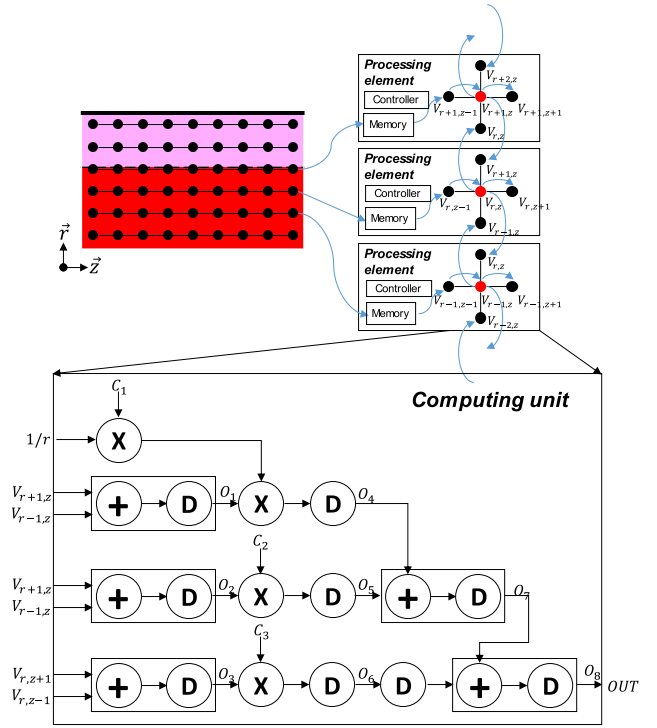


Fig. 5. Computing unit and dataflow of the parallel processing algorithm.

We generated 11 PEs in FPGA that iteratively processes a line of grids along the Z-axis (110 grids) in the 2-D computational domain.

C. Heterogeneous System on a Hybrid CPU-FPGA Chip

The hardware modules on FPGA were subdivided into five modules implemented by Verilog, and one module designed using a schematic method with Intel IPs. The programmed modules of Verilog included an ADC/DAC controller, a peak-to-peak voltage calculator, an accelerator for FDTD, temperature monitoring, and a feedback controller. In addition, the modules using Intel IPs support the RF electrosurgical system to display the computing results on a monitor. Fig. 6 shows the overall implementation of the FPGA part.

The ADC/DAC controller module coordinates the operation of the RF signal generation and the measurement of the electrode voltage. It sends out control signals to the RF ADC and DAC IC through GPIOs of the DE1-SoC board. In the domain of the 50-MHz clock which generates a 500 kHz sinusoidal signal, the module sends one command signal to the DAC repeatedly 100 times per clock. The command signal is 14-bit data pre-stored in ROM and controls the amplitude of the DAC output. Meanwhile, this module receives 14-bit data from RF ADC at the 65 MHz clock. The ADC data are transmitted to the on-chip RAM in the peak-to-peak voltage calculator module. The on-chip RAM has two ports: s1 and s2. The s1 port is connected to the ADC/DAC controller module at the 65 MHz clock, and the s2 port is connected to the AXI-bus master of the CPU at the 50 MHz clock. Starting from 100 μ s after generating the 500-kHz sinusoidal signal at the DAC, the RAM in the domain of the 65 MHz clock

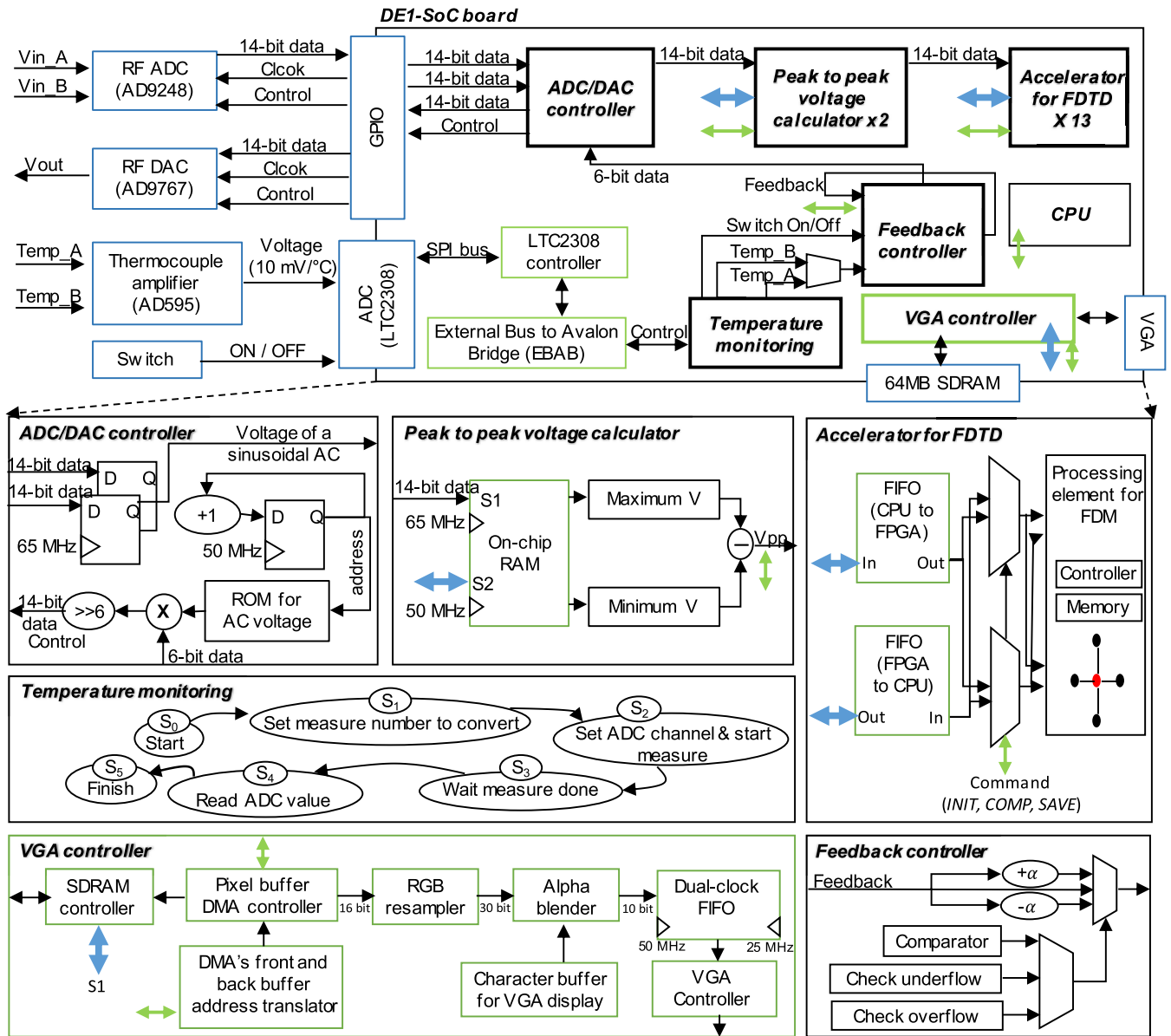


Fig. 6. Configuration of the hybrid CPU-FPGA chip. Black box is a textual programmed module using Verilog, green box is a module supported by Intel, and blue box is an integrated circuit. Blue arrow is an AXI bus and green arrow is a lightweight AXI bus.

stores 128 ADC data that can store over one period of the 500 kHz signal. Therefore, the peak-to-peak voltage calculator module can find the maximum and minimum values on the received ADC data and derive the peak-to-peak voltage of the 500 kHz signal. We used two modules of the peak-to-peak voltage calculator to find the voltage of the transmitted and reflected RF energy. Finally, the peak-to-peak voltages will be used as the boundary conditions of (4) at the accelerator for the FDTD module.

The accelerator for the FDTD module, which operates at 100 MHz, embedded two first-in first-out (FIFO) buffers and a PE for FDTD. Data communication between FPGA and CPU was performed by the FIFO buffers using the AXI bus. The PE consists of a controller, an on-chip memory (M10K), and the five-stage pipelined computing unit (see Fig. 5).

This module is controlled by three commands (INIT, COMP, SAVE) from CPU; INIT loads the initial voltage and boundary conditions on the on-chip memory, COMP operates the PEs, and SAVE sends out all data on the on-chip memories to the CPU.

The temperature data from AD595 can be read from the LTC2308 ADC built into the DE1-SoC board. We used an Intel IP to control the built-in ADC and connected the Avalon slave of the IP to the FPGA system with a bridge module, called the external bus to Avalon bridge (EBAB). In the FPGA system, the temperature monitoring module connects to the EBAB and can operate the LTC2303 ADC. The feedback controller module receives temperature data. This module normalizes the difference between the measured temperature and a preset target temperature to a 6-bit integer. The integer is used by

TABLE I
RESOURCE UTILIZATION OF A HYBRID CPU-FPGA CHIP

Module	ADAPTIVE LOGIC MODULE (ALM)	LUT	DSP	M10K
Accelerator for FDTD (x11)	About 2000	About 2900	4	1
Accelerator for FDTD at the boundary (x2)	About 40	About 75	0	1
Temperature monitoring	54	77	0	0
Peak to peak voltage calculator (x2)	About 117	157	0	0
ADC/DAC controller	87	119	1	0
Intel IPs	5390	9213	6	19
Total	28,283	42,924	51	41

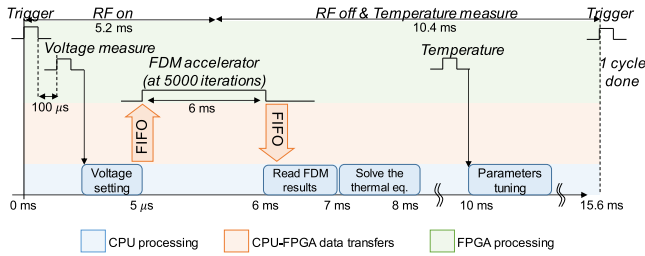


Fig. 7. Timing diagram of the hybrid CPU-FPGA chip.

the ADC/DAC controller module to modulate the amplitude of the 500 kHz sinusoidal signal.

IV. RESULTS AND VALIDATION

A. Operation of the RF Electrosurgical System

The hybrid CPU-FPGA chip targeted the Intel Cyclone V 5CSEMA5F31C6 FPGA. This chip integrates the FPGA fabric and dual-core ARM cortex-A9 (CPU) on a single die. Table I shows the hardware resource utilization of each module on the chip. The 13 accelerators for FDTD modules solve the Laplace equation over a 2 (r -axis) \times 3.85 (z -axis) mm² computational domain. Two module-processing grids at a radial distance of 0 (first) and 2 (last) are configured with fewer hardware resources than those used for other modules because the boundaries of the computational domain do not require a PE.

The controller of the overall system is implemented in CPU using the C application (see Fig. 7). The first step is to send a trigger signal from the CPU to the FPGA. The signal controls the ADC/DAC controller module to generate a 500 kHz RF signal and measures the voltage on the RF electrode. When the voltage measurement is complete, FPGA sends a signal to the CPU. Then, the CPU initiates the electrical boundary conditions using the measured voltage by transmitting the INIT command to the accelerator for the FDTD module and starts to calculate (4) using the FDTD accelerator by transmitting the COMP command. After the trigger signal is generated, ~ 7 ms are required for the accelerator to complete the operation and the CPU to receive the result. In the next

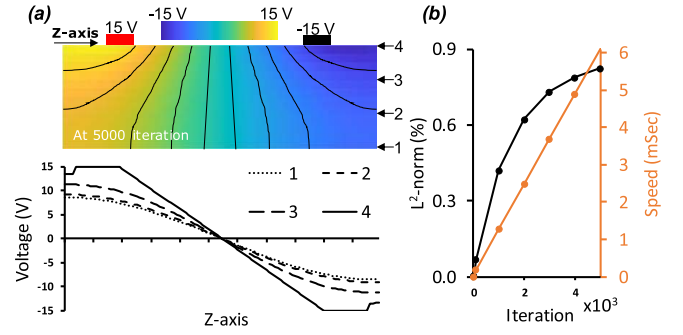


Fig. 8. (a) Calculating the Laplace equation using the FDTD accelerator after 5000 iterations. (b) L^2 -norm of error and solution time at different iterations.

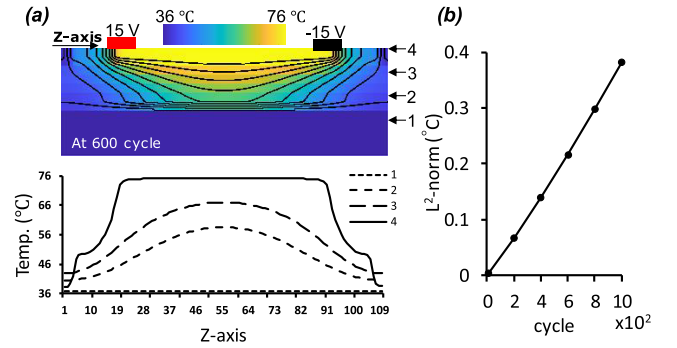


Fig. 9. (a) Calculating the bio-heat equation on the hybrid CPU-FPGA chip at 600 cycle. (b) L^2 -norm of error at different cycles.

step, the CPU computes (5) using the FDTD accelerator results at a time interval (Δt) of 5.2 ms (RF ON) and 0 V at a time interval of 10.4 ms (RF OFF and temperature measurement). Finally, the bio-heat equation can be solved within 10 ms after the trigger start. Before the next trigger signal starts, the CPU compares the computational result against the measured temperature and adjusts the physical characteristics of tissues to minimize the error. The time interval between two triggers is 15.6 ms, which is one cycle.

We evaluated the computing performance of the bio-heat equation by setting the electrical boundary condition as 15 V (Figs. 8 and 9). The bio-heat equation and boundary conditions were also implemented in MATLAB using an Intel Core i7-6700. The FDTD accelerator numerically solved the Laplace equation by repeating the calculation 5000 times [see Fig. 8(a)]. At 5000 iterations, the L^2 -norm of the error between the FDTD accelerator (27-bit floating point) and MATLAB (64-bit floating point) numerical solution was ~ 0.05 V (0.82%). In addition, it takes about 6 ms when operating the accelerator at 100 MHz clock [see Fig. 8(b)] that is 2000 times that required by MATLAB (12.8 s). After solving the Laplace equation, the hybrid CPU-FPGA chip calculates the remaining thermal problems in the CPU. When computing is repeated up to 1000 cycles (15.6 s), the L^2 -norm of the error between the results of the chip and results of MATLAB becomes ~ 0.4 °C (0.11%) [see Fig. 9(b)].

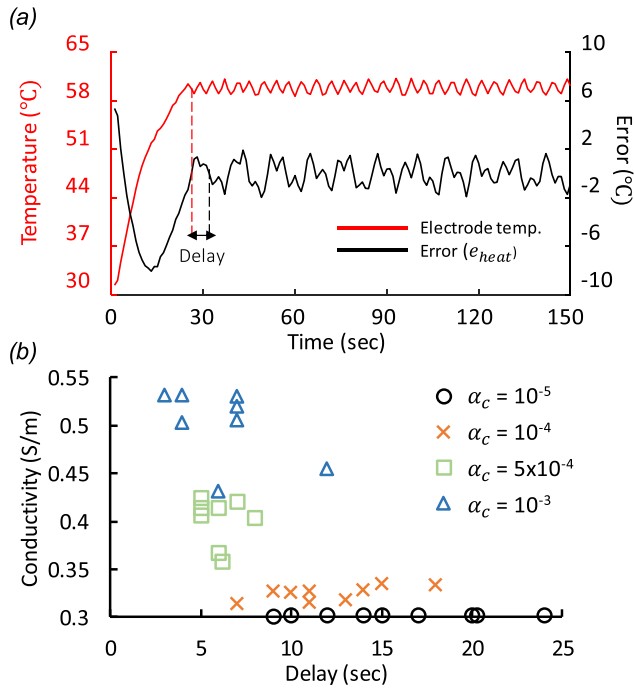


Fig. 10. (a) Profile of electrode temperature and error (e_{heat}). (b) Modified electrical conductivity on various tuning constants (α_c).

B. Phantom Experiment

We tested the RF electrosurgical system on a phantom gel fabricated by mixing 200 mL of water with 10 g of gelling agent (No. 436364, Sigma-Aldrich) and 3.3 g of sodium chloride (No. S7653, Sigma-Aldrich). The electrical conductivity could be controlled by the amount of sodium chloride, but the mass density and thermal properties were the same as those of water [18], [28]. Therefore, this phantom gel had an electrical conductivity of 0.33 S/m (at room temperature, 25 °C), which is similar to that of the human artery. The electrical conductivity is a temperature-dependent value which can be assumed to increase by 2% for a 1 °C rise [8]. In the phantom experiment, the simulation model is considered as a macro-fragment of a gel.

The experiment started at 30 °C, and the temperature of the electrode was raised to 60 °C [see Fig. 10(a)]. The maximum used to maintain the laparoscopic electrode at a constant ablation temperature during the renal denervation is 60°C [19]. The maximum has been determined empirically from our previous *in-silico* studies and *in vivo* studies using pigs [7], [29]. We evaluated the relationship between the actual electrode temperature and the numerical result by changing the constants of α_q , q_{max} , and α_c . When α_q was 0.00001 and q_{max} was 0.05, the modified conductivity was converged. The constant α_c affected the magnitude of the conductivity [see Fig. 10(b)]. We finally determined α_c as 0.001 because the modified electrical conductivity should be 0.51 S/m at 60 °C. This shows that the conductivity modified in real-time based on temperature rise during RFA further reduces the error due to changes in tissue properties.

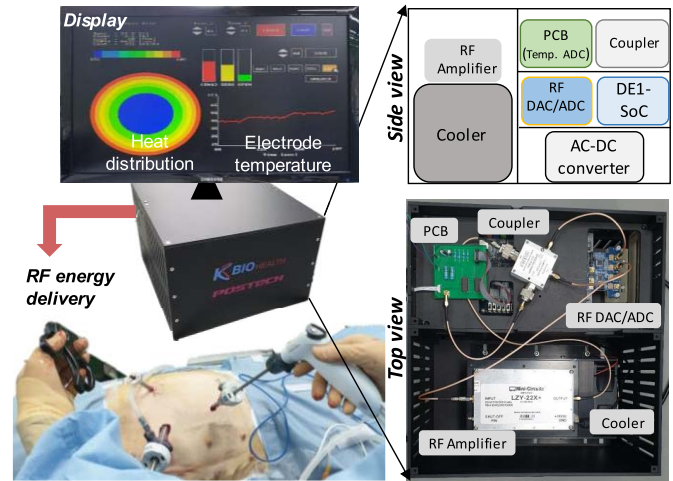


Fig. 11. Box containing the hybrid chip and RF components.

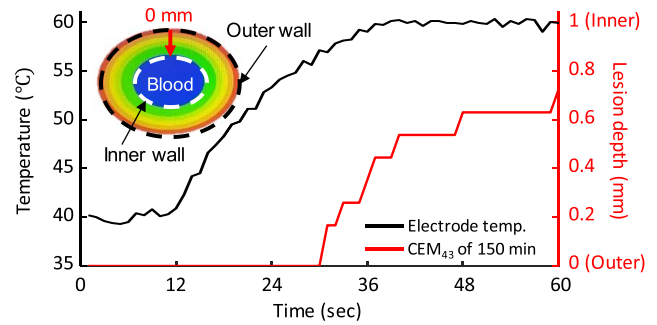


Fig. 12. Real-time thermal lesion approximation.

C. RFA on an Animal Study Using a Pig

We integrated the hybrid CPU-FPGA chip and RF components in a box (see Fig. 11). A laparoscopic electrosurgical instrument for LRDN and a monitor were connected to the box. The electrode of the laparoscopic instrument consisted of a Nitinol substrate and a flexible printed circuit board, which was proved to be safe from the previous survival animal study using pigs [7]. The feasibility of the system was demonstrated through an animal study using a swine model that mimics the human anatomy and physiology the best. We performed LRDN with this system on two main renal arteries of a pig (Movie 1). The animal experiment involved an open surgery and was approved by the Institutional Animal Care and Use Committee in the Osong Medical Innovation Foundation (KBIO-IACUC-2021-221). For the animal study, the system could keep the electrode temperature at a preset value (black line in Fig. 12). Concurrently, it approximated the thermal lesion by calculating the bio-heat equation (red line in Fig. 12). We defined the thermal lesion as the CEM_{43} of 150 min, which is the damage threshold of an artery. All results were displayed on a monitor in real-time. Details of the system operation are explained in Movie 2.

V. CONCLUSION

We developed a novel RF electrosurgical instrument with a hybrid CPU-FPGA chip that numerically computed the bio-heat equation by solving electrical problems in FPGA and thermal problems in CPU. The instrument conducted online numerical simulation during RFA treatment following five steps: 1) simplification of the simulation model; 2) determination of the boundary conditions; 3) task allocation to computational resources; 4) online tuning of physical characteristics of tissues; and 5) approximating the thermal lesion. In addition, the system modularly implemented the functions of RF control and electrode voltage and temperature measurement using RF components. The system could anticipate a thermal lesion in 15.6 ms within a time step of real-time simulation while performing RFA treatment. Such a framework can be used in various RFA treatments for online simulation of thermal lesions. Furthermore, the hybrid chip can be applied to conventional RF equipment as long as a few boundary conditions are met, such as electrode temperature or voltage. The modularly designed system using off-the-shelf chips and RF components is highly scalable for real-world applications. In addition, each module in the system is compatible with other imaging devices and existing electrosurgical systems, making it more useful as a mix-and-match system.

The current study mainly focused on implementing a system with online computation capability to numerically simulate LRDN with a simplified main renal artery model in real-time. However, it should be noted that further development of artery models considering more realistic conditions such as the bulked blood flow and the occluded artery is required before translating the system into clinical setting. In addition, future work will include system characterization with the developed system to evaluate the accuracy of the depth and size of the lesion produced by the thermal effect of RF. Characterizing the system accuracy on those biological parameters such as lesion depth and size require a large survival animal study since it takes weeks for target tissues to recover from acute thermal shock and stabilize. We also plan to perform various case studies, such as RFA treatment in the brain, tumor, or spine.

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