Information for Authors

The IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—II: EXPRESS BRIEFS (TCAS-II) is a monthly publication with a distribution of over 11 000 copies. Its primary goal is *rapid dissemination* of original, timely, and significant contributions to the research community. The field of interest covers ALL AREAS of interest to the IEEE Circuits and Systems Society and, thus, in particular, includes all areas covered by IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—I: REGULAR PAPERS (TCAS-I) as well.

Targeted publication time is *four months* from the end of the month in which the manuscript is received, provided that the author responds immediately to all communications. In order to accomplish this, the outcome of the prompt review process will be binary: an accept, or a reject decision. Authors are strongly encouraged to submit well prepared manuscripts that are complete and ready for publication. Only minor modifications to accepted manuscripts, that do not warrant another review, are allowed upon indication of the Associate Editor handling the paper.

The papers are expected to contain new results, ideas, or innovations that advance significantly the state-of-the-art circuits and systems. Submission of a contribution is taken to manifest the fact that the submission has not been submitted, accepted, published, or copyrighted elsewhere. However, since the intent is quick announcement of new results, more detailed versions of manuscripts submitted to or accepted by this TRANSACTIONS may also appear elsewhere *later*.

Specific topics to be covered include, but are not limited to the following: general theory of circuits and systems; neural networks, CNNs; nonlinear circuits and systems, chaos and bifurcations; power electronics and systems; numerical, graphical and symbolic computational methods; adaptive, blind and statistical signal processing and learning paradigms; multimedia signal processing; multidimensional signals and systems; wavelets and filter banks; (wireless) communication and sensors networks; analog, digital, mixed-mode and RF circuits; devices, circuits, architectures and system-on-chips for silicon as well as emerging technologies, e.g., MEMS, optoelectronics, quantum and nanoelectronics; biologically motivated circuits devices and associated computing paradigms.

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