

VERY ***HIGH SPEED*** MULTIPROCESSOR

ESL, located in the SAN FRANCISCO BAY AREA, with a staff of over 2400, has a track record for delivering significant digital signal processing systems in response to high priority government requirements.

For nearly two decades, ESL has been developing Signal and Image intelligence systems that regularly compute hundreds of millions of operations per second (MEGAOPS).

We are now ready to build intelligent systems that compute billion of floating point operations per second (GIGAFLOPS). This requires optimization of algorithms, architectures, custom VLSI and state-of-the-art software.

ESL's leadership continues to increase the demand for our design team. We need additional staff to develop a variety of multi-processors.

ESL has demonstrated:

- 30 MHZ real-time FFT processor in 1978
- 280 MOPS systolic processor in 1981
- Multi 68000 systems in 1982
- Knowledge-based military systems in 1983

ESL will soon demonstrate:

- Single board FFT processor that computes 4K complex FFT in less than 300 micro seconds
- Nearly .5 GIGAFLOP 32-bit floating point adaptive beam former
- Multi 68020 tightly & loosely coupled systems
- A framework for intelligent GIGAFLOP systems

Computer Scientists
Electrical Engineers
Signal Processing Specialists
Knowledge Engineers
System Engineers

Openings exist AT ALL LEVELS for PROJECT MANAGERS, ARCHITECTS, TECHNICAL LEADERS and DESIGNERS to make major contributions in every aspect of parallel processor development.

Compensation and benefits are outstanding. For immediate consideration, please call Bijoy G. Chatterjee, Deputy Director, Advanced Technology at (408) 738-2888, Ext. 6254, or send your resume to Professional Staffing, Dept. SFT-611, ESL, 1345 Crossman Avenue, P.O. Box 3510, Sunnyvale, CA 94088-3510. An equal opportunity employer.

U.S. CITIZENSHIP IS REQUIRED FOR ALL POSITIONS

ESL
A Subsidiary of TRW

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